

[54] **CONTENT ADDRESSABLE MEMORY WITH FLAG STORAGE TO INDICATE MEMORY STATE**

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[52] U.S. Cl. .... 365/49; 365/189.01; 364/200; 364/900

[58] Field of Search ..... 365/49, 189.01, 189.07; 364/200 MS File, 900 MS File

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[57] **ABSTRACT**

A content addressable memory device capable of correct retrieval operation comprises a flag bit column (12) provided in a memory cell array. The flag bit column (12) stores a flag signal indicating whether a word is in a data written state or an empty state for each word in a data array (2). In the retrieval operation, the data written in the data array (2) and a flag bit column (12) are simultaneously retrieved, providing a correct retrieval result. In addition, since the flag bit column (12) is provided in the memory cell array, it can be controlled in a manner similar to controlling the data array (2).

10 Claims, 13 Drawing Sheets

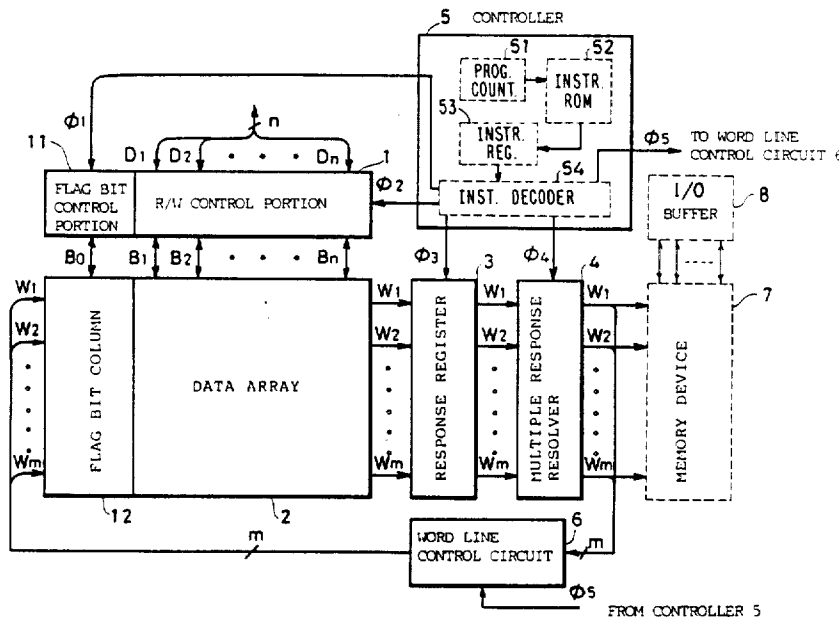
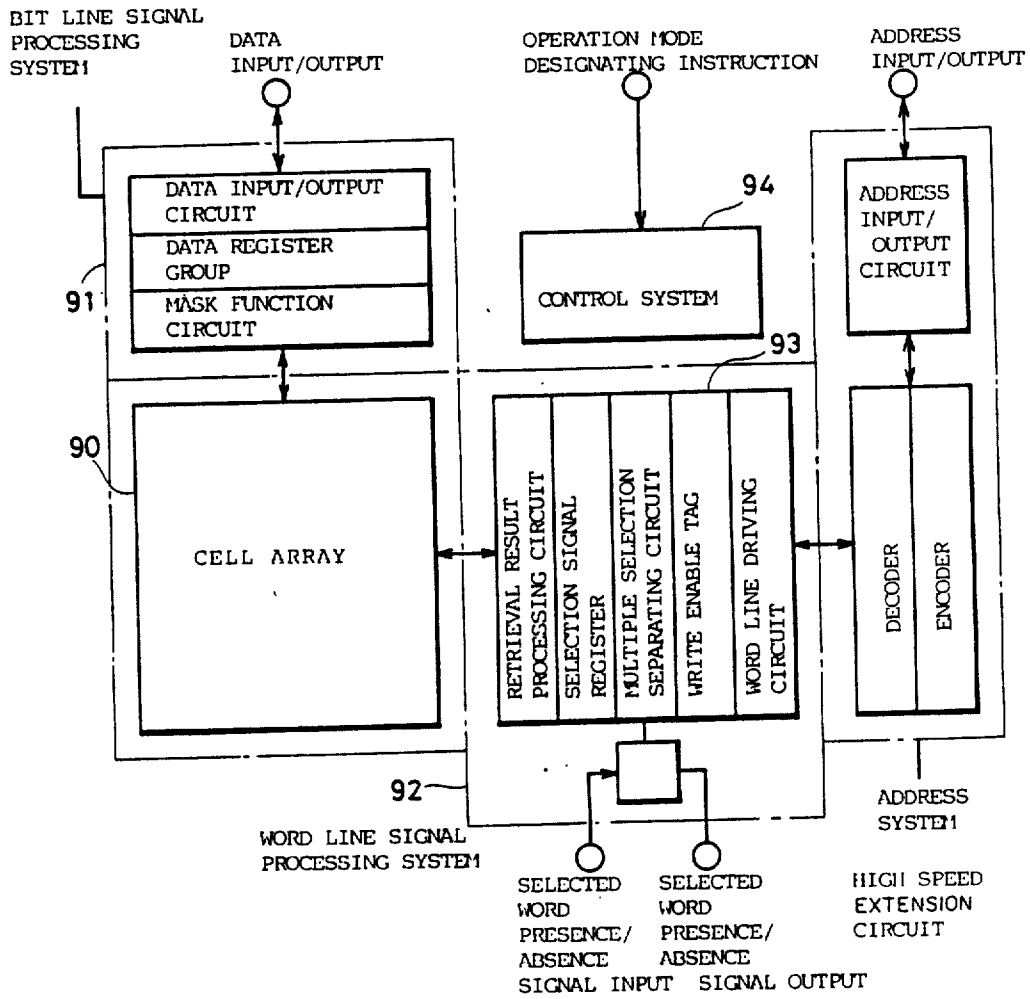


FIG.1 PRIOR ART



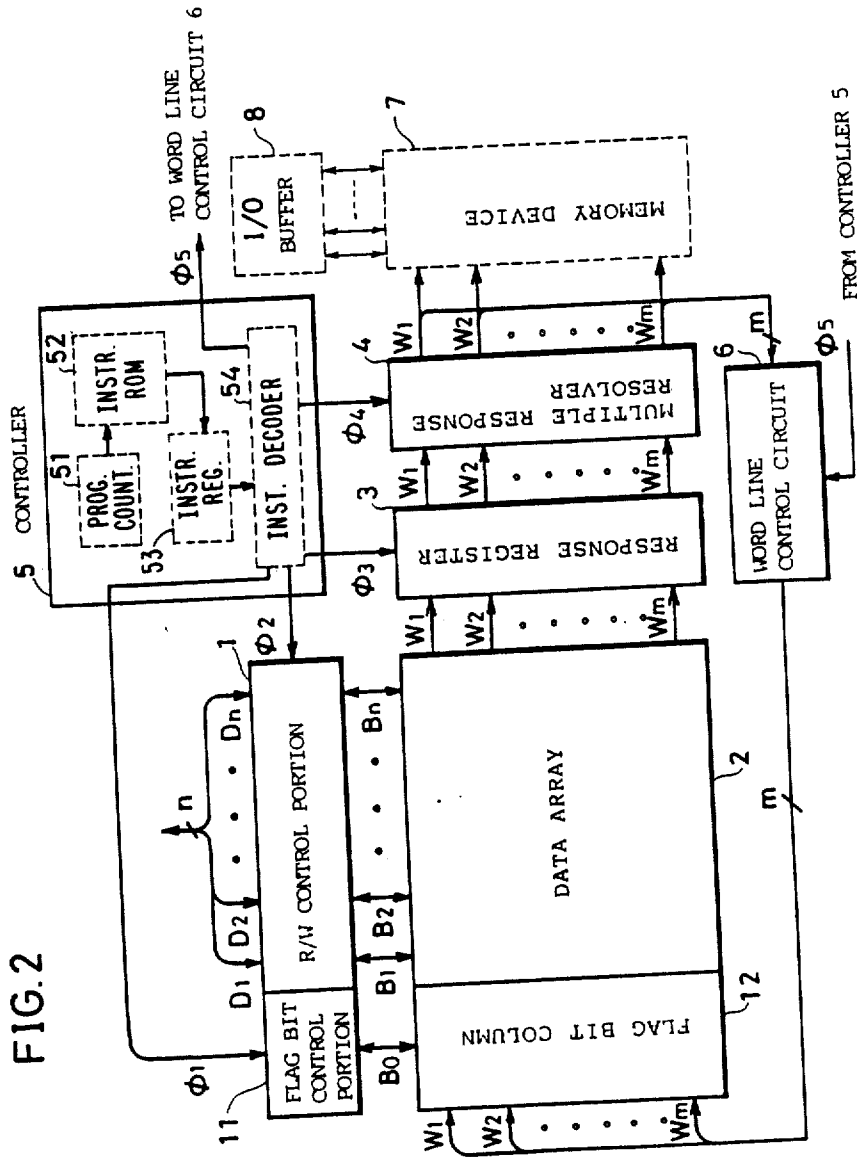


FIG. 2

FIG. 3A

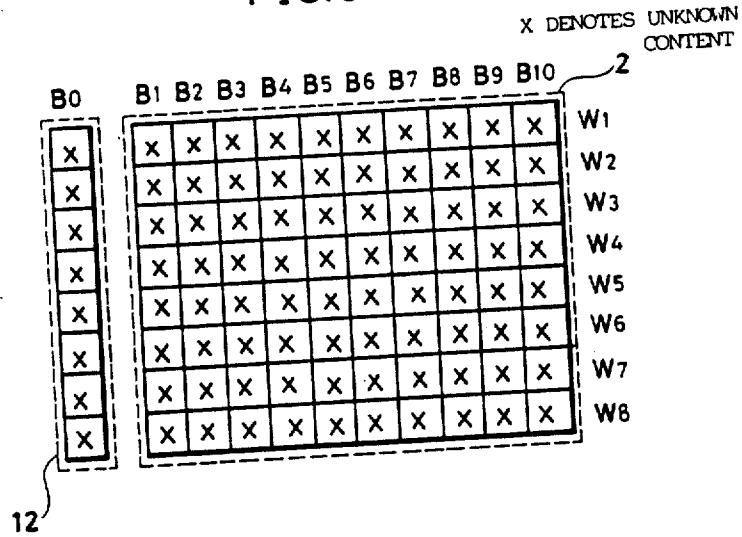


FIG. 3B

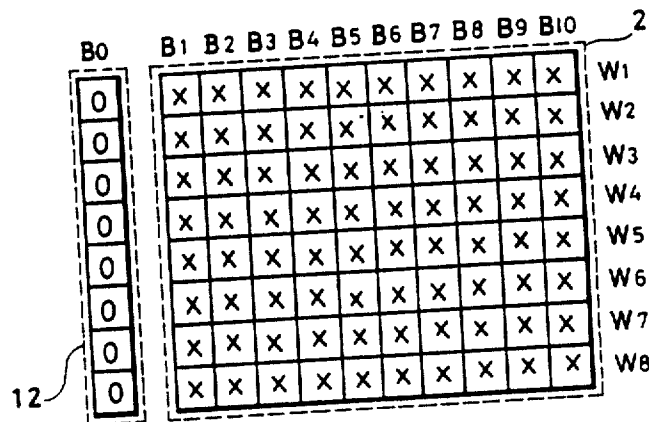


FIG.3C

B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	B10	
1	0	0	0	0	0	0	0	0	0	0	W1
1	1	1	1	1	1	1	1	1	1	1	W2
1	0	0	0	0	0	1	1	1	1	1	W3
0	x	x	x	x	x	x	x	x	x	x	W4
0	x	x	x	x	x	x	x	x	x	x	W5
0	x	x	x	x	x	x	x	x	x	x	W6
0	x	x	x	x	x	x	x	x	x	x	W7
0	x	x	x	x	x	x	x	x	x	x	W8

FIG.3D

B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	B10	
1	0	0	0	0	0	0	0	0	0	0	W1
1	1	1	1	1	1	1	1	1	1	1	W2
1	0	0	0	0	0	1	1	1	1	1	W3
1	1	1	1	1	0	0	0	0	0	0	W4
0	x	x	x	x	x	x	x	x	x	x	W5
1	0	1	0	1	0	1	0	1	0	1	W6
0	x	x	x	x	x	x	x	x	x	x	W7
1	1	0	1	0	1	0	1	0	1	0	W8

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