UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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APPLE INC.,

Petitioner,

v.

RFCYBER CORP.,

Patent Owner.

Patent No. 9,189,787
Filing Date: May 28, 2013
Issue Date: November 17, 2015

Inventors: Liang Seng Koh, Futong Cho, Hsin Pan, and Fuliang Cho
Title: METHOD AND APPARATUS FOR CONDUCTING
E-COMMENCE AND M-COMMENCE

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DECLARATION OF MIGUEL GOMEZ

Case No. IPR2022-00412

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I, Miguel Gomez, declare as follows:

1.   I have been asked by counsel for Patent Owner RFCyber Corp. ("RFCyber" or "Patent Owner") to review U.S. Patent No. 9,189,787 (the "'787 Patent") entitled PROCESSING WITH COMPACT ARITHMETIC PROCESSING ELEMENT, and to provide my technical review, analysis, insights, and opinions regarding the '787 Patent in view of the prior art cited by Petitioner Apple Inc. ("Apple" or "Petitioner"). I submit this declaration in support of Patent Owner's Response in this IPR proceeding. I have personal knowledge of the matters stated herein and would be competent to testify to them if required.

2.   I have been retained on behalf of RFCyber Corp. for the above-captioned *inter partes* review proceeding. I understand that the '787 Patent is currently assigned to RFCyber Corp.

3.   I am over 18 years of age. I have personal knowledge of the facts stated in this Declaration and could testify competently if asked to do so.

I.   INTRODUCTION

A.   Background and Qualifications

4.   I have reviewed and am familiar with the specification of the '787 Patent. I understand that the '787 Patent has been provided as Exhibit 1001. I will cite to the specification using the following format: '787 Patent at col.:line.

1.   Research and Professional Experience

5.   My CV is being submitted simultaneously herewith as Exhibit 2008.

6.   I received a Bachelor of Science in Electrical Engineering degree from Yale University in 1983. I have over forty years of experience developing hardware and software technology used in computer systems, communications systems, networking, storage infrastructure, and database systems. My experience includes extensive knowledge of computer operating systems, computer protocols, and programming languages used in both fixed and mobile applications. I am also highly skilled in the use of microelectronics simulation software, ASIC and FPGA development and the languages thereof such as Verilog and VHDL. I'm also skilled at programming in C , C++ , and Python and C#.

7.   From August 2006 to March 2009, I was VP of engineering for ActSolar. ActSolar developed solar power conversion systems that included power efficiency and cost analysis tools.

8.   In 2006, ActSolar was sold to National Semiconductor. The transfer of technology included the wireless interface for data collection, the inverter and a variant of the power converter hardware that performed shading compensation.

9.   From September 2004 to August 2005, I was a consultant for BridgeWave Inc. Bridgewave was a microwave connection company to wirelessly transmit Ethernet packets for long haul telephony networks. The equipment was

mostly intended for international markets where copper wire installations is prohibitively expensive.

10.   From September 2003 to March 2004, I was a consultant for PA Consulting Group. I provided hardware and software evaluation services for corporate mergers and acquisitions. These services included system reviews in the following areas: (1) Analysis of system cost to performance ratios; (2) Review of the hardware and software code implementations, documentation and development strategies; (3) compilation processes, simulation, test coverage, bug tracking and source code control; (4) Tool chain management analysis; (5) Circuit board design and layout design rules for production environments; (6) Circuit board certification testing for FCC, UL, and Environmental tests; (7) Review of production line management including assembly and test processes; and (8) Review of hardware code for copyright or license violations.

11.   From January 2003 to May 2003, I was a consultant for Santel Networks. Santle Networks was a high speed fiber optics supplier to the telecom industry. Here I developed an optical duo-binary (ODB) encoder that operated at 10.7 Giga bits per second (GBps), Several patents were applied for and the board was shown at the Optical Fiber Communication Conference & Exposition in March of 2003.

12.   From March 2001 to December 2002, I was Director of Hardware,

Content Networking Division for Extreme Networks, Inc. I managed a team of

hardware and software engineers that developed and maintained a Layer 2-5

Content Addressable Switch. The switch was capable of L2, L3, L4 switching as

well as L5 switching based on packet content. Security was provided through

Secure Sockets and DES encryption layers.

13.   From January 2000 to March 2001, I was Director of Hardware for

Webstacks, Inc. (now Extreme Networks, Inc.). At Webstacks I assembled a team

to build the Content Addressable Switch later sold to Extreme Networks. This

switch provided L2-5 load balancing services for routing via MAC, IP and HTTP

content based routing mechanisms. The system included firewall and security

capabilities utilizing Secure Sockets Layers (SSL). My responsibilities were to

design the initial system architecture as well as to hire and manage the hardware

and software implementation teams. Product development time was 16 months

after which we were acquired by Extreme Networks for $68MM cash and stock.

14.   From February 1997 to December 1999, as consultant for Philips

Semiconductor I developed the certification environment used by Microsoft to

validate Windows CE on the Philips' Poseidon handheld chipset.

15.   From September 1994 to January 1997, I was President and Founder

of Minden Group, Inc. The Minden Group developed and sold several types of

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