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Sinclair et al.

#### (54) HYBRID NON-VOLATILE MEMORY **SYSTEM**

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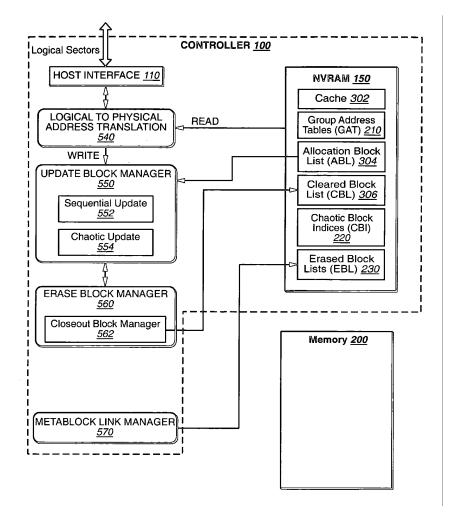
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## **ABSTRACT**

The present invention presents a hybrid non-volatile system that uses non-volatile memories based on two or more different non-volatile memory technologies in order to exploit the relative advantages of each these technology with respect to the others. In an exemplary embodiment, the memory system includes a controller and a flash memory, where the controller has a non-volatile RAM based on an alternate technology such as FeRAM. The flash memory is used for the storage of user data and the non-volatile RAM in the controller is used for system control data used by the control to manage the storage of host data in the flash memory. The use of an alternate non-volatile memory technology in the controller allows for a non-volatile copy of the most recent control data to be accessed more quickly as it can be updated on a bit by bit basis. In another exemplary embodiment, the alternate non-volatile memory is used as a cache where data can safely be staged prior to its being written to the to the memory or read back to the host.



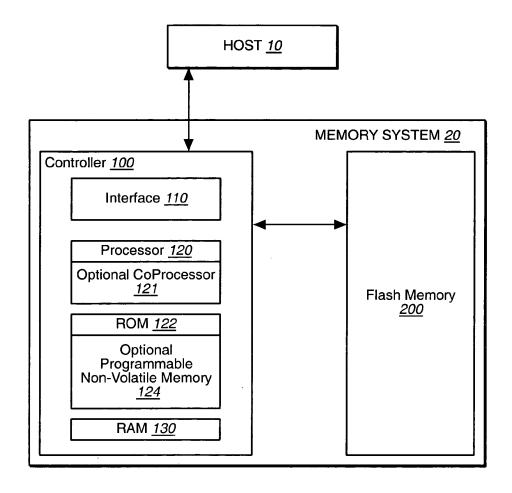
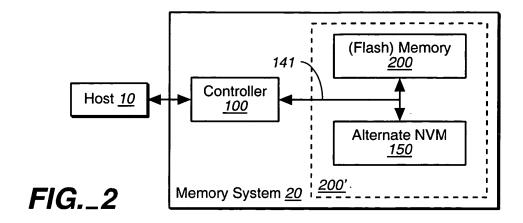
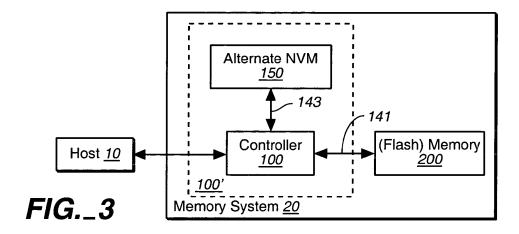
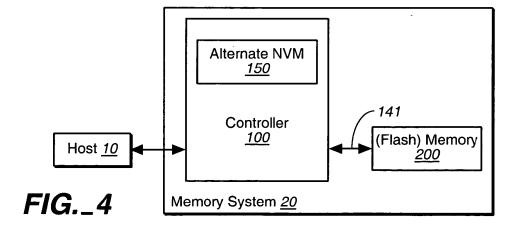


FIG.\_1



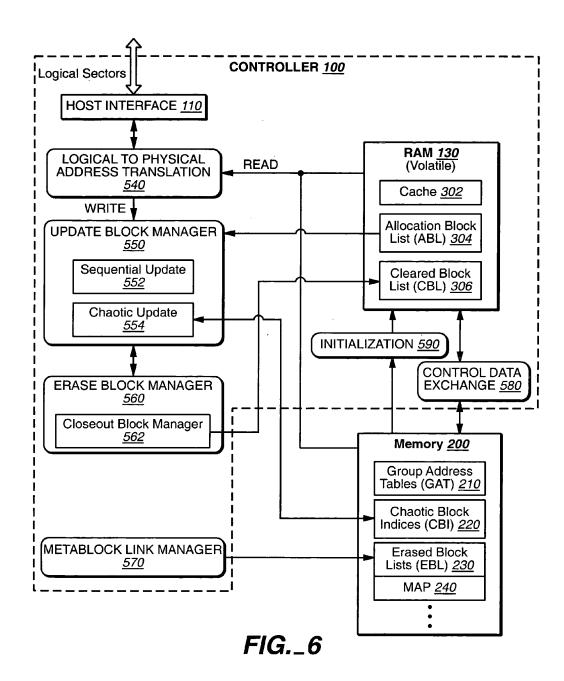




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Parameter Storage <u>151</u>
CPU Code Storage <u>153</u>
Logical Data Structure Cache <u>157</u>
Host Boot Sectors <u>159</u>
Single Sector Cache <u>161</u>
Multi-Segment Read/Write Cache <u>162</u>
Copy Buffers <u>163</u>

**FIG.\_**5



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