

(10) **Patent No.:** US 8,120,960 B2  
(45) **Date of Patent:** Feb. 21, 2012

- (56)
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- Primary Examiner — VanThu Nguyen

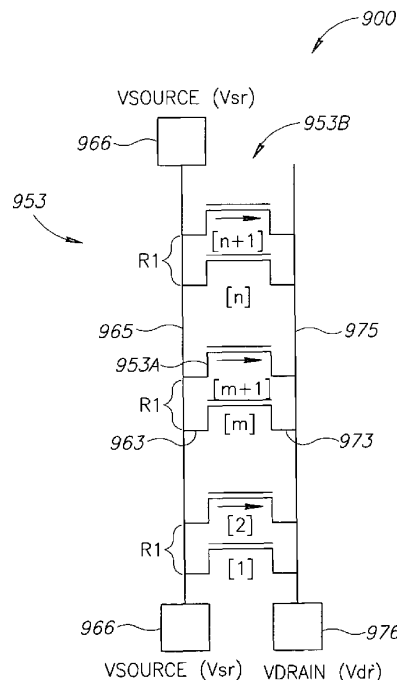
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## ABSTRACT

A non-volatile memory (NVM) having an array of memory cells and a unidirectional multiplexer (UMUX), the UMUX may be comprised of two or more address line ports adapted to receive addressing signals corresponding with elements in the memory array, and a set of switching transistors adapted to switch a supply voltage in accordance with the addressing signal such that current only flows into the array.

See application file for complete search history.

**5 Claims, 18 Drawing Sheets**



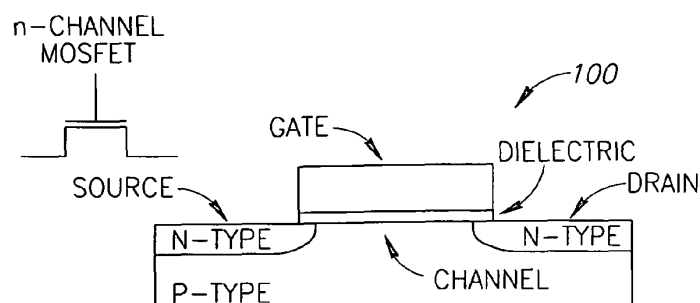


FIG. 1  
PRIOR ART

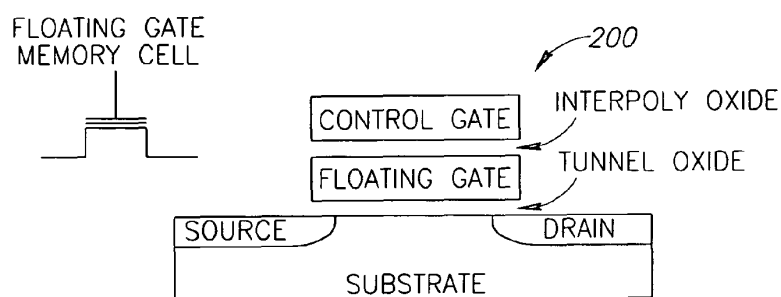


FIG. 2  
PRIOR ART

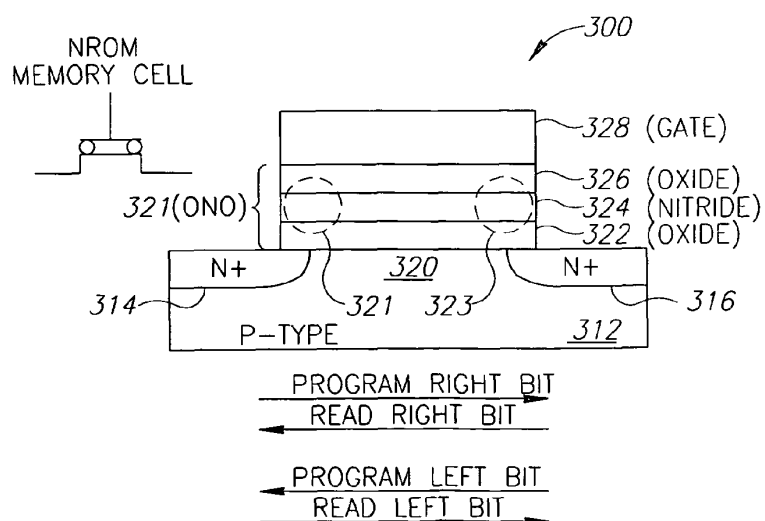


FIG. 3  
PRIOR ART

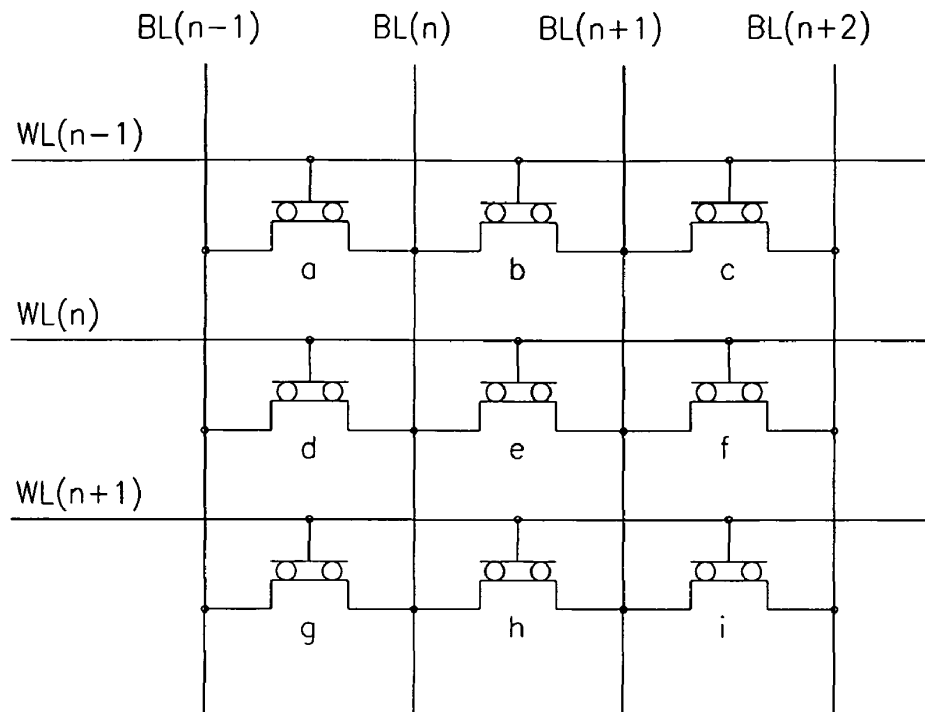


FIG. 4A  
PRIOR ART



FIG. 4B  
PRIOR ART

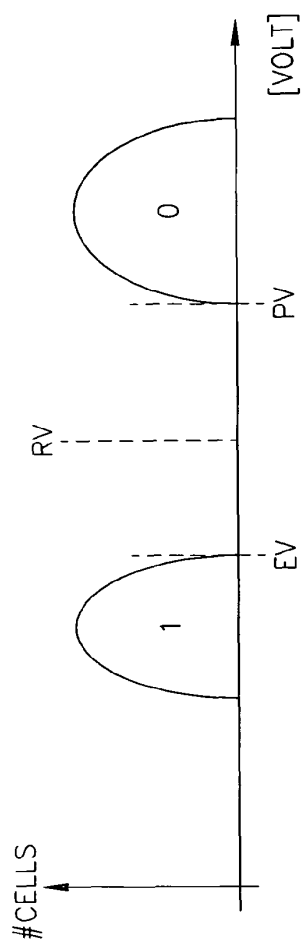


FIG. 5A  
PRIOR ART

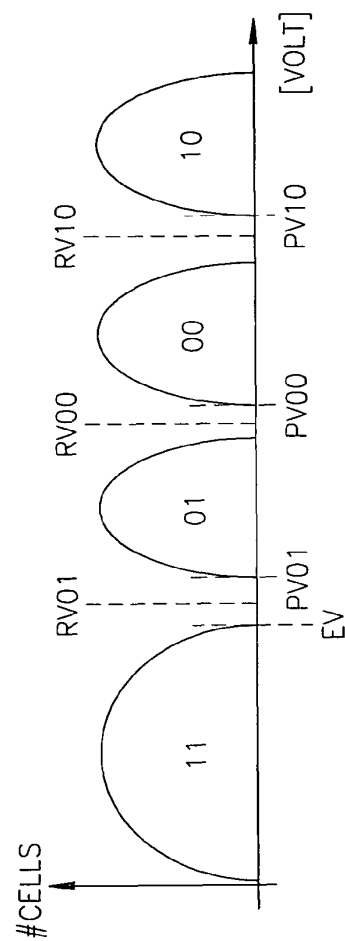
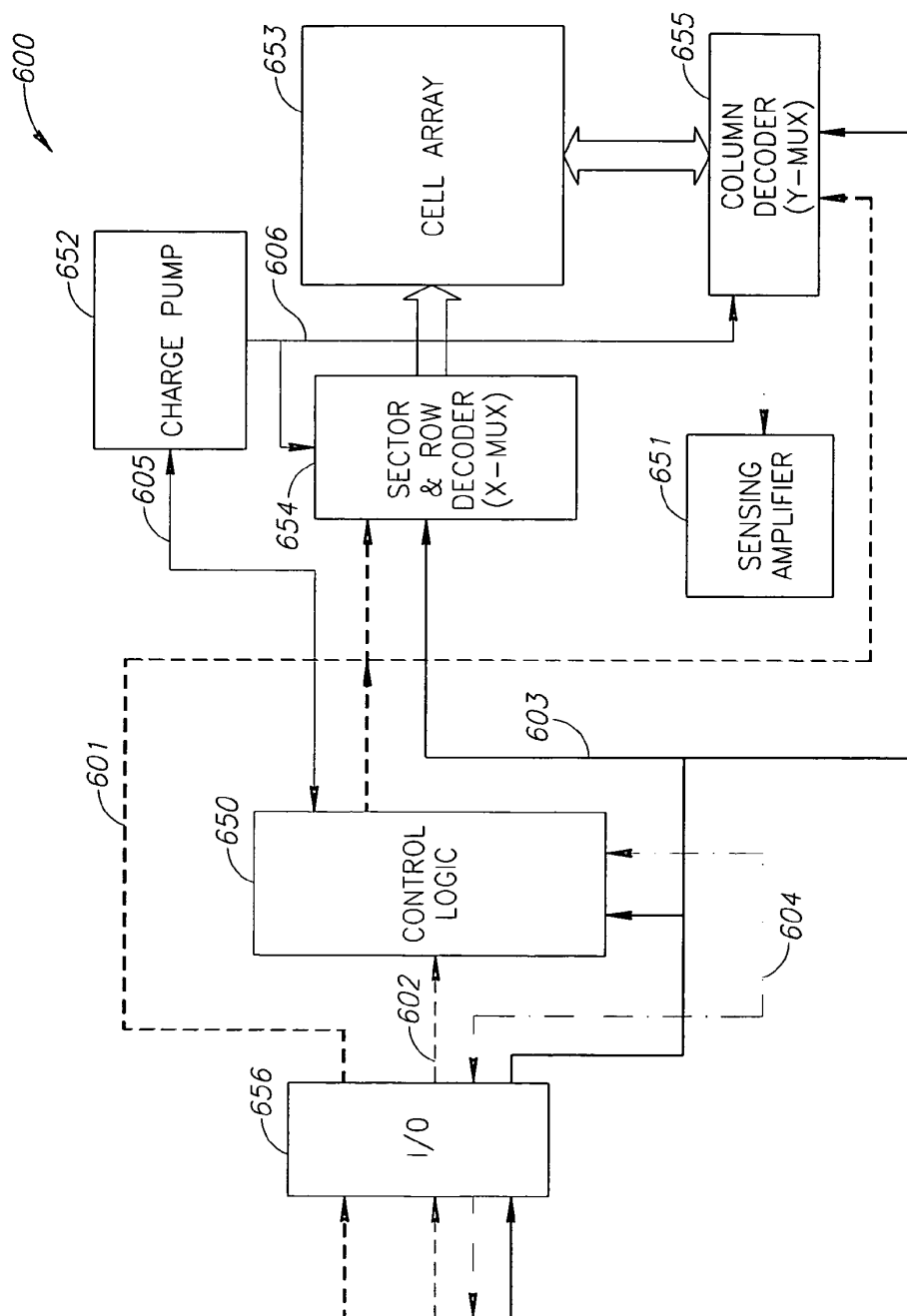


FIG. 5B  
PRIOR ART

FIG. 6  
PRIOR ART

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