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(12) **United States Patent**
Kolokowsky

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(54) **FLASH DRIVE FAST WEAR LEVELING**
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San Jose, CA (US)

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 147 days.

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(65) **Prior Publication Data**
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Related U.S. Application Data
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(57) **ABSTRACT**

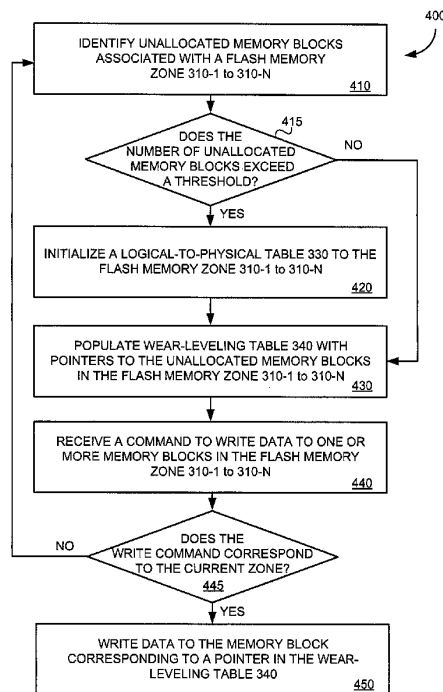
A system and method comprising a non-volatile memory including one or more memory blocks to store data, a controller to allocate one or more of the memory blocks to store data, and a wear-leveling table populated with pointers to unallocated memory blocks in the non-volatile memory, the controller to identify one or more pointers in the wear-leveling table and to allocate the unallocated memory blocks associated with the identified pointers for the storage of data.

(51) **Int. Cl.**
G06F 12/00 (2006.01)
(52) **U.S. Cl.** **711/103; 711/156; 365/218**
(58) **Field of Classification Search** **711/103**
See application file for complete search history.

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10 Claims, 4 Drawing Sheets



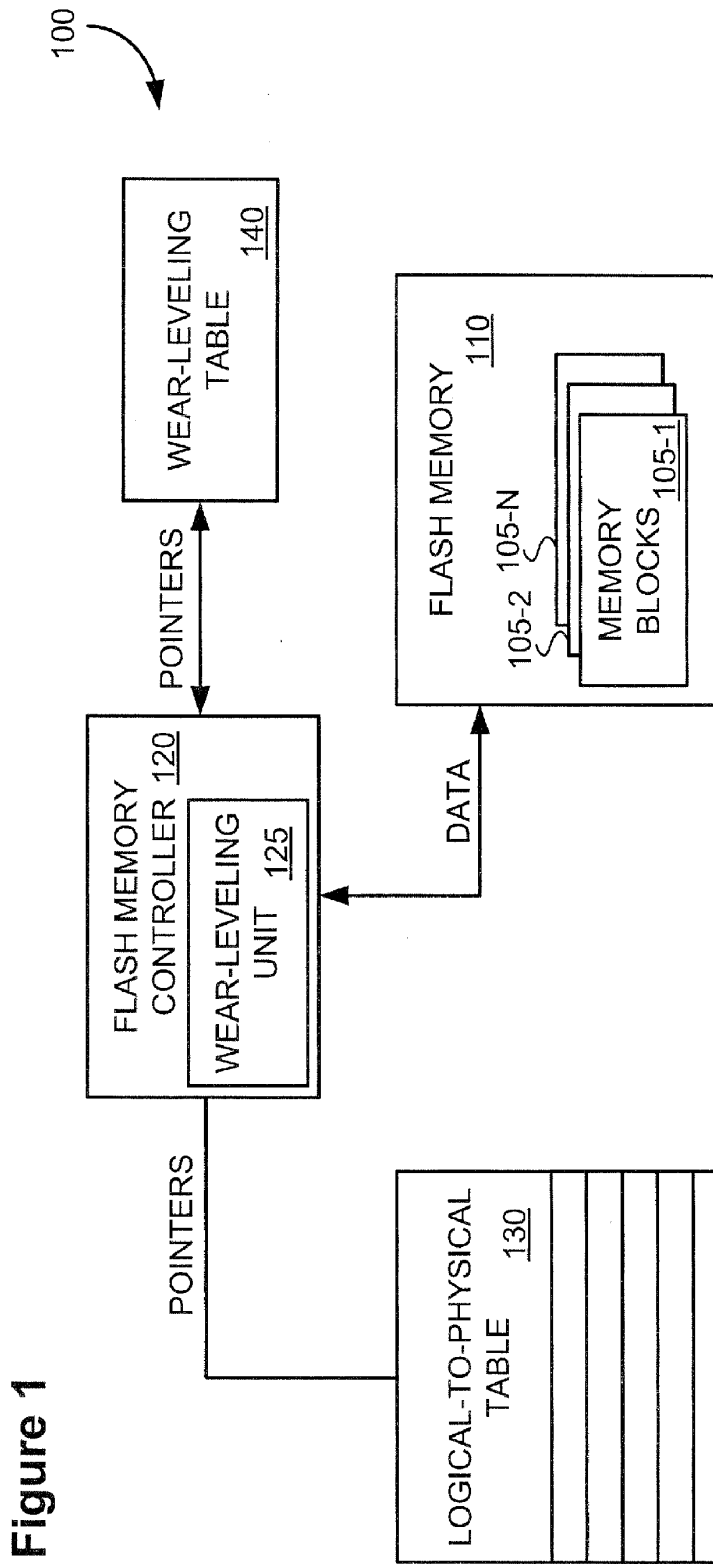
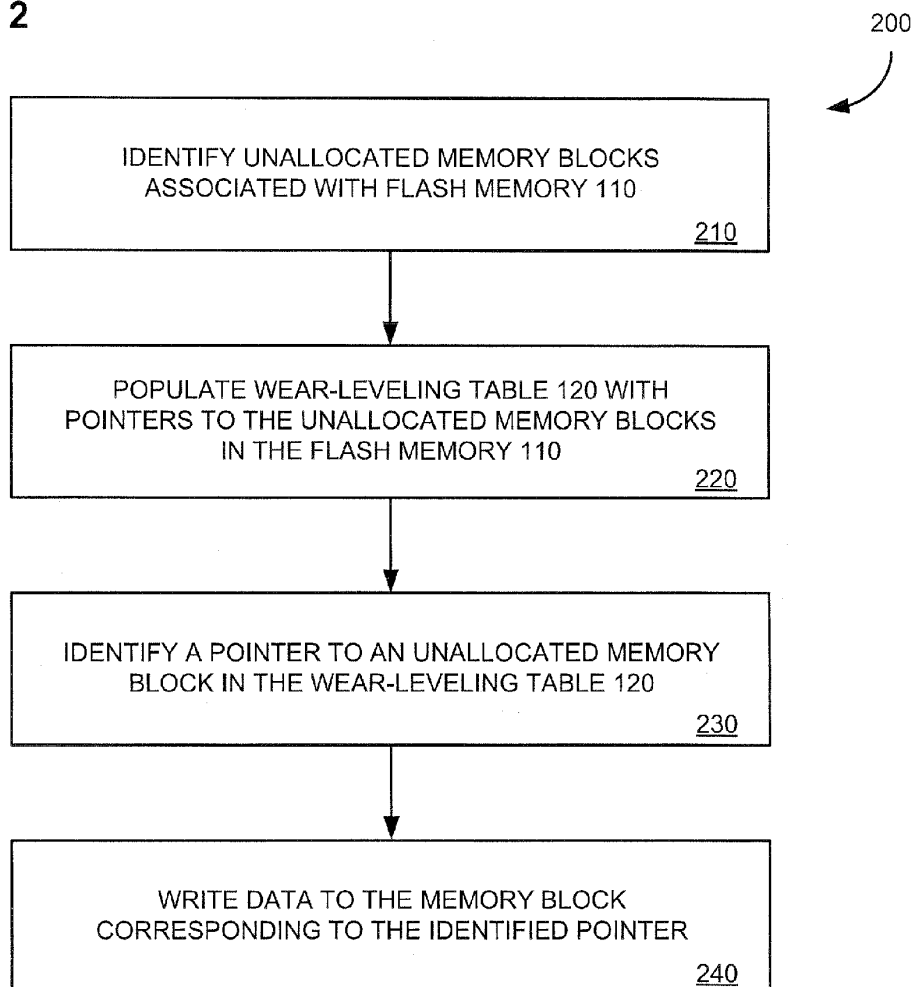


Figure 1

Figure 2



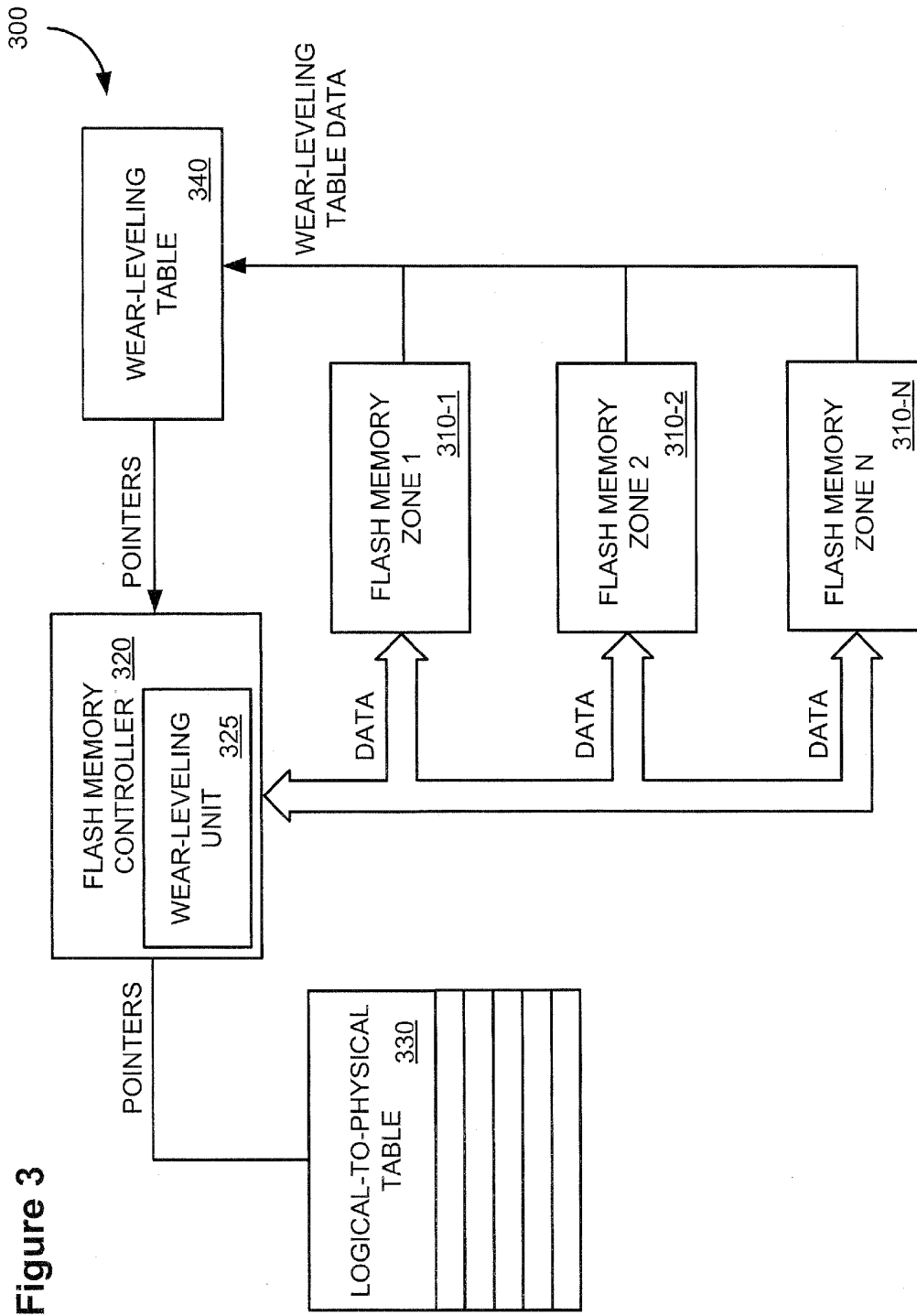
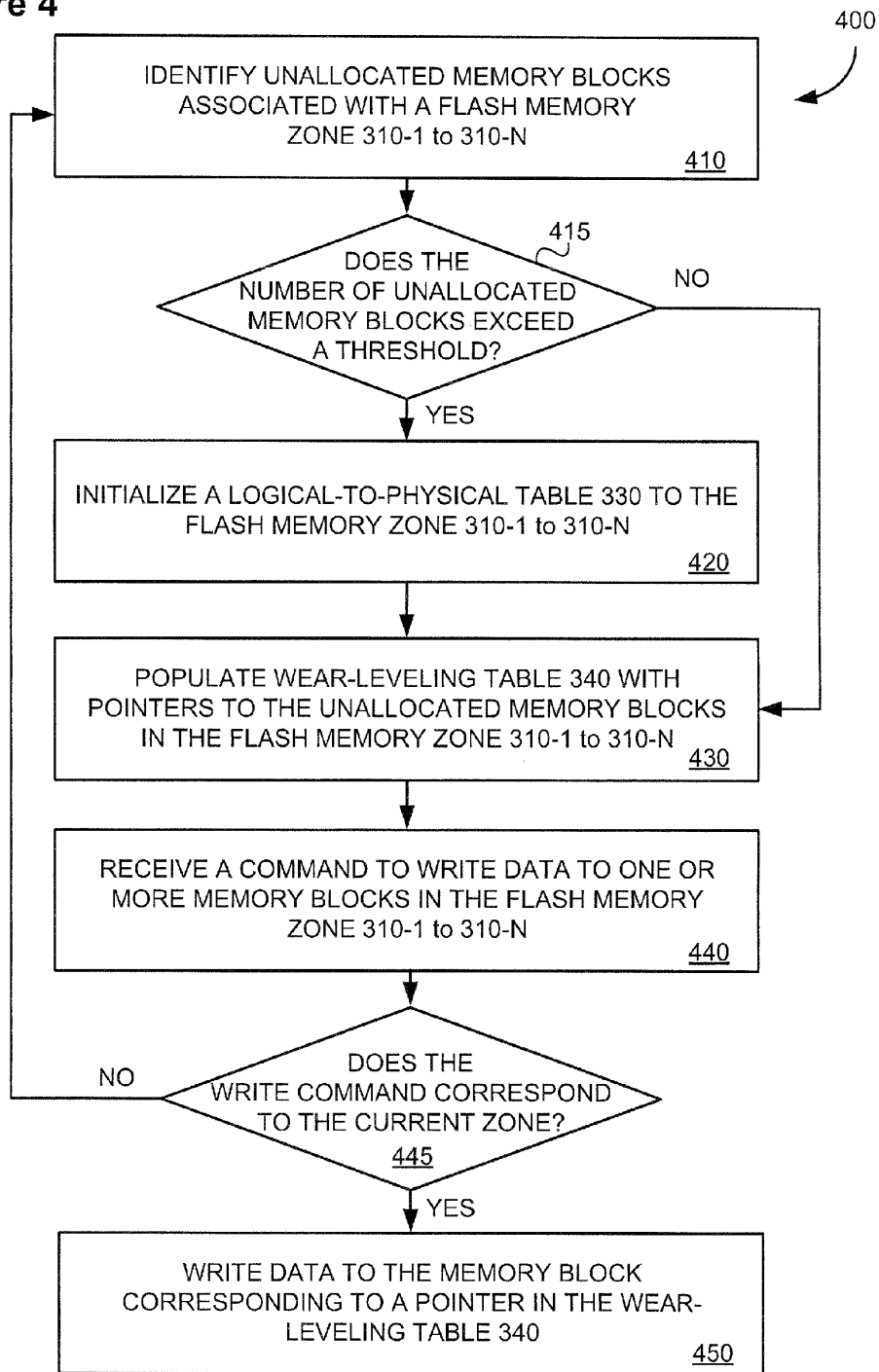


Figure 3

Figure 4



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