

# United States Patent [19]

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Blitz et al.

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[54] SOLID STATE DISK DRIVE UNIT HAVING ON-BOARD BACKUP NON-VOLATILE **MEMORY** 

[75] Inventors: Martin C. Blitz, Worcester; James C. Stegeman; Peter B. Bareham, both of

Acton, all of Mass.

[73] Assignee: Quantum Corporation, Milpitas, Calif.

[21] Appl. No.: 130,047

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**U.S. Cl.** ...... **395/750**; 395/182.04; 395/471;

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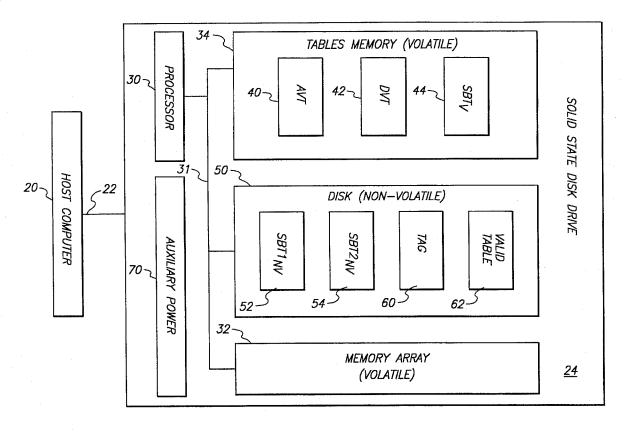
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Primary Examiner—Rebecca L. Rudolph Attorney, Agent, or Firm-David B. Harrison

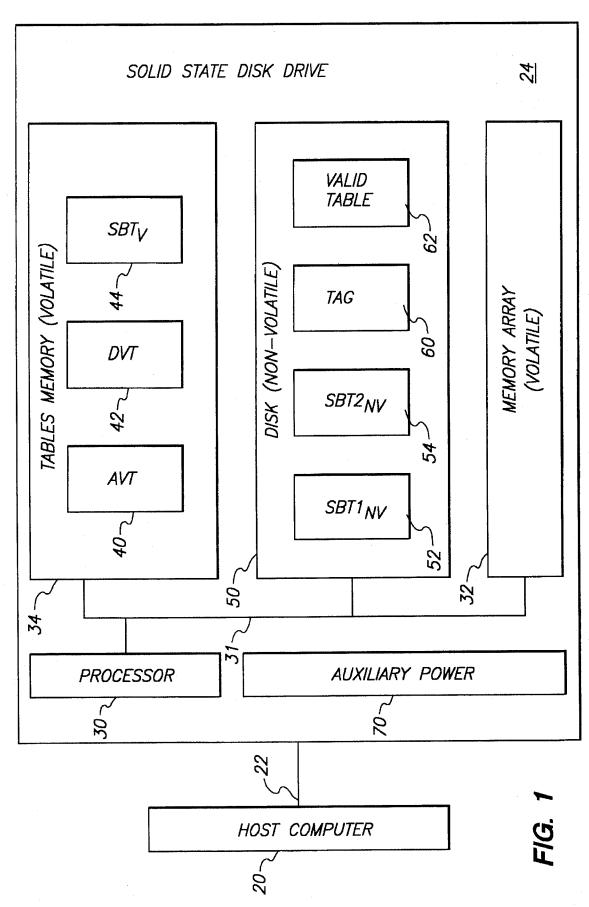
**ABSTRACT** 

Disclosed is a solid state disk drive, including a volatile, electronic RAM, memory and a non volatile, magnetic disk. The drive continuously saves unique data stored in the memory back to the disk. Additionally, the drive includes a number of tables and bit fields, in both volatile electronic memory and disk, for generally keeping track of what data has been restored from disk to memory, what data in the memory has been modified since it was restored from disk, and what modified data in the memory has been saved back to disk. In the event of a primary power outage, the drive first saves the volatile tables onto disk, and then saves the volatile, modified data onto disk, while using auxiliary power. If, however, auxiliary power is lost before any or all of the modified data is saved on disk, the saved tables provide information which enables the drive to distinguish the valid from the invalid data on disk.

### 27 Claims, 22 Drawing Sheets









DISK 50 (NON-VOLATILE)

(NON YOUNNEL)			
ADDR.	DATA	DISK TAGS	
0	DATA	*	
1	DATA	*	
2	DATA	*	
3	DATA	*	
4	DATA	*	
5	DATA	*	
6	DATA	*	
	• • •		
(N-2) (N-1)	DATA	*	
(N-1)	DATA	*	

FIG. 2A (AT T1)

MEMORY ARRAY 32 (VOLITILE)

ADDR.	DATA
0	*
1	*
2	*
3	*
4	*
5	*
6	*
	• • •
(N-2) (N-1)	*
(N-1)	*

# DISK 50 (NON-VOLATILE)

(**************************************					
ADDR.	SBT nv 52	SBT nv 54	TAG 60	TB VA 62	
0	*	*	*	*	
1	*	*			
2	*	*			
3	*	*			
4	*	*			
5	*	*			
6	*	*			
(N-2)	*	*			
(N-2) (N-1)	*	*			
CNTR	*	*			
,	90	92	-		

## TABLES MEMORY 34 (VOLITILE)

ADDR.	AVT 40	DVT 42	SBT <sub>V</sub> 44
0	*	*	*
1	*	*	*
2	*	*	*
3	*	*	*
4	*	*	*
5	*	*	*
6	*	*	*
(N-2)	*.	*	*
(N-1)	*	*	*

FIG. 3A (AT T1)

DISK 50 (NON-VOLATILE)

•		
ADDR.	DATA	DISK TAGS
0	DATA	1
1	DATA	1
2	DATA	1
3	DATA	1
4	DATA	1
5	DATA	1
6	DATA	1
(N-2)	DATA	1
(N-1)	DATA	1

MEMORY ARRAY 32 (VOLITILE)

ADDR.	DATA
0	*
1	*
2	*
3	*
4	*
5	*
6	*
(N-2) (N-1)	*
(N-1)	*

FIG. 2B (AT T2)

DISK 50 (NON-VOLATILE)

TBL. VAL.

ADDR.	SBT nv 52	SBT nv 54	TA 60
0	0	0	1
1	0	0	
2	0	0	
3	0	0	
4	0	0	
5	0	0	
6	0	0	
		0 • •	
(N-2) (N-1)	0	0	
(N-1)	0	0	
CNTR	0	1	
	90	92	

TABLES MEMORY 34 (VOLITILE)

ADDR.	AVT 40	DVT 42	SBT <sub>V</sub> 44
0	0	0	. 0
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
6	0	0	0
(N-2)	0	0	0
(N-1)	0	0	0

FIG. 3B (AT T2)

DISK 50 (NON-VOLATILE)

ADDR.	DATA	DISK TAGS
0	DATA	1
1	DATA	1
2	DATA	1
3	DATA	1
4	DATA	1
5	DATA	1
6	DATA	1
(N-2)	DATA	1
(N-1)	DATA	1

FIG. 2C (AT T3)

## MEMORY ARRAY 32 (VOLITILE)

ADDR.	DATA
0	DATA
1	*
2	*
3	*
4	*
5	*
6	*
(N-2)	*
(N-1)	*

## DISK 50 (NON-VOLATILE)

TBL. VAL. 62

ADDR.	SBT nv 52	SBT nv 54	TAG 60
0	0	0	1
1	0	0	
2	0	0	
3	0	0	
4	0	0	
5	0	0	
6	0	0	
(N-2) (N-1)	0	0	
(N-1)	0	0	
CNTR	0	1	
	90	92	

## TABLES MEMORY 34 (VOLITILE)

ADDR.	AVT 40	DVT 42	SBT <sub>V</sub> 44
0	1	1	0
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
6	0	0	0
(N-2)	0	0	0
(N-1)	0	0	0

FIG. 3C (AT T3)



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