



US 20080140918A1

(19) **United States**

(12) **Patent Application Publication**  
**Sutardja**

(10) **Pub. No.: US 2008/0140918 A1**

(43) **Pub. Date: Jun. 12, 2008**

(54) **HYBRID NON-VOLATILE SOLID STATE MEMORY SYSTEM**

**Publication Classification**

(76) Inventor: **Pantas Sutardja**, Los Gatos, CA (US)

(51) **Int. Cl.**  
*G06F 12/02* (2006.01)  
*G06F 12/00* (2006.01)  
(52) **U.S. Cl.** ..... **711/103**; 711/154; 711/E12.008; 711/E12.001

Correspondence Address:  
**HARNES, DICKEY & PIERCE P.L.C.**  
**5445 CORPORATE DRIVE, SUITE 200**  
**TROY, MI 48098**

(57) **ABSTRACT**

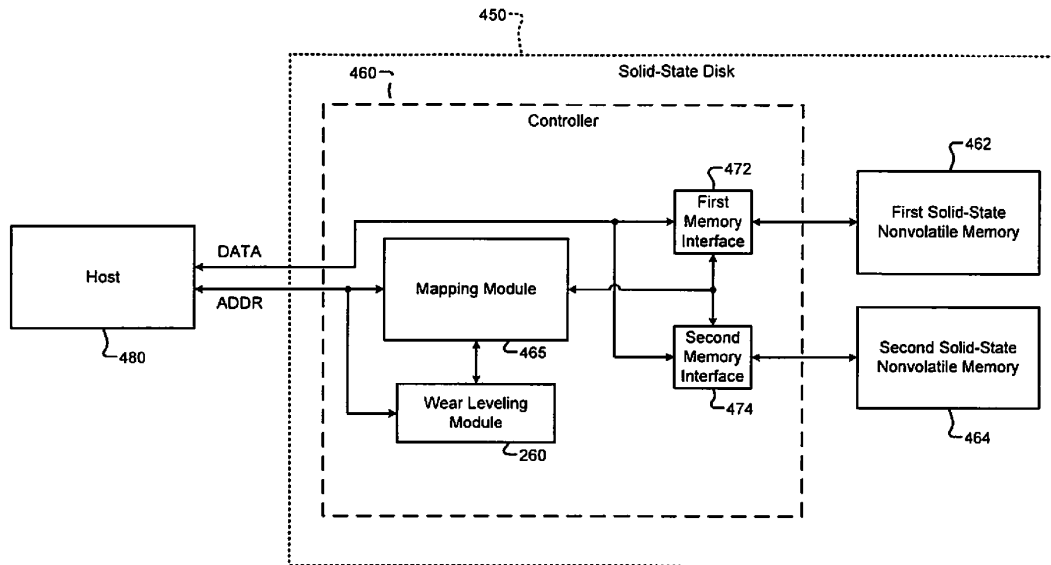
A solid state memory system comprises a first nonvolatile semiconductor (NVS) memory that has a first write cycle lifetime, a second nonvolatile semiconductor (NVS) memory that has a second write cycle lifetime that is different than the first write cycle lifetime, and a wear leveling module. The wear leveling module generates first and second wear levels for the first and second NVS memories based on the first and second write cycle lifetimes and maps logical addresses to physical addresses of one of the first and second NVS memories based on the first and second wear levels.

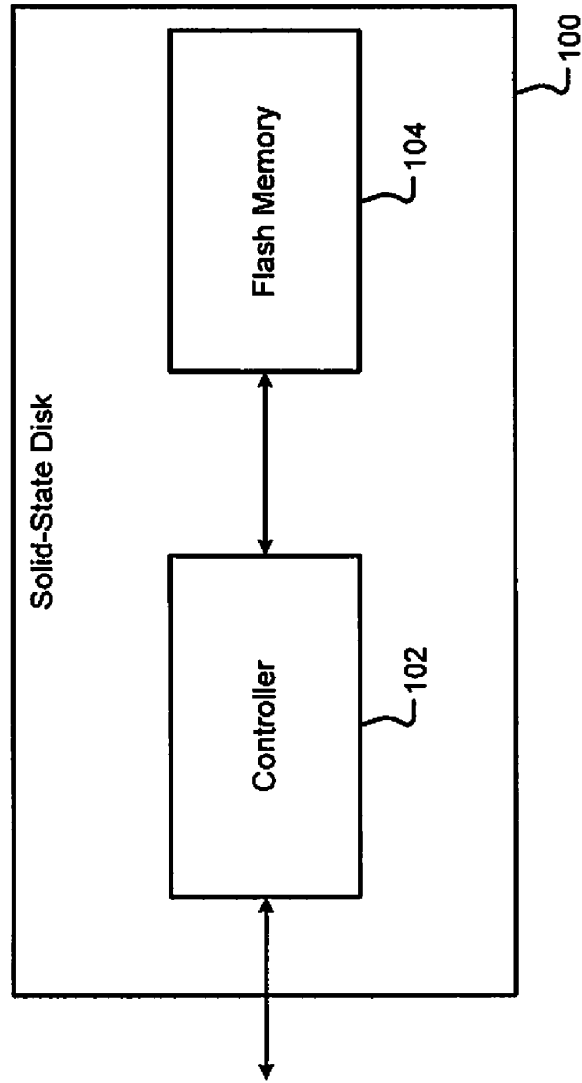
(21) Appl. No.: **11/952,648**

(22) Filed: **Dec. 7, 2007**

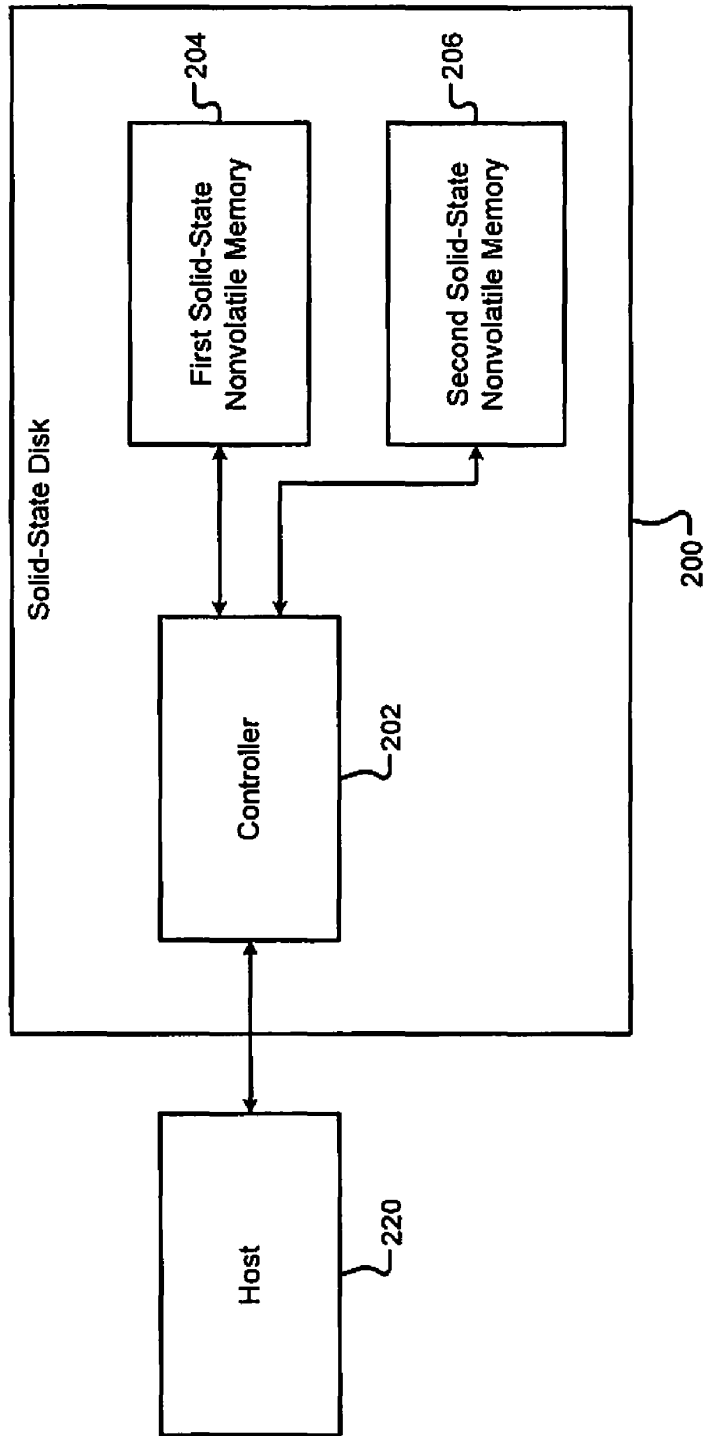
**Related U.S. Application Data**

(60) Provisional application No. 60/869,493, filed on Dec. 11, 2006.

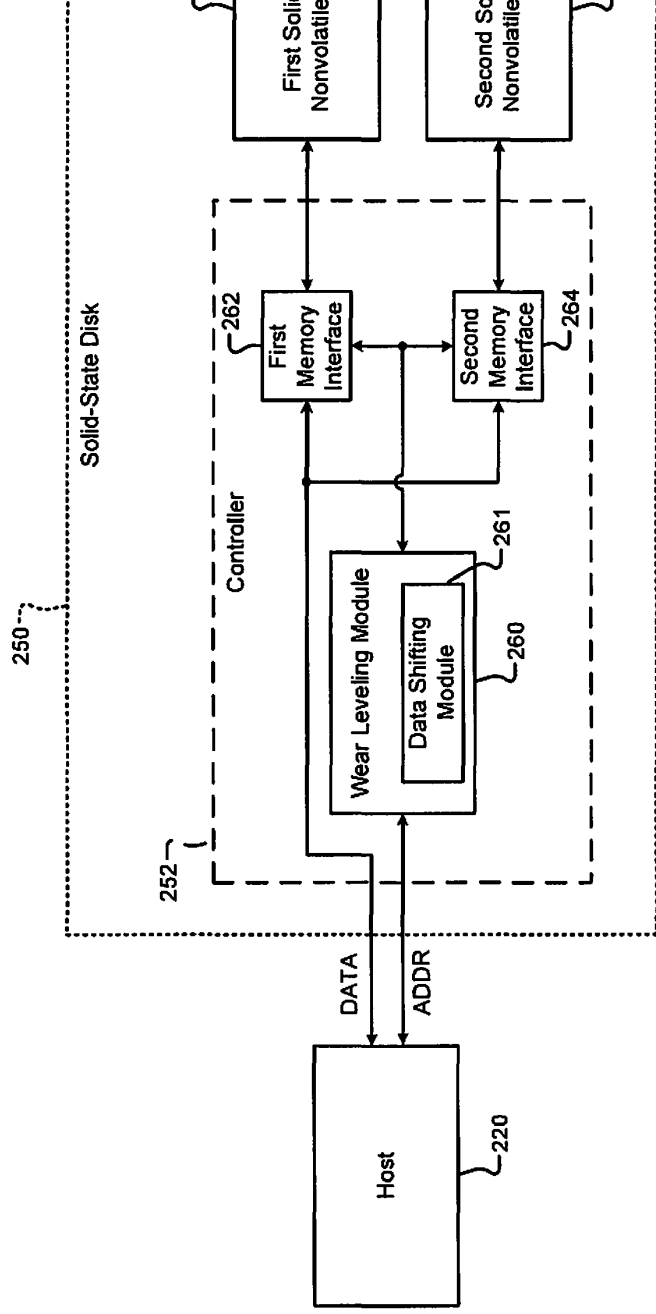




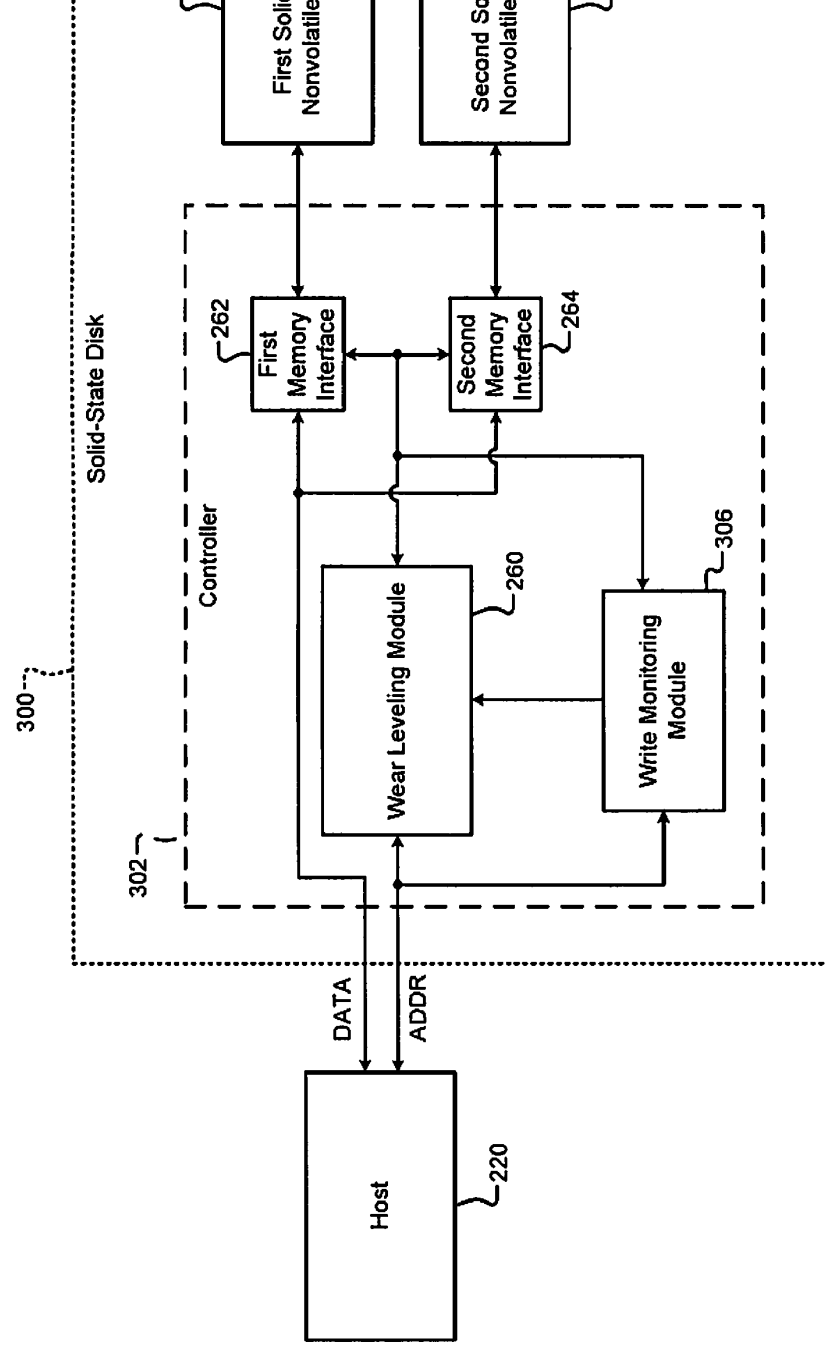
**FIG. 1**  
Prior Art



**FIG. 2**



**FIG. 3**



**FIG. 4A**

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