

[54] **POWER CONSUMPTION CONTROL SYSTEM FOR ELECTRONIC DIGITAL DATA PROCESSING DEVICES**

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[51] Int. Cl.<sup>3</sup> ..... **G06F 11/30; H03K 5/13**

[52] U.S. Cl. .... **364/900; 307/269; 365/227**

[58] **Field of Search** ..... 235/455, 472; 250/205, 250/568; 340/146.3 F, 146.3 R, 365 R; 365/226-229; 307/203, 269; 364/200 MS File, 900 MS File, 736

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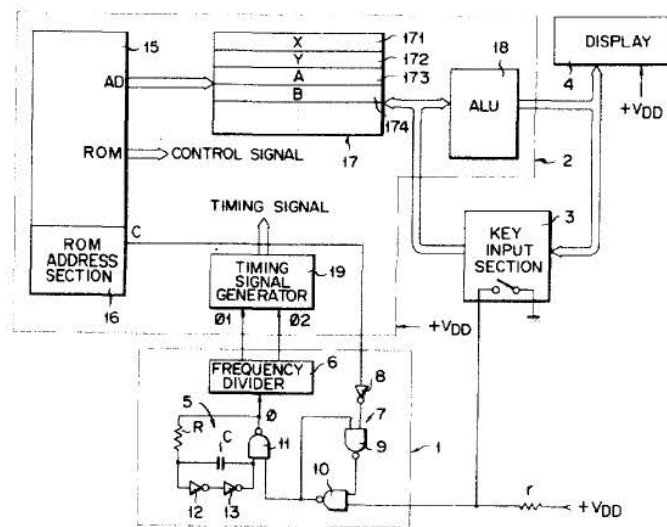
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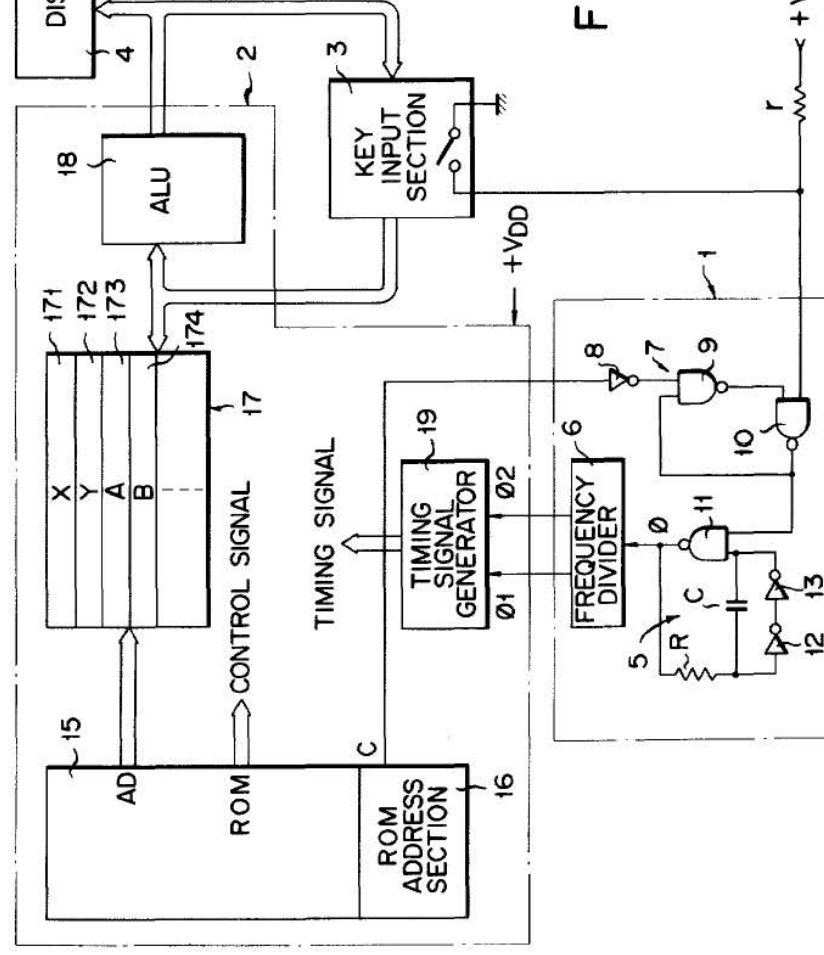
*Primary Examiner*—Leo H. Boudreau  
*Attorney, Agent, or Firm*—Frishauf, Holtz, Goodman and Woodward

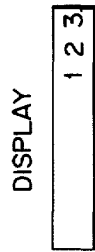
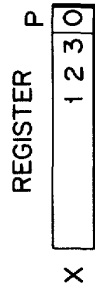
[57] **ABSTRACT**

A power consumption control system for electronic digital data processing devices is provided with a counter circuit to update a time count operation and to count a given time every time any one of the keys in the key input section is depressed. When none of the keys in the key input section is depressed before the counter circuit finishes the count of the given time, the oscillation operation of the oscillator circuit which supplies the clock pulse to the data processing device is stopped.

**9 Claims, 39 Drawing Figures**







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FIG. 2(a)



FIG. 2(b) AUTOMATIC OFF



FIG. 2(c) KEY ON

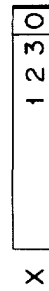


FIG. 2(d) KEY OFF

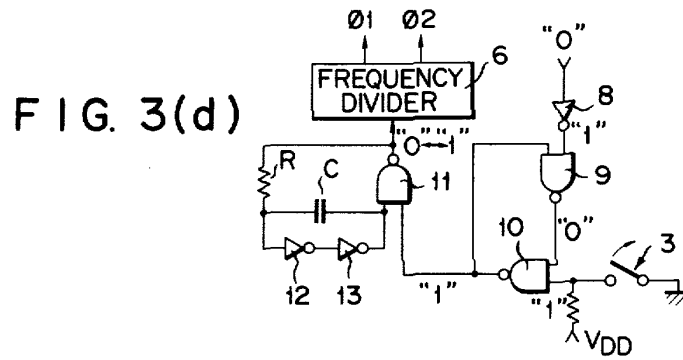
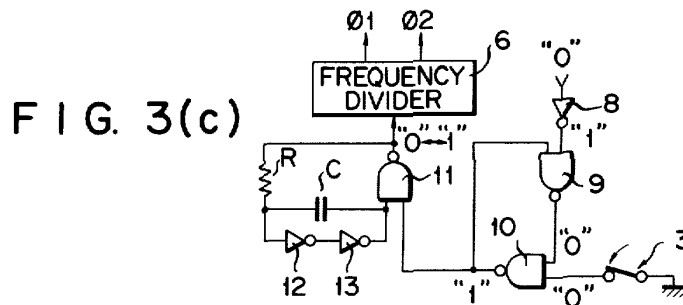
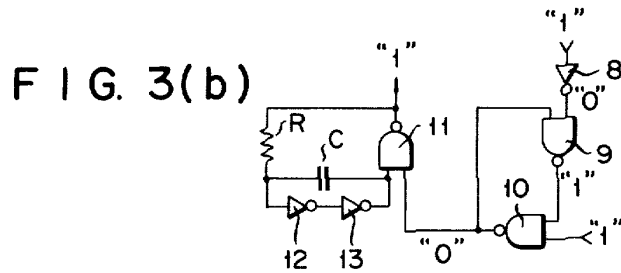
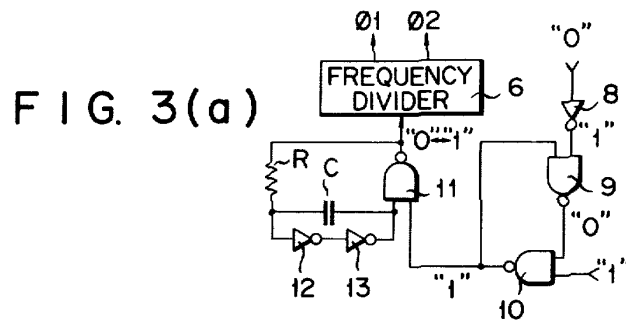
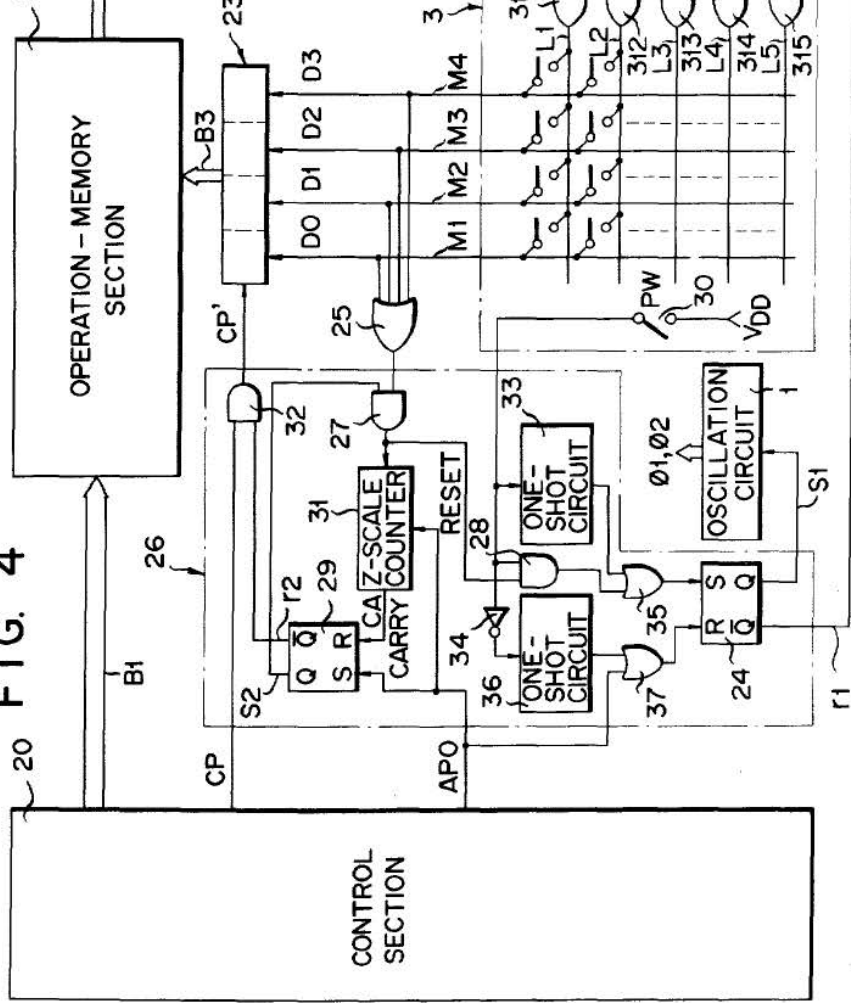


FIG. 4



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