JESD100-A

JEDEC STANDARD

Terms, Definitions, and Letter Symbols for Microcomputers, Microprocessors, and Memory Integrated Circuits

JESD100-A
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1. GENERAL CONCEPTS AND TYPES (cont'd)

cycle

(1) A sequence of operations in which one set of events is completed.

(2) Any set of operations that is repeated regularly in the same sequence. The operations may be subject to variations on each repetition. (ANSI X3.172)

data bus

A bus used to communicate data internally and externally to and from processing units, storage devices, or peripheral devices. (Adapted from ANSI X3.172.)

data retention mode

A standby or battery mode of operation in which the integrity of stored data is maintained although the supply voltage is below that specified for reading or writing.

duplex transmission

Data transmission in both directions simultaneously. (ANSI X3.172)

dynamic (read/write) memory (DRAM)

A read/write memory in which the cells require the repetitive application of control signals generated inside or outside the integrated circuit in order to retain stored data. (Adapted from IEC 748-2.)

NOTE 1: The words "read/write" may be omitted from the term when no misunderstanding is likely.

NOTE 2: Each repetitive application of the control signals is normally called a refresh operation or cycle.

NOTE 3: A dynamic memory may use static addressing or sensing circuits.

NOTE 4: Contrast with static (read/write) memory.

electrically alterable read-only memory (EAROM)

Synonym for "electrically erasable programmable read-only memory".

electrically erasable programmable read-only memory (EEPROM)

A reprogrammable read-only memory in which cells may be erased electrically and in which each cell may be reprogrammed electrically.

erasable programmable read-only memory (EPROM)

A reprogrammable read-only memory in which all cells may be simultaneously erased using ultraviolet light and in which each cell may be reprogrammed electrically.