United States Patent [19]

Fairbanks et al.

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[54]	POWER SUPPLY AND OSCILLATOR FOR A COMPUTER SYSTEM PROVIDING AUTOMATIC SELECTION OF SUPPLY VOLTAGE AND FREQUENCY
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[21]	Appl. No.: 374,514
[22]	Filed: Jun. 30, 1989
[52]	Int. Cl. ⁵
[58]	Field of Search
[56]	References Cited
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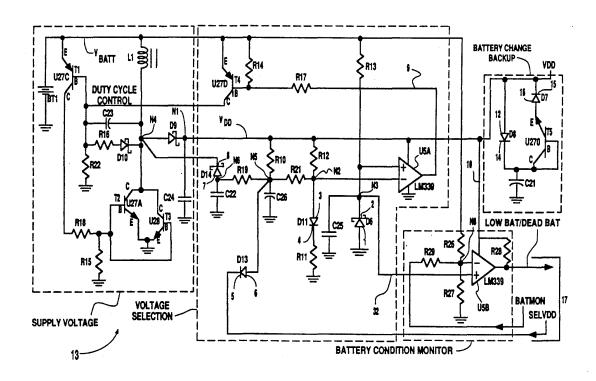
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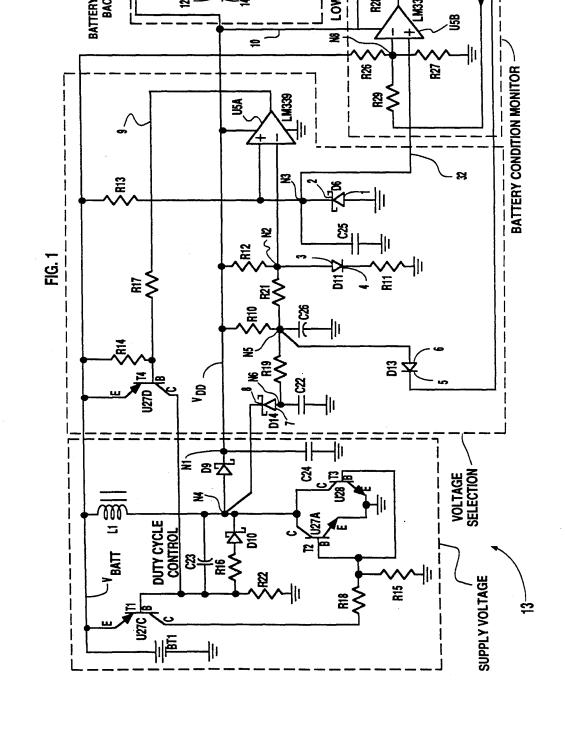
Primary Examiner—J. R. Scott Assistant Examiner—David Osborn Attorney, Agent, or Firm—Skjerven, Morrill, MacPherson, Franklin & Friel

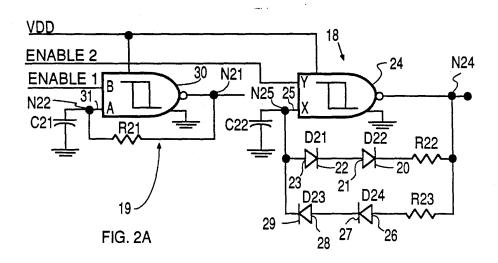
[57] ABSTRACT

Disclosed is a power system for use with a computer, the power system having incorporated in it circuitry for automatically varying the supply voltage output to the computer system based upon the magnitude of the current being supplied to the computer by the power system. Also included in the computer system is a variable frequency clock circuit, the frequency of which changes based upon the supply voltage produced by the power system. This permits, during computer system operation where low voltage and low clock speeds will be sufficient to provide the performance needed, achievement of a power saving since both the voltage and frequency at which the system operates is reduced, thereby markedly reducing the power consumption.

15 Claims, 7 Drawing Sheets







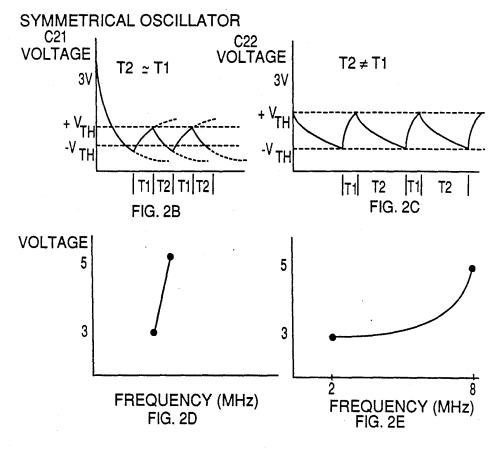
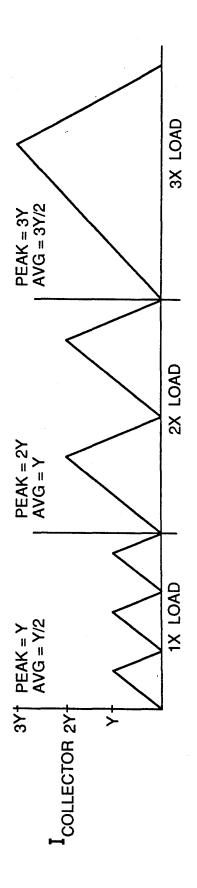
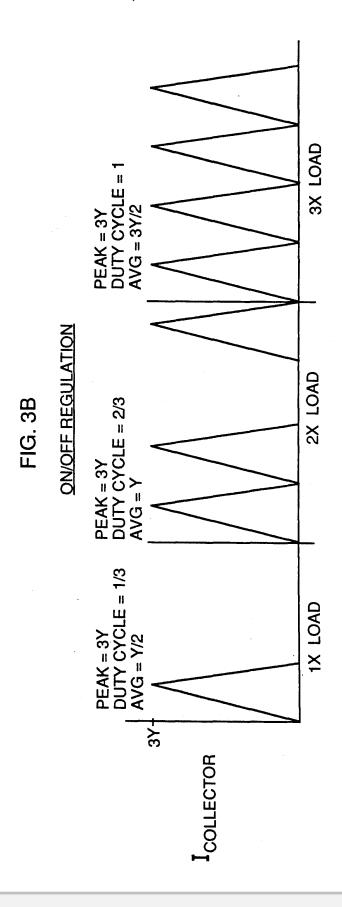




FIG. 3A (PRIOR ART) PULSE WIDTH MODULATION





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