

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

UNIFIED PATENTS, LLC
Petitioner

v.

MEMORYWEB, LLC
Patent Owner

Patent No. 10,621,228

Inter Partes Review No. IPR2021-01413

DECLARATION OF PROFESSOR GLENN REINMAN

I.	INTRODUCTION	3
II.	BACKGROUND AND QUALIFICATIONS	3
III.	MATERIALS CONSIDERED	9
IV.	LEGAL STANDARDS	10
	A. Claim Construction	10
	B. Anticipation	11
	C. Obviousness	12
	D. Method Claims	16
	E. Dependent Claims	16
V.	OVERVIEW OF THE ‘228 PATENT	16
VI.	THE ‘228 PATENT’S EFFECTIVE FILING DATE	21
VII.	LEVEL OF SKILL IN THE ART	22
VIII.	OPINIONS	22
	A. Summary of Petitioner’s References	23
	1. Overview of Okamura	23
	2. Overview of Flora	32
	3. Wagner (EX)	35
	4. Gilley (EX)	35
	B. Claim Construction	35
	1. Limitations [1b], [1d] and [1e]: “responsive to a first input, causing a map view to be displayed ... the map view including: ... a first / second] location selectable thumbnail image	35
	2. Limitations [1n] and [1p]: “the people view including:	37
	3. Limitations [1b], [1d] and claim 3 “the first indication feature is connected to the first location selectable thumbnail image”	38
	4. Claim 5: “the map view further includes a first indication feature” and “second indication feature”	40
	C. Ground 1: Okamura and Flora	41
	1. Limitation [1c]: “the map view including: (i) an interactive map” ..	42
	2. Limitation [1d] the “map view including . . . [first/second] location selectable thumbnail image[s]”	44

a. A POSITA Would Not Modify Okamura with Flora	44
b. Accused Infringer Construction of Thumbnail Image	61
3. Limitations [1b], [1d] and [1e]: “responsive to a first input, causing a map view to be displayed ... the map view including: ... a [first / second] location selectable thumbnail image”	62
4. Limitations [1n] and [1p]: “the people view including:	64
5. Dependent Claims 2 – 7.....	65
a. Limitations [1b], [1d] and claim 3 “the first indication feature is connected to the first location selectable thumbnail image”	66
b. Claim 5: “the map view further includes a second indication feature”	67
D. Ground 2: Okamura, Flora, and Wagner	69
1. Petitioner’s Reliance on Ground 1 for Ground 2.....	69
2. Limitations [1g] and [1j]	70
E. Ground 3: Okamura, Flora, and Gilley.....	71
F. Ground 4: Okamura, Flora, Wagner, and Gilley.....	72
IX. CONCLUSION.....	72

I, Glenn Reinman, declare as follows:

I. INTRODUCTION

1. I have been retained on behalf of MemoryWeb, LLC, (“MemoryWeb” or “Patent Owner”) as an independent expert consultant to provide this declaration concerning the technical subject matter relevant to the *inter partes* review (“IPR”) petition of U.S. Patent No. 10,621,228 (“the ‘228 patent”) filed by Unified Patents, LLC (“Petitioner”).

2. I am being compensated at my standard hourly rate of \$750 per hour for the time I spend on this matter. My compensation is not related in any way to the outcome of this proceeding, and I have no other interest in this proceeding.

3. In this declaration, I offer my expert opinion regarding the technical subject matter of claims 1-7 (“the challenged claims”) of the ‘228 patent. Specifically, I have considered whether claims 1-7 of the ‘228 patent are valid under 35 U.S.C. § 103. The substance and bases of my opinions appear below.

II. BACKGROUND AND QUALIFICATIONS

4. A copy of my curriculum vitae is appended hereto as Appendix A. I am currently a professor of Computer Science, serving as vice chair of the Computer Science department, at the University of California, Los Angeles (UCLA).

5. I received a Bachelor of Science degree in Computer Science and Engineering from the Massachusetts Institute of Technology (MIT) in June 1996. In

March 1999, I received a Master of Science degree in Computer Science from the University of California at San Diego. I received my Doctor of Philosophy degree in Computer Science from the University of California at San Diego in June 2001.

6. In 2001, I became an Assistant Professor at the University of California in Los Angeles (UCLA) in the Department of Computer Science. In 2007, I was promoted to the position of an Associate Professor, and in 2014, I became a Full Professor. From 2016 through 2019, I was the Graduate Vice Chair of the Computer Science department at UCLA, in charge of the Graduate Degree Program. Starting in 2021, I became the Undergraduate Vice Chair of the Computer Science department at UCLA, in charge of the Undergraduate Degree Program.

7. I teach subjects in computer science, such as computer systems architecture, microprocessor design, microprocessor simulation, distributed and parallel systems.

8. I began my career with summer internships at Intel Corporation and Compaq (now HP) in 1998 and 1999, respectively. At Intel I researched issues such as the viability of caching state from the branch predictor, the translation lookaside buffer, and the branch target buffer in the second-level data cache. I also modified SimpleScalar—a system software infrastructure used to build modeling applications for program performance analysis, microarchitectural modeling, and hardware-software co-verification—to use ITR traces for Windows applications for

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