

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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ADVANCED ENERGY INDUSTRIES INC.,  
PETITIONER,

v.

RENO TECHNOLOGIES INC.,  
PATENT OWNER.

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Case IPR2021-01397  
Patent 10,707,057

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**PETITION FOR *INTER PARTES* REVIEW OF USPN 10,707,057  
UNDER 35 U.S.C. §§ 311 *ET SEQ.* AND  
37 C.F.R. § 42.100 *ET SEQ.***

## TABLE OF CONTENTS

I.	Introduction.....	1
II.	MANDATORY NOTICES UNDER 37 C.F.R. § 42.8(a)(1).....	3
	A. Real Party In Interest Under 37 C.F.R. § 42.8(b)(1).....	3
	B. Related Matters Under 37 C.F.R. § 42.8(b)(2) .....	4
	C. Lead and Back-Up Counsel Under 37 C.F.R. § 42.8(b)(3) .....	4
	D. Service Information Under 37 C.F.R. § 42.8(b)(4).....	5
	E. Payment of Fees Under 37 C.F.R. § 42.15.....	5
	F. Certification of Word Count Under 37 C.F.R. § 42.24(d).....	5
III.	GROUNDS FOR STANDING UNDER 37 C.F.R. § 42.104(a).....	6
IV.	IDENTIFICATION OF GROUNDS FOR WHICH REVIEW IS REQUESTED UNDER 37 C.F.R. § 42.104(b)(1).....	6
V.	HOW THE CHALLENGED CLAIMS ARE TO BE CONSTRUED UNDER 37 C.F.R. § 42.104(b)(3) .....	6
VI.	BACKGROUND OF TECHNOLOGY .....	7
VII.	OVERVIEW OF THE ‘057 PATENT .....	10
	A. Summary of the Disclosure of the Claimed Subject Matter .....	10
	B. Summary of the Prosecution of U.S. Patent No. 10,707,057.....	12
	C. Summary of Application No. 15/373,271 (the ‘271 Application).....	13
	D. Summary of <i>Inter Partes</i> Review Proceeding on USPN 9,946,122 .....	15

VIII. OVERVIEW OF THE PRIOR ART .....	16
A. Summary of Mavretic.....	16
B. Summary of Mason .....	18
C. Summary of Navigator II .....	20
D. Summary of Howald .....	24
IX. HOW THE CONSTRUED CLAIMS ARE UNPATENTABLE UNDER 37 C.F.R. § 42.104(b)(4) .....	24
A. Level of Skill in the Art.....	24
B. Claims 1, 2, 4, 5, 9-13, and 16-17 are rendered obvious under 35 U.S.C. § 103 by Mavretic in view of Mason .....	25
C. Claims 3, 6-8, 14 and 15 are rendered obvious under 35 U.S.C. § 103 by Mavretic in view of Mason and Navigator II.....	61
D. Claims 3, 6, 8 and 14 and are rendered obvious under 35 U.S.C. § 103 by Mavretic in view of Mason and Howald.....	67
X. BOARD SHOULD NOT DENY THIS PETITION UNDER §325(D) .....	69
A. First Prong of <i>Advanced Bionics</i> .....	70
B. Second Prong of <i>Advanced Bionics</i> .....	71
XI. CONCLUSION.....	74

## LIST OF EXHIBITS

Exhibit 1001	U.S. Patent No. 10,707,057 (“the ’057 patent”)
Exhibit 1002	Declaration of Dr. Steven Voldman
Exhibit 1003	CV of Dr. Steven Voldman
Exhibit 1004	U.S. Patent No. 5,654,679 to Mavretic, et al. (“Mavretic”)
Exhibit 1005	U.S. Patent No. 8,436,643 to Mason (“Mason”)
Exhibit 1006	Navigator® II Matching Networks With a Solid-State Technology Option, presented by Advanced Energy Industries, Inc. at SEMICON West 2012 (“Navigator II”)
Exhibit 1007	U.S. Patent No. 6,259,334 to Howald (“Howald”)
Exhibit 1008	Valentin Todorow, Impedance Matching and Matching Networks, (PowerPoint slides), Etch Products Business Group, Applied Materials (December 2009) (“Todorow”)  available at:  <a href="https://pdfs.semanticscholar.org/presentation/3711/53475dd48c0f71938d8d49dad2d83c1a2579.pdf">https://pdfs.semanticscholar.org/presentation/3711/53475dd48c0f71938d8d49dad2d83c1a2579.pdf</a> .
Exhibit 1009	Lou Frenzel, Back to Basics: Impedance Matching (Part 1), Electronic Design, (October 24, 2011) (“Frenzel Part 1”)  available at:  <a href="https://www.electronicdesign.com/datasheet/back-basics-impedance-matching-part-1">https://www.electronicdesign.com/datasheet/back-basics-impedance-matching-part-1</a>
Exhibit 1010	Lou Frenzel, Back to Basics: Impedance Matching (Part 2), Electronic Design (March 1, 2012) (“Frenzel Part 2”)  available at:

	<a href="https://www.electronicdesign.com/datasheet/back-basics-impedance-matching-part-2">https://www.electronicdesign.com/datasheet/back-basics-impedance-matching-part-2</a> .
Exhibit 1011	Lou Frenzel, Back to Basics: Impedance Matching (Part 3), Electronic Design (March 14, 2012) (“Frenzel Part 3”)  available at:  <a href="https://www.electronicdesign.com/datasheet/back-basics-impedance-matching-part-3">https://www.electronicdesign.com/datasheet/back-basics-impedance-matching-part-3</a> .
Exhibit 1012	U.S. Patent No. 6,887,339 to Goodman, et al. (“Goodman”)
Exhibit 1013	File history of Application No. 16/665,778 (“the ‘778 Application”)
Exhibit 1014	File history of Application No. 15/637,271 (“the ‘271 Application”)
Exhibit 1015	<i>Daihen Corporation v Reno Technologies, Inc.</i> , IPR2019-002248, Paper No. 11, Decision Denying Inter Partes Review (May 22, 2019)
Exhibit 1016	Declaration of Denis Shaw
Exhibit 1017	<i>Daihen Corporation v Reno Technologies, Inc.</i> , IPR2019-002248, Paper No. 8, Patent Owner’s Preliminary Response (March 13, 2019)
Exhibit 1018	Specification sheet for Analog Devices AD734, “10 MHz, Four-Quadrant Multiplier/Divider”
Exhibit 1019	Specification sheet for Analog Devices ADCMP556, “Dual Ultrafast Voltage Comparator”
Exhibit 1020	Specification sheet for Analog Devices AD9600, “10-Bit, 105 MSPS/125 MSPS/150 MSPS, 1.8 V Dual Analog-to-Digital Converter”
Exhibit 1021	Specification sheet for Analog Devices AD8302, “LF–2.7 GHz RF/IF Gain and Phase Detector”
Exhibit 1022	U.S. Patent No. 7,839,223 to Van Zyl, et al. (“Van Zyl”)

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