

Back to Basics: Impedance Matching (Part 2)

[Electronic Design](#)

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During impedance matching, a specific electronic load (R_L) is made to match a generator output impedance (R_g) for maximum power transfer. The need arises in virtually all electronic circuits, especially in RF circuit design.

[“Back to Basics: Impedance Matching \(Part 1\)”](#) discusses the use of a transformer as a basic way to match impedance. This article will introduce the L-network, which is a simple inductor-capacitor (LC) circuit that can be used to match a wide range of impedances in RF circuits.

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L-Network Applications And Configurations

The primary applications of L-networks involve impedance matching in RF circuits, transmitters, and receivers. L-networks are useful in matching one amplifier output to the input of a following stage. Another use is matching an antenna impedance to a transmitter output or a receiver input. Any RF circuit application covering a narrow frequency range is a candidate for an L-network.

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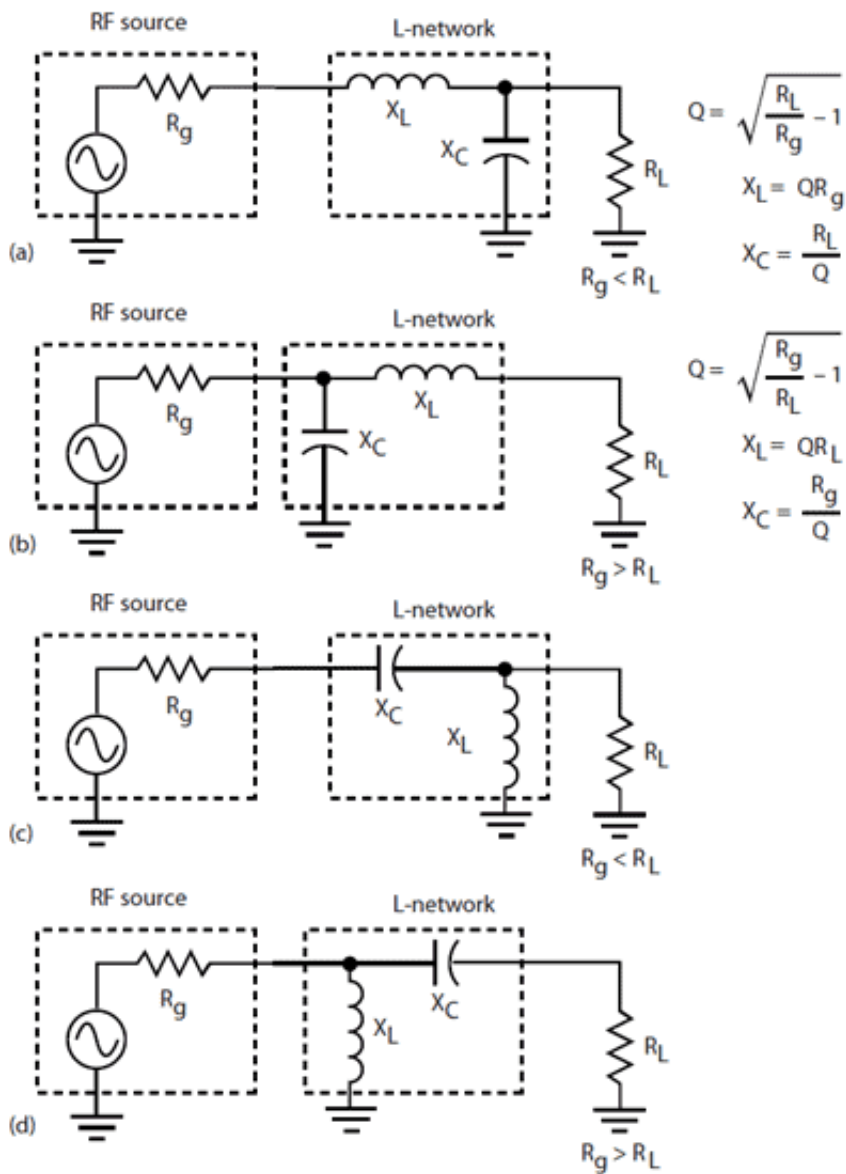
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There are four basic versions of the L-network, with two low-pass versions and two high-pass versions ([Fig. 7](#)). The low-pass versions are probably the most widely used since they attenuate harmonics, noise, and

relative sizes of the driving generator output impedance and load impedance.



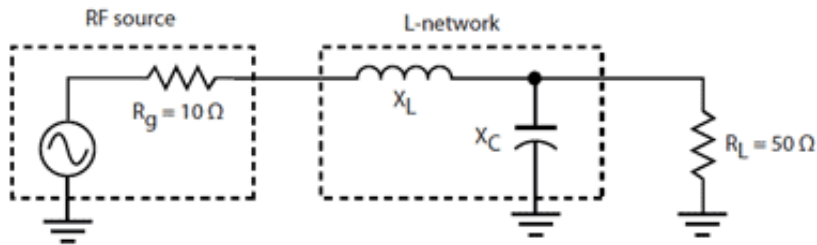
1. There are four basic L-network configurations. The network to be used depends on the relationship of the generator and load impedance values. Those in (a) and (b) are low-pass circuits, and those in (c) and (d) are high-pass versions.

The impedances that are being matched determine the Q of the circuit, which cannot be specified or controlled. If it is essential to control Q and bandwidth, a T or π -network is a better choice. These choices will be covered in a subsequent article.

While the L-network is very versatile, it may not fit every need. There are limits to the range of impedances that it can match. In some instances, the calculated values of inductance or capacitance may be too large or small to be practical for a given frequency range. This problem can sometimes be overcome by switching from a low-pass version to a high-pass version or vice versa.

Design Example #1

The goal is to match the output impedance of a low-power RF transistor amplifier to a 50-ohm load, and 50 Ω is a universal standard for most receiver, transmitter, and RF circuits. Most power amplifiers have a



2. The RF source is a transistor amplifier with an output impedance of $10\ \Omega$ that is to be matched to $50\text{-}\Omega$ output impedance load. The L-network with a parallel output capacitor is used.

[Figure 2](#) shows the desired circuit. Assume an amplifier output (generator) impedance of $10\ \Omega$ at a frequency of $76\ \text{MHz}$. Calculate the needed inductor and capacitor values using the formulas given in [Figure 1a](#):

$$Q = \sqrt{[(R_L/R_g) - 1]}$$

$$Q = \sqrt{[(50/10) - 1]} = \sqrt{[(5) - 1]} = \sqrt{4} = 2$$

$$X_L = QR_g = 2(10) = 20\ \Omega$$

$$L = X_L/2\pi f$$

$$L = 20/[2(3.14)(76 \times 10^6)]$$

$$L = 42\ \text{nH}$$

$$X_C = R_L/Q$$

$$X_C = 50/2 = 25\ \Omega$$

$$C = 1/2\pi f X_C$$

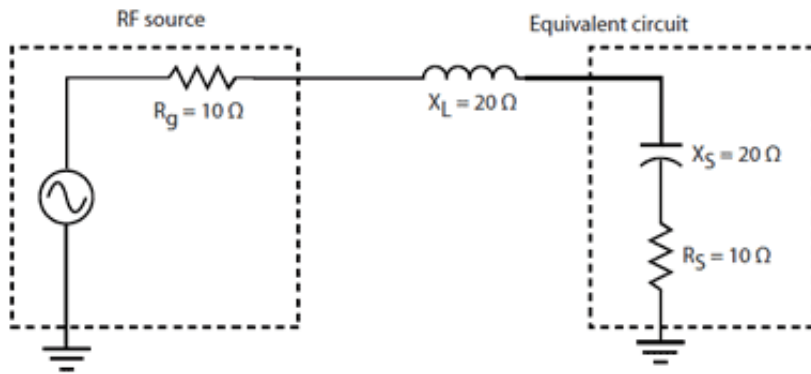
$$C = 1/[2(3.14)(76 \times 10^6)(25)]$$

$$C = 83.8\ \text{pF}$$

This solution omits any output impedance reactance such as transistor amplifier output capacitance or inductance and any load reactance that could be shunt capacitance or series inductance. When these factors are known, the computed values can be compensated.

The bandwidth (BW) of the circuit is relatively wide given the low Q of 2:

$$\text{BW} = f/Q = 76 \times 10^6/2 = 38 \times 10^6 = 38\ \text{MHz}$$



3. The equivalent circuit of the network in Figure 2 is a simple series RLC network where the reactances cancel and the source and load impedances match.

You can see how this matching network functions by converting the parallel combination of the 50-Ω resistive load and the 25-Ω capacitive reactance into its series equivalent ([Fig. 3](#)):

$$R_s = R_p / (Q^2 + 1)$$

$$R_s = 50 / (2^2 + 1) = 10 \Omega$$

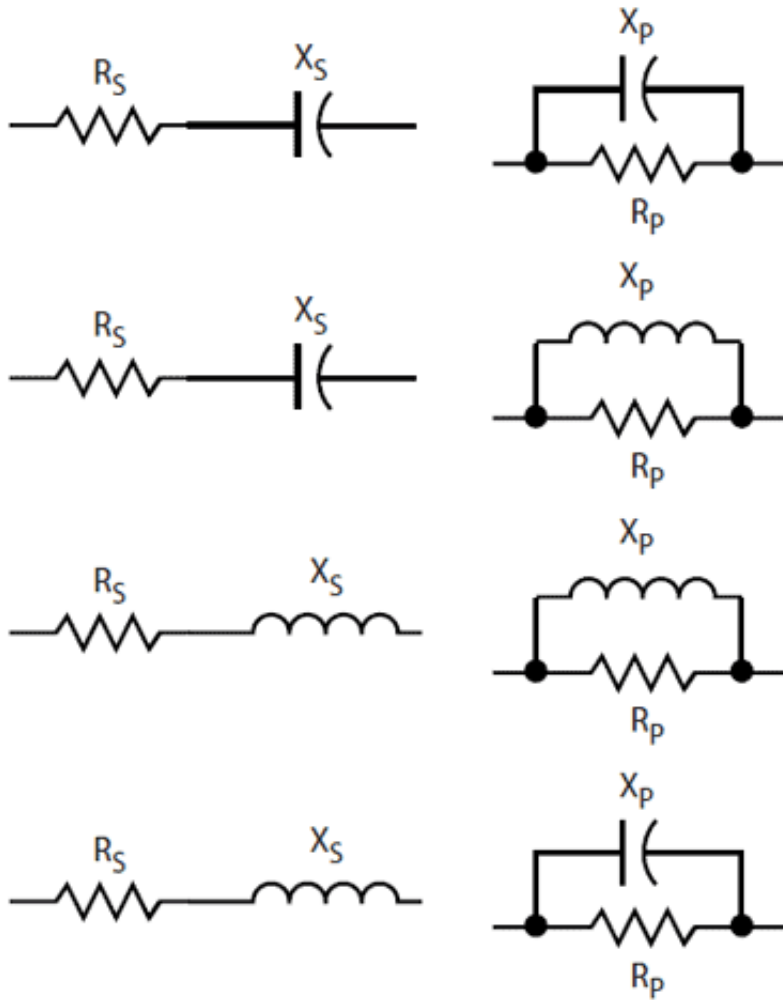
$$X_s = X_p / \sqrt{(Q^2 + 1)Q}$$

$$X_s = 25 / (5/4) = 25 / 1.25 = 20 \Omega$$

Note how the series equivalent capacitive reactance equals and cancels the series inductive reactance. Also the series equivalent load of 10 Ω matches the generator resistance for maximum power transfer.

Parallel And Series Circuit Equivalents

Sometimes it's necessary to convert a series RC or RL circuit into an equivalent parallel RC or RL circuit or vice versa. Such conversions are useful in RLC circuit analysis and design ([Fig. 4](#)).



4. These are all the possible practical series and parallel RC and RL circuit equivalents. The text provides the calculations for R_S , R_P , X_S , and X_P .

These equivalents also can help explain how the L-networks and other impedance-matching circuits work. The designations are:

R_S = series resistance

R_P = parallel resistance

X_S = series reactance

X_P = parallel reactance

The conversion formulas are:

$$R_S = R_P / (Q^2 + 1)$$

$$X_S = X_P / \sqrt{[Q^2 + 1]Q^2}$$

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