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705 S
Class
ISSUE CLASSIFICATION

U.S. UTILITY PATENT APPLICATION

 O.I.P.E. SCANNED <i>bw</i>	PATENT DATE JUN 25 2002
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SECTOR	CLASS <i>35</i>	SUBCLASS <i>400</i>	ART UNIT	EXAMINER <i>Hewitt</i>
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705	59	705	50	51	53
INTERNATIONAL CLASSIFICATION					
B06F 17/60					
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	Sheets Drwg. <i>2</i>	Figs. Drwg. <i>2</i>	Print Fig. <i>1</i>	Total Claims <i>19</i>	Print Claim for O.G. <i>18</i>
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	 (Assistant Examiner) <i>2/20/02</i> (Date)			NOTICE OF ALLOWANCE MAILED <i>3-28-02</i>	
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Issue Fee
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Formal Drawings (*2*) shts) set
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INITIALS _____

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SEARCH NOTES
(INCLUDING SEARCH STRATEGY)

Class	Sub.	Date	Exmr.
705	51	10/2/00	CHZ
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713	187	10/2/00	CHZ
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Date	Exmr.
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2/19/02	CHZ

Used
Web
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attempt
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to identify
relevant and
protect.

Searched
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INTERFERENCE SEARCHED

Class	Sub.	Date	Exmr.

(RIGHT OUTSIDE)



US006411941B1

(12) **United States Patent**
Mullor et al.

(10) **Patent No.:** **US 6,411,941 B1**
(45) **Date of Patent:** **Jun. 25, 2002**

- (54) **METHOD OF RESTRICTING SOFTWARE OPERATION WITHIN A LICENSE LIMITATION**
- (75) Inventors: **Miki Mullor; Julian Valiko**, both of Ramat Hasharon (IL)
- (73) Assignee: **Beeble, Inc.**, Newport Beach, CA (US)
- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.
- (21) Appl. No.: **09/164,777**
- (22) Filed: **Oct. 1, 1998**
- (30) **Foreign Application Priority Data**
May 21, 1998 (IL) 124571
- (51) **Int. Cl.**⁷ **G06F 17/60**
- (52) **U.S. Cl.** **705/59; 705/50; 705/51; 705/53; 705/57**
- (58) **Field of Search** **705/51, 54, 56, 705/57, 58, 59, 1, 50, 52, 53; 713/187, 189, 200**

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Primary Examiner—Hyung-Sub Souh
Assistant Examiner—Calvin L Hewitt
(74) *Attorney, Agent, or Firm*—Venable; Robert Kinberg; Jeffri A. Kaminski

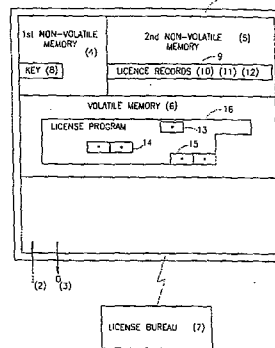
(57) **ABSTRACT**

A method of restricting software operation within a license limitation that is applicable for a computer having a first non-volatile memory area, a second non-volatile memory area, and a volatile memory area. The method includes the steps of selecting a program residing in the volatile memory, setting up a verification structure in the non-volatile memories, verifying the program using the structure, and acting on the program according to the verification.

19 Claims, 2 Drawing Sheets

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