



United States Patent [19]

Koziuk et al.

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[45] Date of Patent: **May 2, 2000**

- [54] **METHOD AND APPARATUS FOR MANAGING POWER CONSUMPTION OF A DIGITIZING PANEL**
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- [73] Assignee: **LSI Logic Corporation**, Milpitas, Calif.

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- [21] Appl. No.: **08/902,146**
- [22] Filed: **Jul. 29, 1997**
- [51] Int. Cl.⁷ **G06F 1/32**
- [52] U.S. Cl. **713/320; 713/323; 713/340; 345/173**
- [58] Field of Search **713/320, 323, 713/321, 340; 345/173**

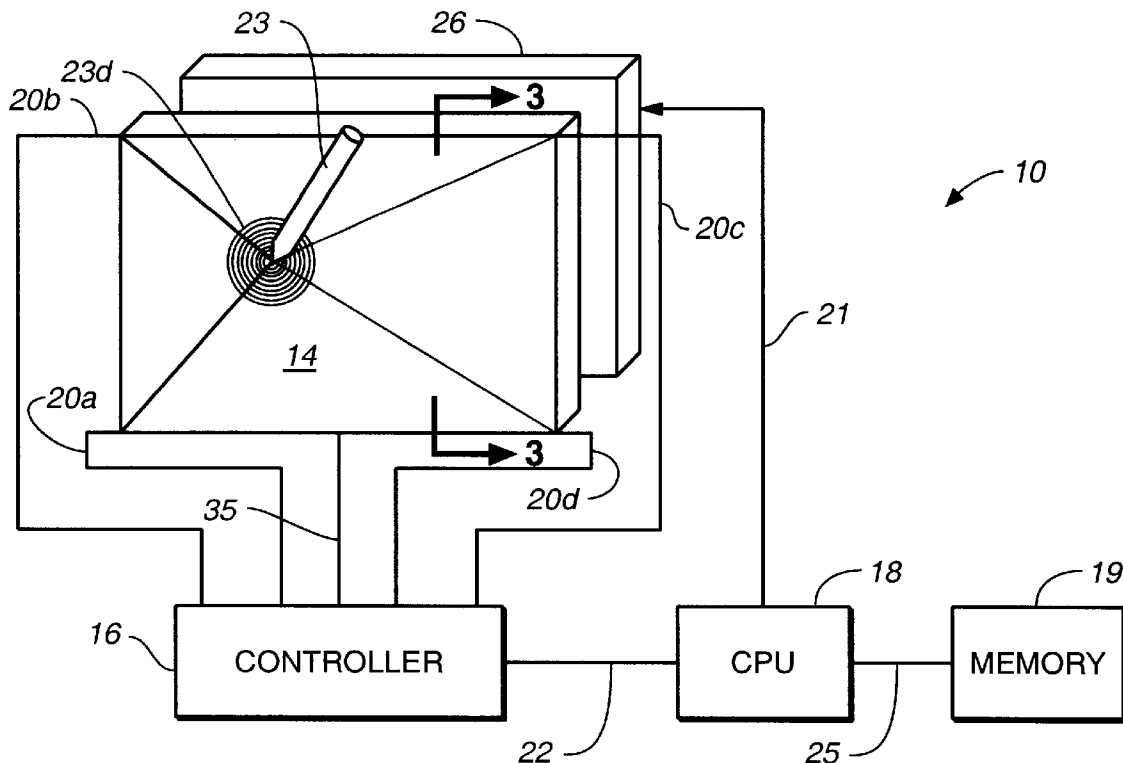
Primary Examiner—Xuan M. Thai

[57] ABSTRACT

A method for managing power consumption of a digitizing panel includes the steps of: (a) applying a biasing voltage to the digitizing panel for a first period of time and ceasing to apply the biasing voltage to the digitizing panel for a second period of time; (b) determining whether a user has touched the digitizing panel during the first period of time; and (c) if the user has touched the digitizing panel during the first period of time, then applying a biasing voltage to the digitizing panel for a third period of time that is longer in duration than the first period of time and ceasing to apply the biasing voltage to the digitizing panel for a fourth period of time. An apparatus for implementing the method is also disclosed.

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25 Claims, 5 Drawing Sheets



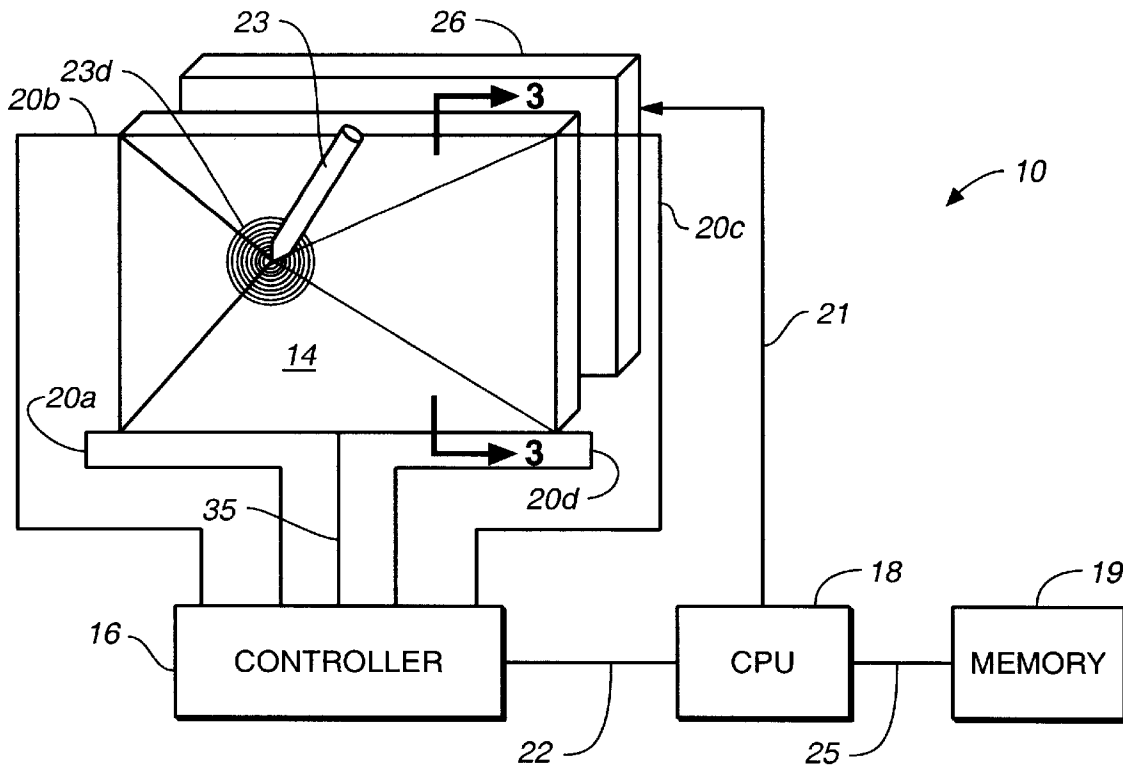


FIG. 1

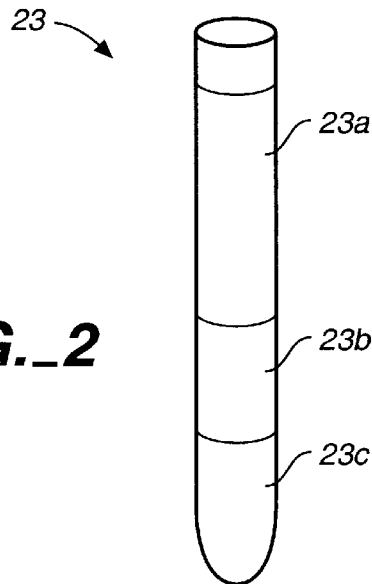


FIG. 2

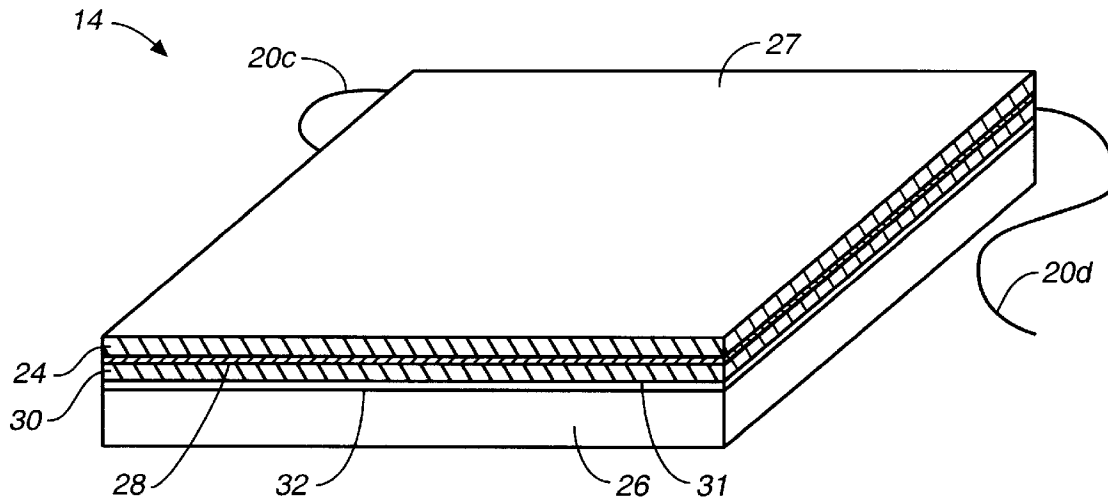


FIG. 3

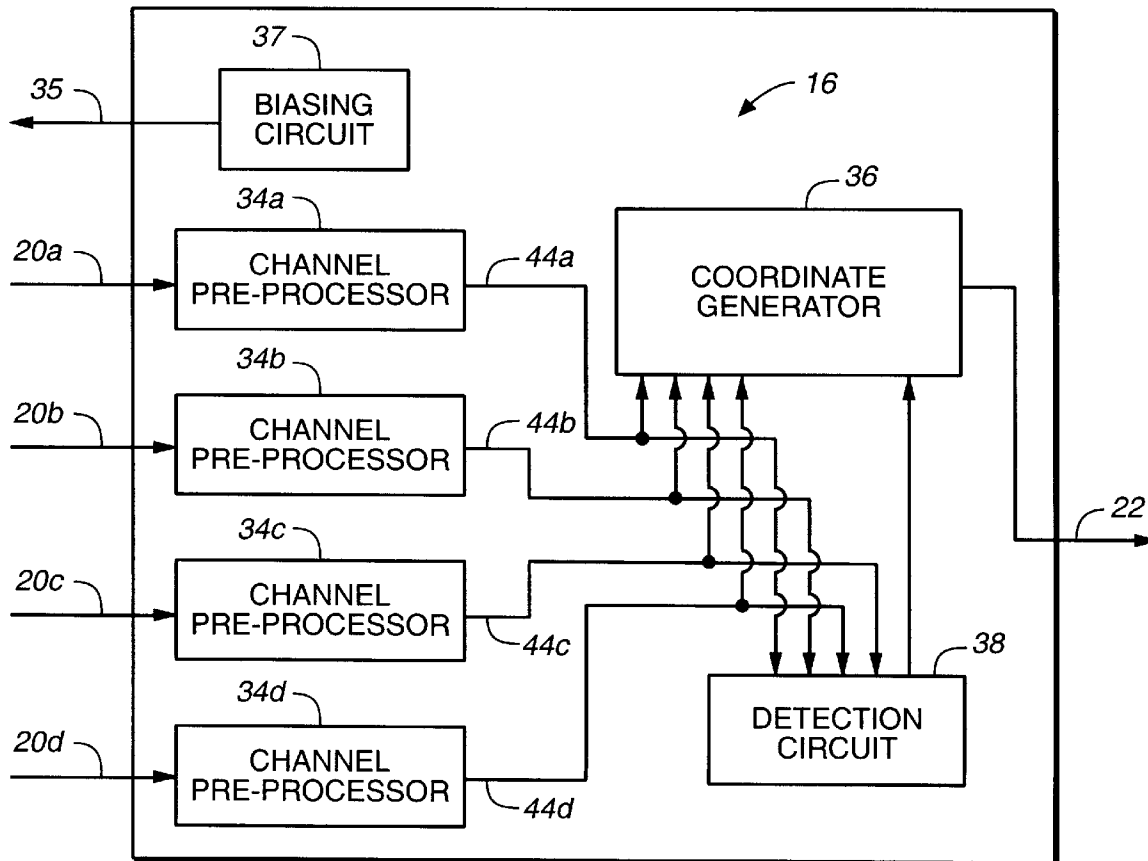


FIG. 4

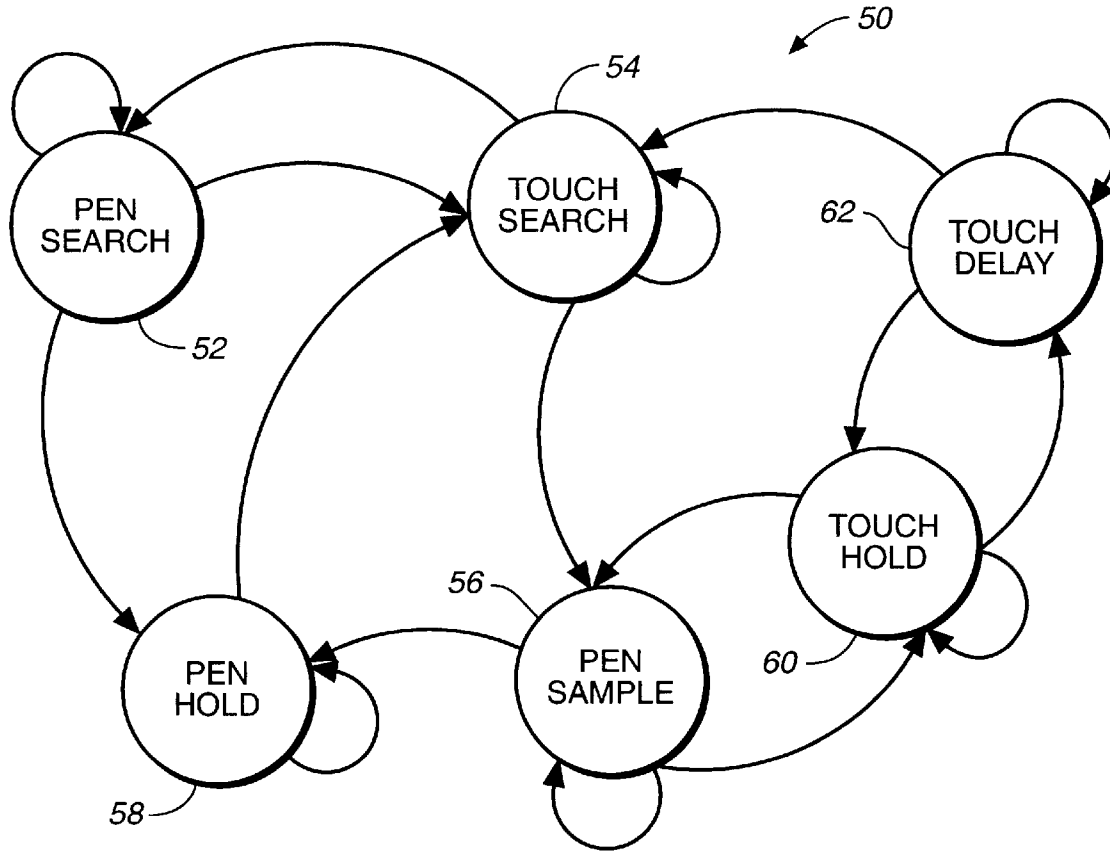


FIG._5

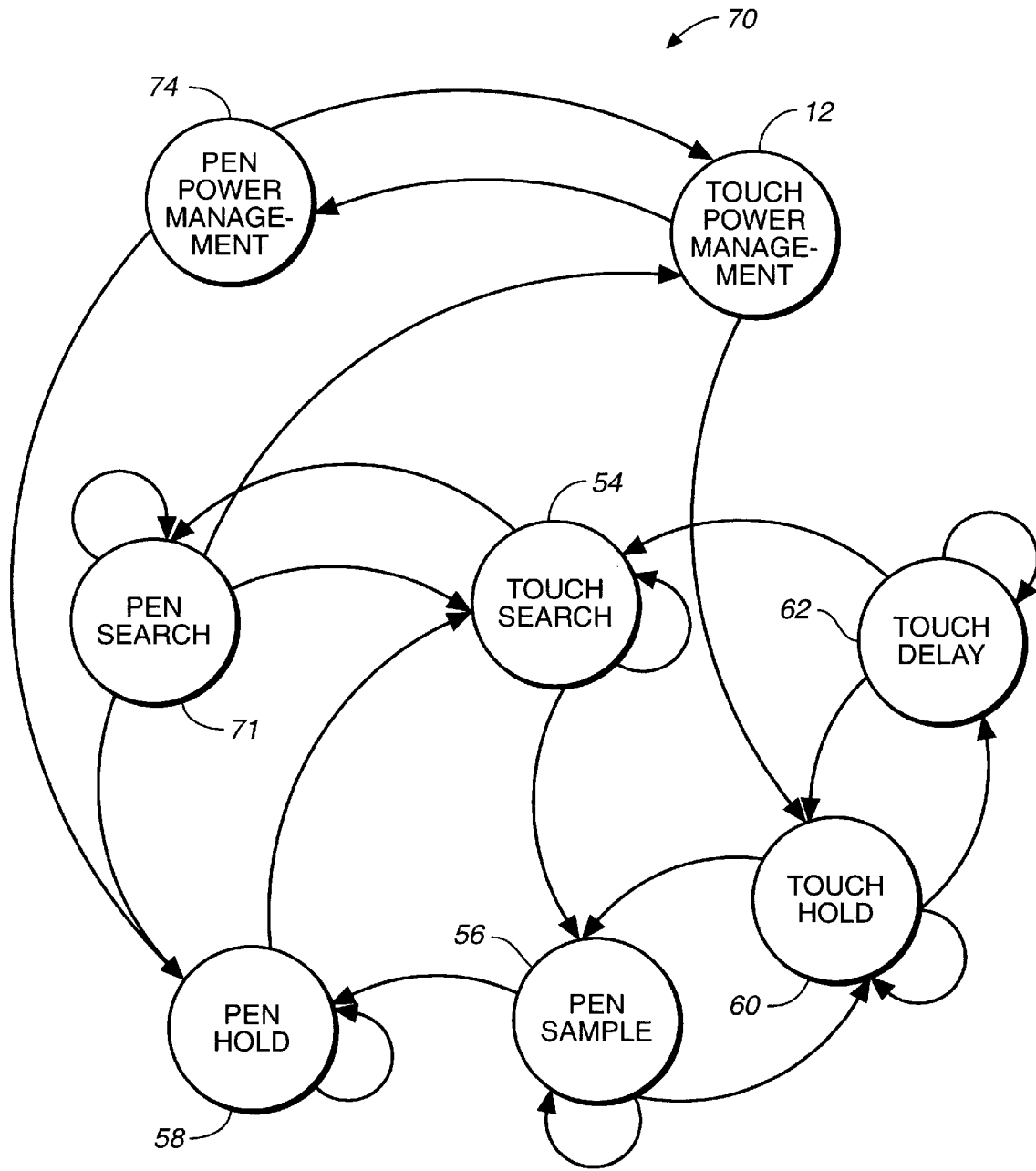


FIG._6

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