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Hui et al.

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(54) **PLANAR PRINTED CIRCUIT-BOARD TRANSFORMERS WITH EFFECTIVE ELECTROMAGNETIC INTERFERENCE (EMI) SHIELDING**

(75) Inventors: **Ron Shu Yuen Hui**, Shatin (HK); **Sai Chun Tang**, Yuen Long (HK)

(73) Assignee: **City University of Hong Kong**, Kowloon (HK)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 190 days.

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Related U.S. Application Data

(63) Continuation-in-part of application No. 09/883,145, filed on Jun. 15, 2001, now Pat. No. 6,501,364.

(51) **Int. Cl.**⁷ **H01F 5/00**

(52) **U.S. Cl.** **336/200; 336/232; 336/223**

(58) **Field of Search** **336/200, 223, 336/232; 29/602.1**

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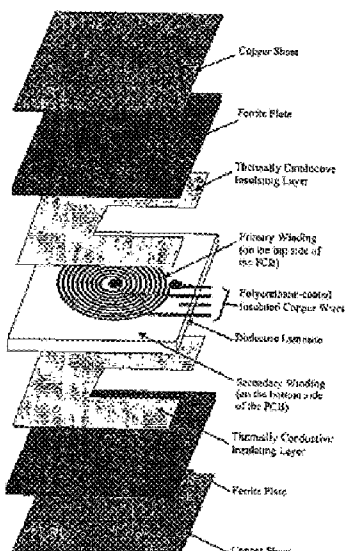
Primary Examiner—Elvin Enad
Assistant Examiner—Jennifer A. Poker

(74) *Attorney, Agent, or Firm*—Merchant & Gould P.C.

(57) **ABSTRACT**

Novel designs for printed circuit board transformers, and in particular for coreless printed circuit board transformers designed for operation in power transfer applications, in which shielding is provided by a combination of ferrite plates and thin conductive sheets.

7 Claims, 8 Drawing Sheets



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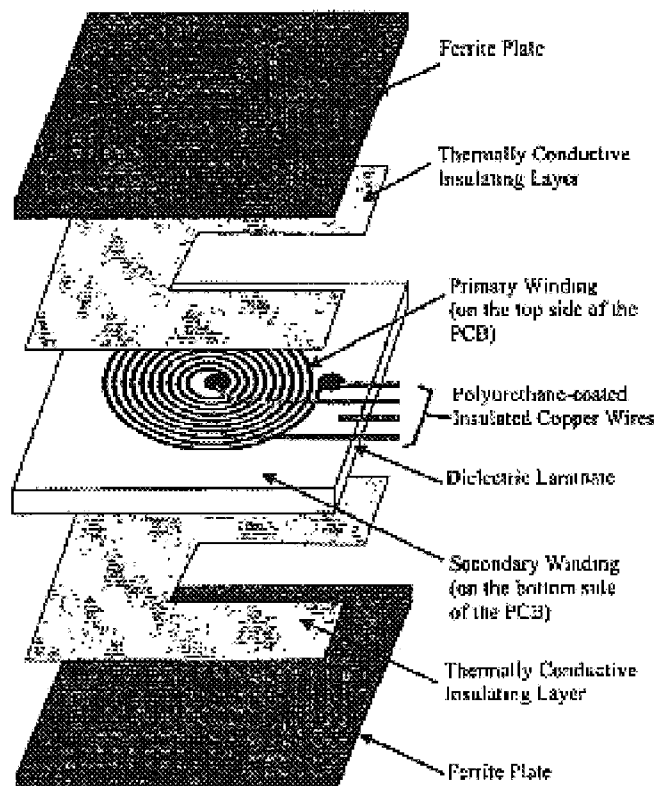


Fig. 1.

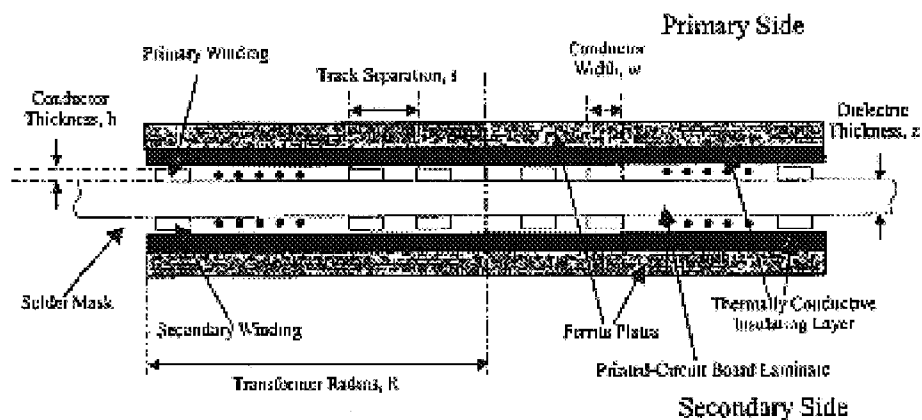


Fig. 2.

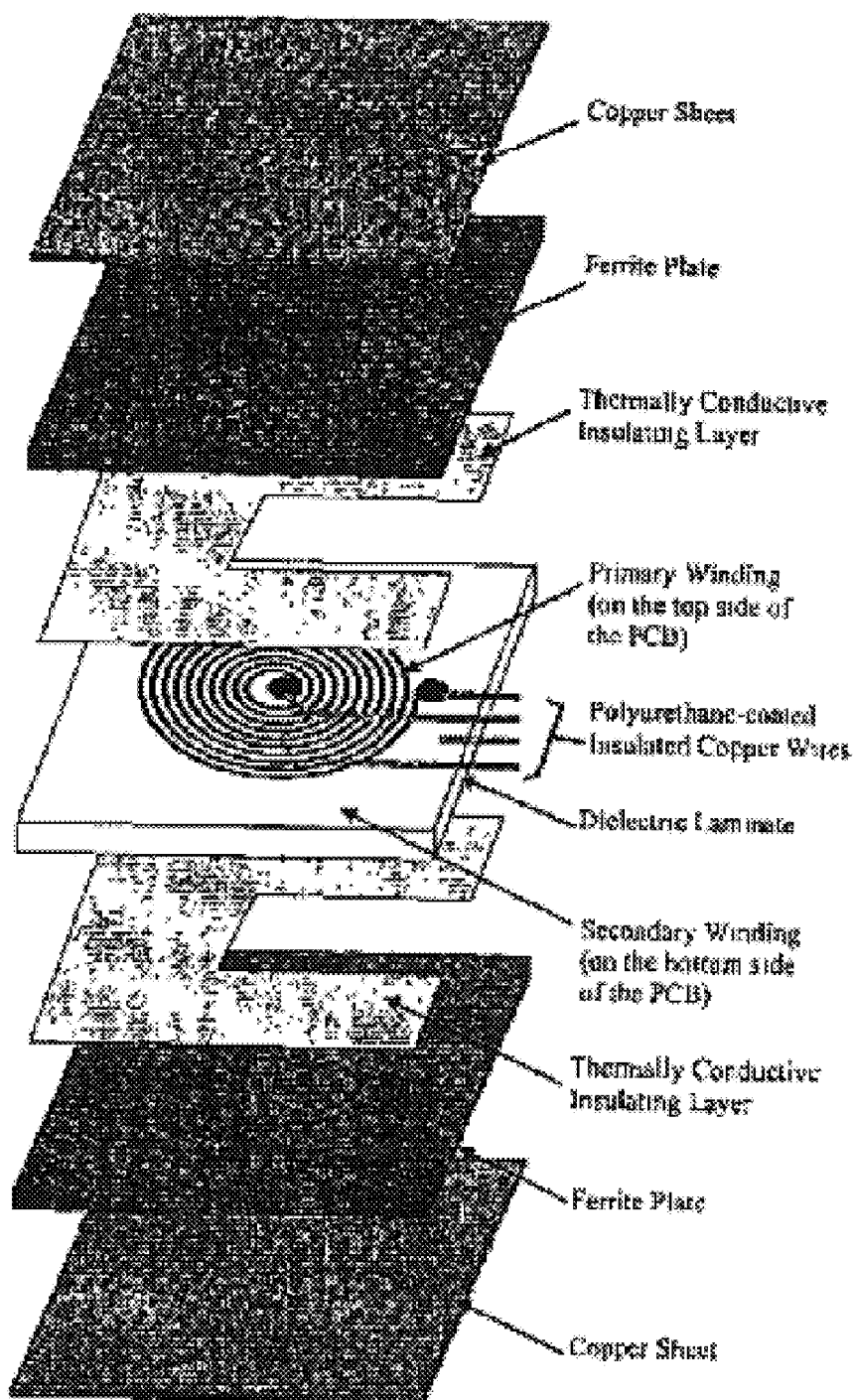


Fig. 3a.

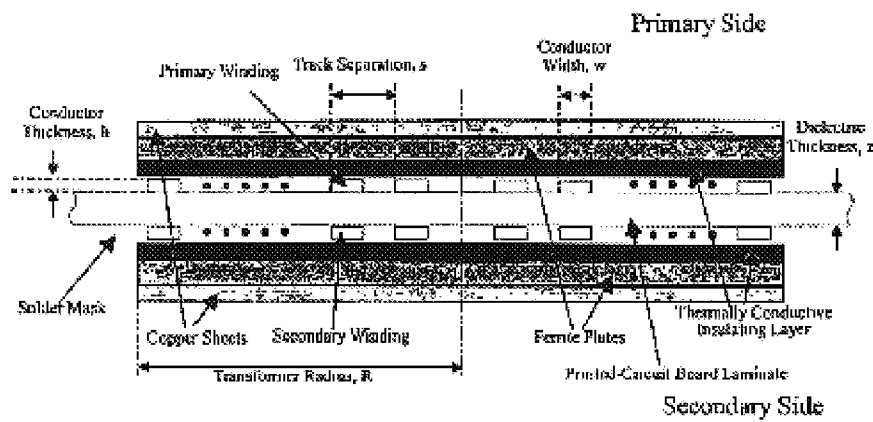


Fig. 3b.

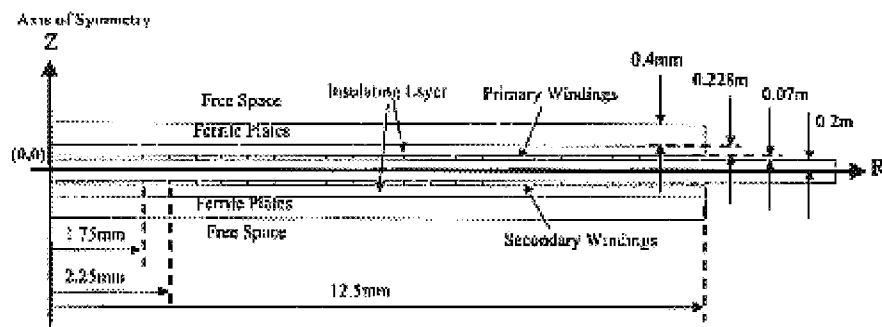


Fig. 4.

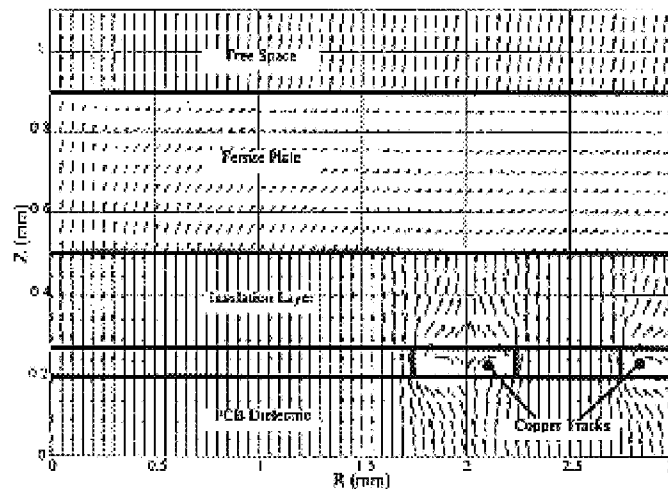


Fig. 5.

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