UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE PATENT TRIAL AND APPEAL BOARD
TIANMA MICROELECTRONICS CO. LTD., Petitioner,
V.
JAPAN DISPLAY INC. and PANASONIC LIQUID CRYSTAL DISPLAY CO., LTD., Patent Owner.

Case No. IPR2021-01061 U.S. Patent No. 10,423,034

DECLARATION OF DR. BRUCE W. SMITH IN SUPPORT OF PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 10,423,034



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Declaration of Dr. Bruce Smith - IPR2021-01061 U.S. Patent No. 10,423,034

I. <u>Introduction</u>

- 1. I, Dr. Bruce W. Smith, submit this declaration to state my opinions on the matter described below.
- 2. I have been retained by Petitioner Tianma Microelectronics Co. Ltd., ("Tianma" or "Petitioner"), as an independent expert in this proceeding before the United States Patent and Trademark Office. Although I am being compensated at my usual and customary rate of \$500 per hour, no part of my compensation depends on the outcome of this proceeding, and I have no other interest in this proceeding.
- 3. I understand that this proceeding involves U.S. Patent No. 10,423,034 (the "'034 patent"), and I have been asked to provide my opinions as to the patentability of the claims of the '034 patent. I understand that the application for the '034 patent was filed on June 27, 2018, and claims priority to a foreign application having a filing date of February 26, 2008.
- 4. I have been asked to consider the validity of certain claims of the '034 patent based on certain prior art references. I have also been asked to consider the state of the art and prior art available as of February 26, 2008. Based on the prior art discussed in this declaration, it is my opinion that claims 1-4 and 6-8 of the '034 patent are unpatentable for the reasons provided below.



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II. QUALIFICATIONS AND BACKGROUND

- 5. I believe that I am well qualified to serve as a technical expert in this matter based upon my over 30 years of research, academic, industry, and consulting engineering experience in IC (integrated circuit) processing, semiconductor device materials, microelectronics, microlithography, imaging science, thin film transistor (TFT) processing, and LCD-TFT flat panel display technology.
- 6. I am a professor of Microelectronic Engineering and the Director of the Ph.D. program in Microsystems Engineering at the Rochester Institute of Technology (RIT).
- 7. I am a Fellow of the Institute of Electrical and Electronics Engineers (IEEE), a Fellow of the International Society for Optical Engineering (SPIE), and a Fellow of the Optical Society of America (OSA). I have received numerous awards including the IEEE Technology Innovation Award, the RIT Trustees Scholarship Award, the SPIE Research Mentoring Award, the RIT Creators Award, and the Rush Henrietta Outstanding Alumni Award, among others, and have been inducted into RIT's Innovator Hall of Fame.
- 8. I have published over 200 technical papers, articles, textbooks, and textbook chapters, and I hold 27 patents in the fields of microelectronics and semiconductor device process technology. My patented technologies have been licensed both nationally and internationally.



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