

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 190131/US	APPLICATION NO. Not Yet Assigned
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANT(S) Ronald A. Oliver et al.	
		FILING DATE Concurrently Herewith	GROUP ART UNIT Not Yet Assigned

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA	7,167,090 B1	01/23/07	Mandal et al.	340	538.14	
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						

**FOREIGN PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
AK							
AL							
AM							
AN							
AO							

**OTHER PRIOR ART** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AP	
AQ	
AR	

EXAMINER	DATE CONSIDERED
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\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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Table with columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO., EXAMINER, ART UNIT, PAPER NUMBER, NOTIFICATION DATE, DELIVERY MODE. Includes details for application 12/042,117 and examiner OBINIYI, PAUL.

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

- docteting@turkiplaw.com
carlt@turkiplaw.com
kathyk@turkiplaw.com

<b>Office Action Summary</b>	<b>Application No.</b> 12/042,117	<b>Applicant(s)</b> OLIVER ET AL.	
	<b>Examiner</b> PAUL OBINIYI	<b>Art Unit</b> 2612	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1)  Responsive to communication(s) filed on \_\_\_\_\_.
- 2a)  This action is **FINAL**.
- 2b)  This action is non-final.
- 3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4)  Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5)  Claim(s) \_\_\_\_\_ is/are allowed.
- 6)  Claim(s) 1-25 is/are rejected.
- 7)  Claim(s) \_\_\_\_\_ is/are objected to.
- 8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9)  The specification is objected to by the Examiner.
- 10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
    - a)  All
    - b)  Some
    - c)  None of:
      - 1.  Certified copies of the priority documents have been received.
      - 2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
      - 3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date: _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date: _____ | 6) <input type="checkbox"/> Other: _____  |



## DETAILED ACTION

### *Claim Rejections - 35 USC § 103*

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1, 2, 12, 13, 14, 15, 16, 17, 18, & 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Smith et al (2005/0162145), (herein after, Smith), in view of Watanabe et al (2004/0061544), (herein after, Watanabe).

As to claim 1, Smith teaches power rectifier for a Radio Frequency Identification tag circuit, comprising[0031 shows a RF-DC converter (104/106) that rectifies the RF signal received at an input node (302), 0008 shows a RF data communication device]: a first antenna input node configured to receive a first phase of a wirelessly received alternating RF signal[0023 teaches RFID chip having the first pad (130) operatively connected to the first antenna (100) signal (RF1). 0034 further teaches positive and negative RF cycles]; a second antenna input node configured to receive a second phase of the wirelessly received alternating RF signal, and which is substantially opposite to the first phase;[0023 teaches RFID chip having the first pad (130) and a

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second pad (132) operatively connected to the first antenna (100) and a second antenna (102) for receiving RF signal RF1 and RF2 respectively] a plurality of serially coupled stages, at least one of the stages [0032 teaches a multiple stages 300(1), 300(2), .....300(N-1)] including a first synchronous element [including synchronous element such as transistor, see Fig.4] with a first beginning coupled to receive the second phase and a first ending, [Fig. 4 shows the end of (406) connected with the beginning of (408) and the output of 300(1) connected to the input of 300(2) ] the first synchronous element including: a first transistor having an input terminal at the first beginning, an output terminal, and a first gate coupled to receive the first phase; [ Fig. 4 shows a MOS transistor (406) having an input terminal, an output terminal, and a gate couples to the RF input] and a second transistor having an input terminal, an output terminal at the first ending, and a second gate coupled to receive the second phase, [Fig. 4 show a MOS transistor (408) having an output terminal connected to the input terminal of the first transistor] in which the input terminal of the second transistor is connected to the output terminal of the first transistor at a first intermediate node [0029 shows a transistor (110) configured with a gate connected to the CTRL node, a drain tied to the RF1 node, and a source connected to the VSS node, while the n-channel MOS transistor 112 is configured with a gate connected to the CTRL node, a drain tied to the RF2 node, and a source connected to the VSS node. 0029 further shows that when asserted, the n-channel MOS transistors 110 and 112 will attenuate the incoming RF signals RF1 and RF2,] so as to form a first charge-accumulating path between the beginning and the ending, and there is no charge-accumulating path between the

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