



(12) **United States Patent**
Dawson et al.

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(45) **Date of Patent:** **Feb. 19, 2002**

- (54) **LINE SCANNING CIRCUIT FOR A DUAL-MODE DISPLAY**
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- (73) Assignee: **Sarnoff Corporation**, Princeton, NJ (US)
- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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- (21) Appl. No.: **09/384,063**
- (22) Filed: **Aug. 26, 1999**

Related U.S. Application Data

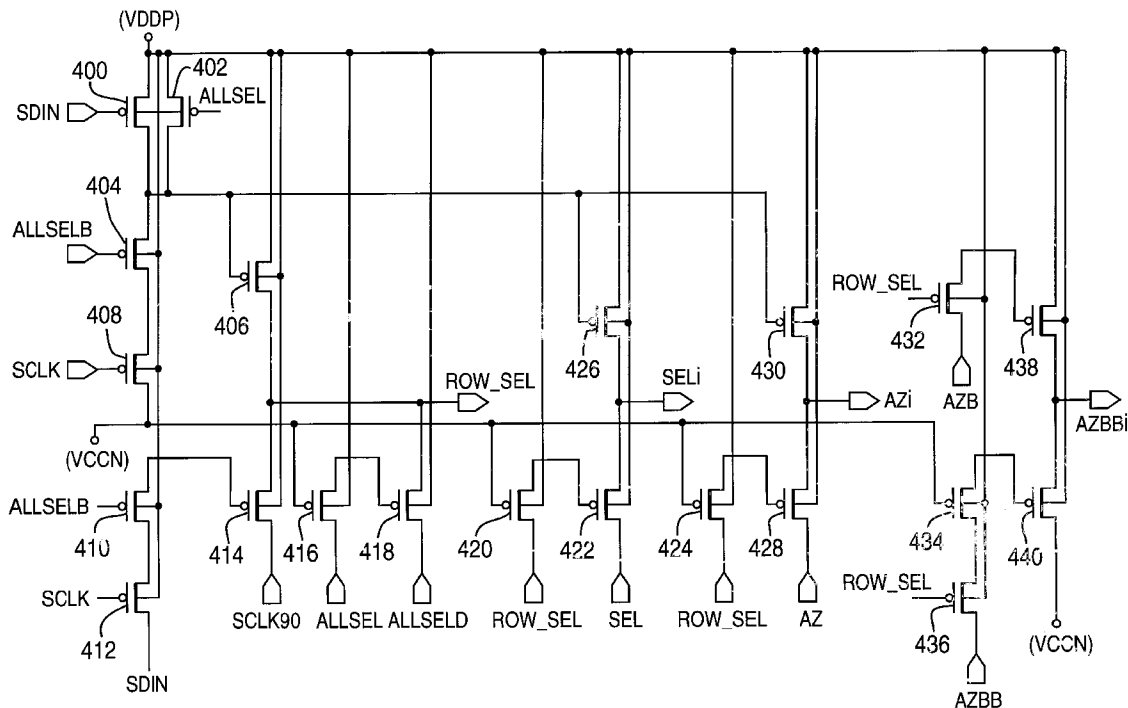
- (60) Provisional application No. 60/099,019, filed on Sep. 3, 1998.
- (51) **Int. Cl.⁷** **G09G 3/32**
- (52) **U.S. Cl.** **345/82; 345/92; 345/98; 345/100**
- (58) **Field of Search** 345/82, 55, 87, 345/94, 98, 99, 100, 206, 208, 90, 92

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11 Claims, 6 Drawing Sheets



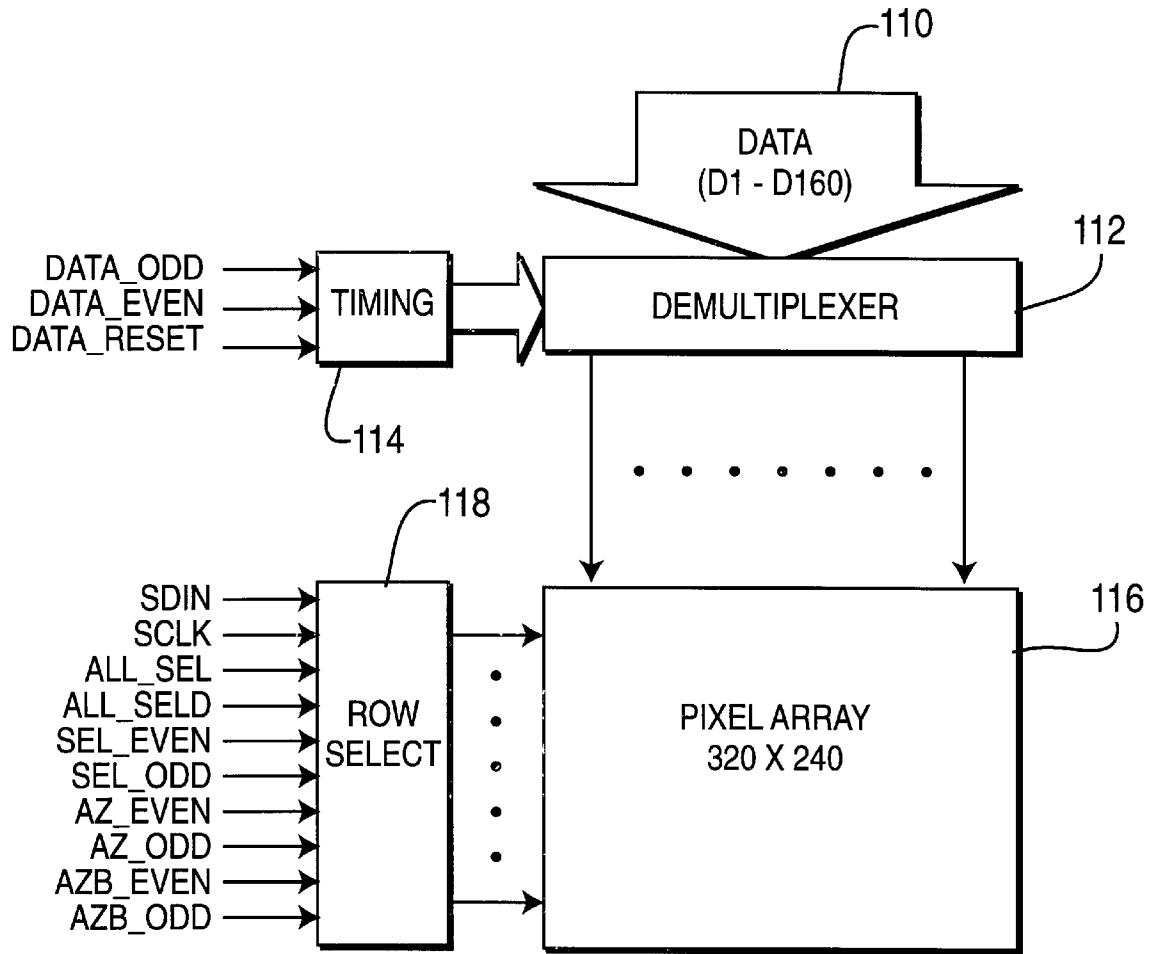


FIG. 1

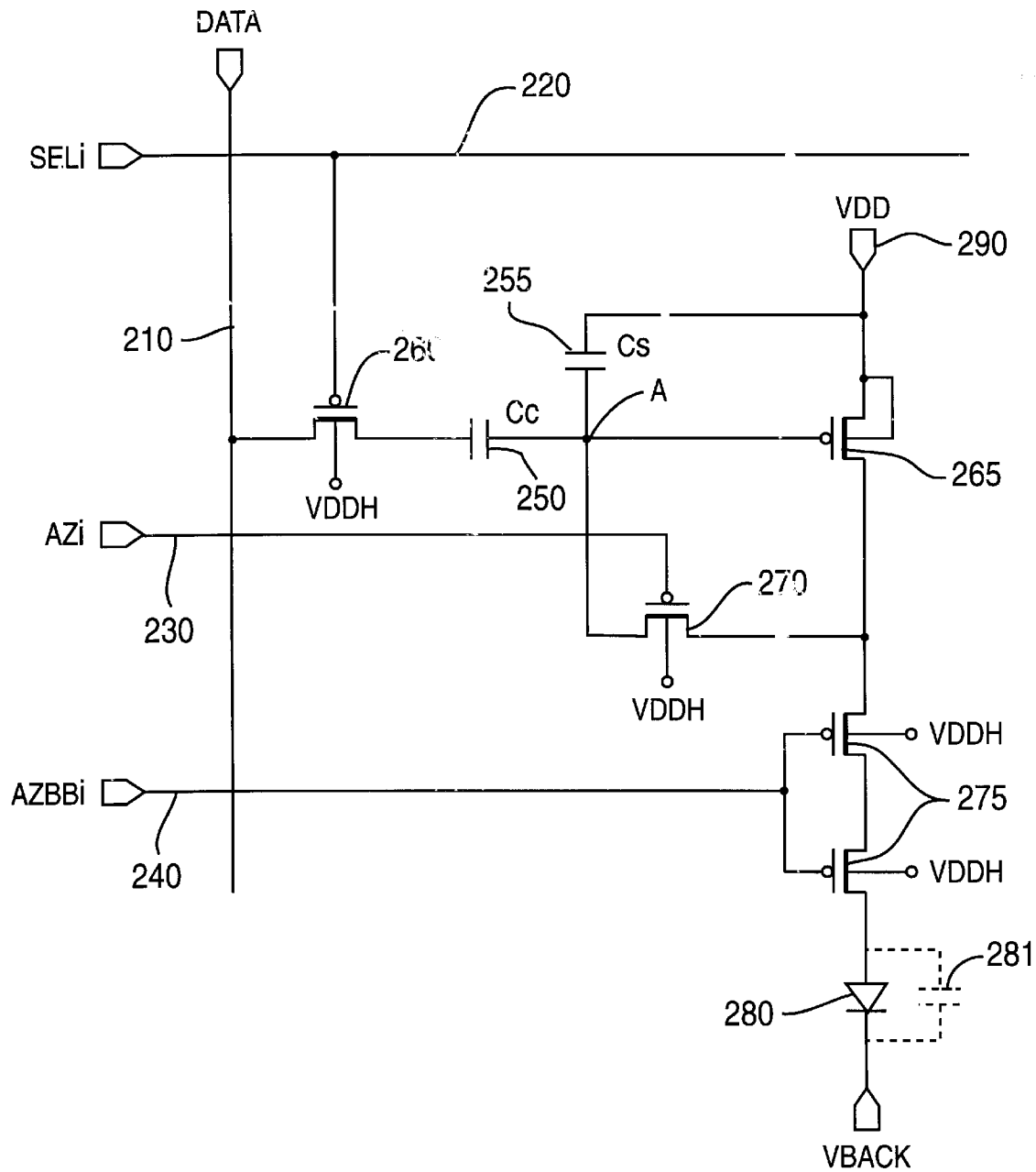


FIG. 2

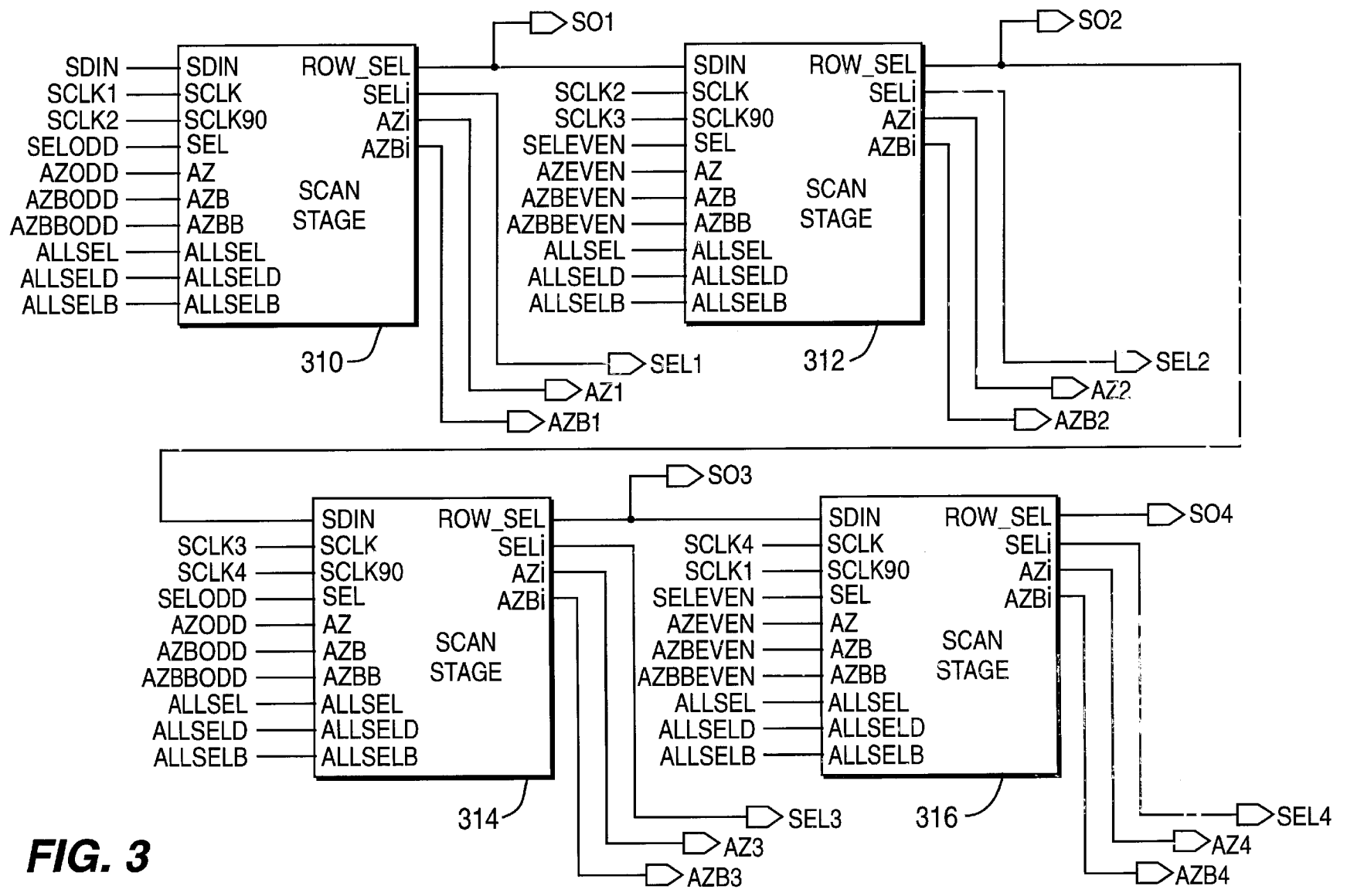


FIG. 3

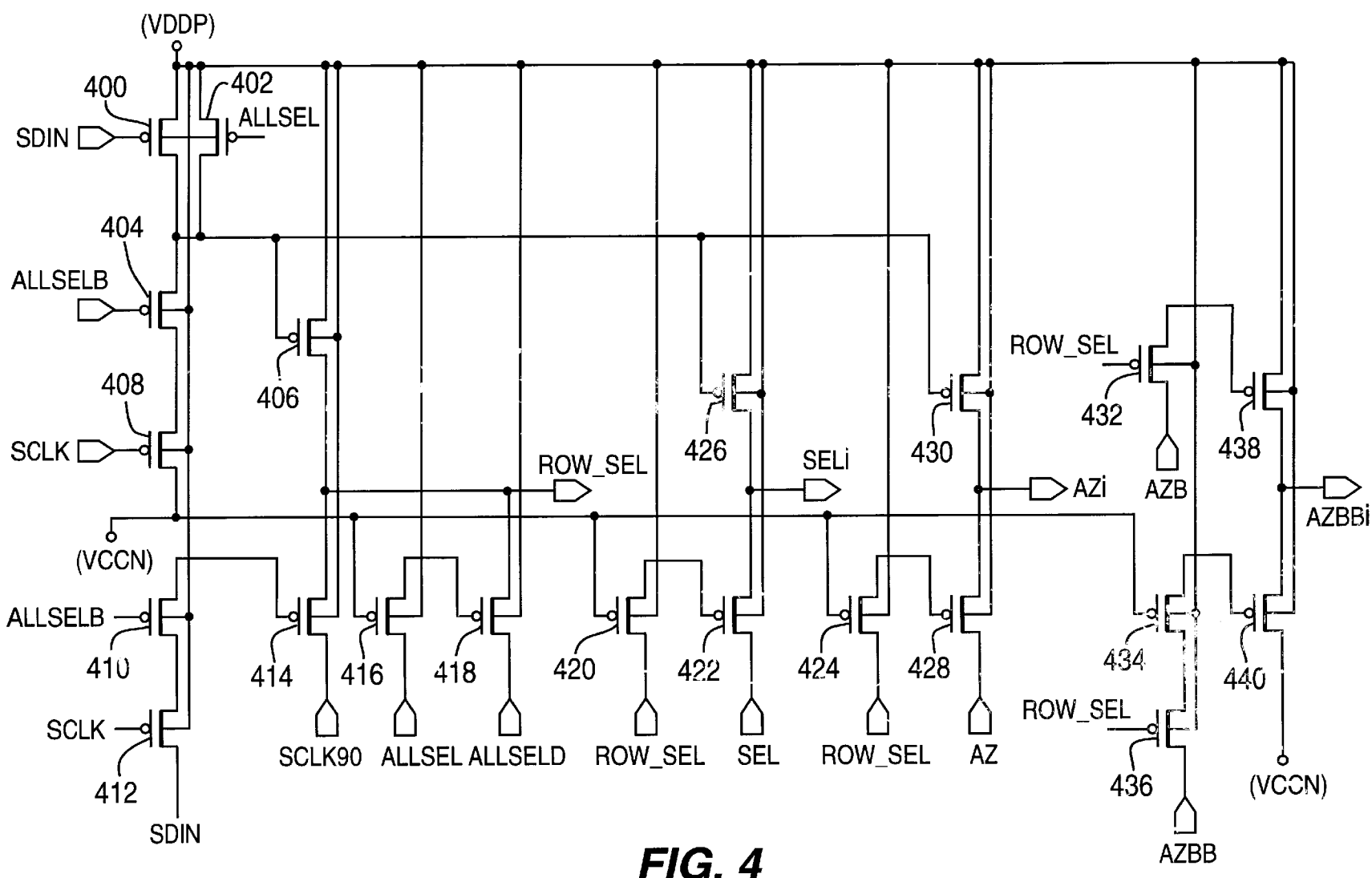


FIG. 4

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