

PLAINTIFF’S PROPOSED CLAIM TERMS FOR CONSTRUCTION

<u>Term Number</u>	<u>Term</u>	<u>Proposed Construction</u>
1.	“initially resetting the pixel circuit” / “initially resets a gate terminal of the driving transistor” / “resetting . . . a second pixel”	<i>See Defendants’ Proposed Terms 6 and 7 below.</i>

DEFENDANTS’ PROPOSED CLAIM TERMS FOR CONSTRUCTION

<u>Term Number</u>	<u>Term</u>	<u>Proposed Construction</u>
1.	“a gate driver circuit which includes a first gate driver circuit, a second gate driver circuit...” / “a gate driver circuit which includes a first gate driver circuit and a second gate driver circuit...” / “the gate driver circuit includes a first gate driver circuit and a second gate driver circuit” / “a gate driver circuit including a first gate driver circuit...and a second gate driver circuit...” (’130, cls. 1, 13; ’597, cls. 1, 16; ’108, cls. 1, 16; ’336, cls. 10, 18; ’992, cls. 1, 8, 10)	plain meaning, no construction necessary

<u>Term Number</u>	<u>Term</u>	<u>Proposed Construction</u>
2.	<p>“wherein the first switch transistor and the second switch transistor are independently on/off controlled by the first gate driver circuit and the second gate driver circuit” /</p> <p>“the first gate driver circuit and second gate driver circuit control the first gate signal line and the second gate signal line independently” /</p> <p>“the first switching transistor and the second switching transistor are independently on/off controlled by the first gate driver circuit and the second gate driver circuit” /</p> <p>“independently on/off controlling the first switching transistor and the second switching transistor by the first gate driver circuit and the second gate driver circuit” /</p> <p>“wherein, by the first gate driver circuit and the second gate driver circuit, the first switch transistor is independently on/off controlled from the second switch transistor and the third switch transistor”</p> <p>(’130, cls. 1, 13; ’597, cl. 4; ’108, cl. 4; ’336, cls. 11, 18)</p>	plain meaning, no construction necessary

<u>Term Number</u>	<u>Term</u>	<u>Proposed Construction</u>
3.	<p>“the first gate driver circuit is configured to select the plurality of first gate signal lines as a block simultaneously” / “selecting, by the first gate driver circuit, the plurality of first gate signal lines as a block simultaneously” / “selecting the plurality of first gate signal lines via the first gate driver circuit as a block simultaneously”</p> <p>(’108, cls. 6, 20; ’992, cls. 6, 8, 14)</p>	plain meaning, no construction necessary
4.	<p>“the source driver circuit [is/being] provided as a semiconductor chip and [is/being] attached to the EL display apparatus”</p> <p>(’130, cls. 1, 13)</p>	plain meaning, no construction necessary
5.	<p>“the second gate driver circuit is arranged at a second side of the display screen”</p> <p>(’130, cl. 4)</p>	plain meaning, no construction necessary
6.	<p>“initially resetting the pixel circuit” / “initially resets the pixel circuit” / “resetting...[a/the] second pixel”</p> <p>(’597, cls. 1, 16, 17; ’108, cls. 1, 16, 17)</p>	<p>“setting the pixel circuit to a predetermined state to render the pixel ready for programming”</p> <p>“sets the pixel circuit to a predetermined state to render the pixel ready for programming”</p> <p>“setting . . . [a/the] second pixel circuit to a predetermined state to render the second pixel ready for programming”</p>
7.	<p>“initially resets [a/the] gate terminal of the driving transistor” / “a gate terminal of the driving transistor is initially reset” / “[a/the] gate terminal of the driving transistor is reset”</p> <p>(’597, cls. 1, 6, 16, 17; ’108, cls. 7, 9, 17-19)</p>	<p>“sets [a/the] gate terminal of the driving transistor to a predetermined state to render the pixel ready for programming”</p> <p>“a gate terminal of the driving transistor is set to a predetermined state to render the pixel ready for programming”</p> <p>“[a/the] gate terminal of the driving transistor is set to a predetermined state to render the pixel ready for programming”</p>

<u>Term Number</u>	<u>Term</u>	<u>Proposed Construction</u>
8.	<p>“when the third switch transistor initially resets the gate terminal of the driving transistor” / “when the third switch transistor initially resets the pixel circuit”</p> <p>(’597, cls. 1, 16; ’108, cls. 1, 16)</p>	<p>“when the third switch transistor sets the gate terminal of the driving transistor to a predetermined state to render the pixel ready for programming” “when the third switch transistor sets the pixel circuit to a predetermined state to render the pixel ready for programming”</p>
9.	<p>“the first switch transistor of the Nth row is controlled in an OFF state by the first gate driver circuit” / “the first switch transistor of the Nth pixel row is controlled in an OFF state by the first gate driver circuit”</p> <p>(’597, cl. 10; ’108, cl. 11)</p>	<p>plain meaning, no construction necessary</p>
10.	<p>“the gate driver circuit is configured to change a ratio of an area of the plurality of band-shaped non-display regions on the display screen to an area of the plurality of band-shaped display regions on the display screen depending on at least one of a brightness adjustment, a type of image data, or whether a display image is a motion image or a still image” / “the gate driver circuit is configured to change a number of divisions by which the display screen is divided into the plurality of band-shaped non-display regions and the plurality of band-shaped display regions depending on the type of image data” / “type of image data”</p> <p>(’336, cls. 1, 6, 19)</p>	<p>plain meaning, no construction necessary</p>

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