Appendix A

Patents and Published Patent Applications

Patent / Published Application	Issue / Pub. Date
U.S. Pat. 4,658,093 ("Hellman")	Apr. 14, 1987
U.S. Pat. 4,688,169 ("Joshi")	Aug. 18, 1987
U.S. Pat. 4,757,533 ("Allen")	July 12, 1988
U.S. Pat. 4,866,769 ("Karp")	Sep. 12, 1989
U.S. Pat. 5,103,476 ("Waite 476")	Apr. 7, 1992
U.S. Pat. 5,222,134 ("Waite 134")	June 22, 1993
U.S. Pat. 5,325,430 ("Smyth")	June 28, 1994
U.S. Pat. 5,371,876 ("Ewertz")	Dec. 6, 1994
U.S. Pat. 5,473,692 ("Davis 692")	Dec. 5, 1995
U.S. Pat. 5,490,216 ("Richardson")	Feb. 6, 1996
U.S. Pat. 5,509,070 ("Schull")	Apr. 16, 1996
U.S. Pat. 5,568,552 ("Davis 552")	Oct. 22, 1996
U.S. Pat. 5,579,522 ("Christeson")	Nov. 26, 1996
U.S. Pat. 5,666,411 ("McCarty")	Sep. 9, 1997
U.S. Pat. 5,734,819 ("Lewis")	Mar. 31, 1998
U.S. Pat. 5,745,568 ("O'Connor")	Apr. 28, 1998
U.S. Pat. 5,844,986 ("Davis 986")	Dec. 1, 1998
U.S. Pat. 5,892,902 ("Clark 902")	Apr. 6, 1999
U.S. Pat. 5,892,906 ("Chou")	Apr. 6, 1999
U.S. Pat. 5,901,311 ("Labatte 311")	May 4, 1999
U.S. Pat. 5,913,057 ("Labatte 057")	June 15, 1999
U.S. Pat. 5,940,504 ("Griswold")	Aug. 17, 1999
U.S. Pat. 5,944,820 ("Beelitz")	Aug. 31, 1999
U.S. Pat. 6,026,293 ("Osborn")	Feb. 15, 2000
U.S. Pat. 6,038,320 ("Miller")	Mar. 14, 2000
U.S. Pat. 6,049,670 ("Okada")	Apr. 11, 2000
U.S. Pat. 6,138,236 ("Mirov")	Oct. 24, 2000
U.S. Pat. 6,148,083 ("Fieres")	Nov. 14, 2000
U.S. Pat. 6,153,835 ("Schwartz")	Nov. 28, 2000
U.S. Pat. 6,185,678 ("Arbaugh")	Feb. 6, 2001
U.S. Pat. 6,189,146 ("Misra")	Feb. 13, 2001
U.S. Pat. 6,209,099 ("Saunders")	Mar. 27, 2001
U.S. Pat. 6,243,468 ("Pearce")	June 5, 2001
U.S. Pat. 6,269,392 ("Cotichini")	July 31, 2001
U.S. Pat. 6,523,119 ("Pavlin")	Feb. 18, 2003
U.S. Pat. 6,735,696 ("Hannah")	May 11, 2004
U.S. Pat. 5,802,592 ("Chess")	Sep. 1, 1998
U.S. Pat. 6,049,671 ("Slivka")	Apr. 11, 2000

Patent / Published Application	Issue / Pub. Date
U.S. Pat. 5,421,006 ("Jablon")	May 30, 1995
U.S. Pat. 5,748,084 ("Isikoff")	May 5, 1998
U.S. Pat. 6,009,524 ("Olarig")	Dec. 28, 1999
U.S. Pat. 4,757,534 ("Matyas")	July 12, 1988
U.S. Pat. 5,850,559 / EP0824233 ("Angelo 559")	Dec. 15, 1998
U.S. Pat. 5,949,882 ("Angelo 882")	Sep. 7, 1999
U.S. Pat. 5,222,135 ("Hardy")	June 22, 1993
U.S. Pat. 5,724,425 ("Chang")	Mar. 3, 1998
U.S. Pat. 5,835,594 ("Albrecht")	Nov. 10, 1998
U.S. Pat. 5,379,342 ("Arnold")	Jan. 3, 1995
U.S. Pat. 5,933,498 ("Schneck")	Aug. 3, 1999
U.S. Pat. 5,852,736/ WO97/36241 ("Shipman")	Oct. 2, 1997
EP0766165A2/ U.S. Pat. 5,935,243 ("Hasebe")	Apr. 2, 1997
U.S. Pat. 6,256,391/ JPH10301492 ("Ishiguro")	July 03, 2001
U.S. Pat. 5,010,571 / WO1988002202 ("Katznelson")	April 23, 1991
U.S. Pat. 5,832,083/ JP08-83205 ("Iwayama")	Nov. 03, 1998
JPH09-26875 ("Fujitsu")	Jan. 1, 1997
U.S. Pat. 5,892,900 ("Ginter")	Apr. 8, 1999
U.S. Pat. 6,009,520 ("Gharda")	Dec. 28, 1999
U.S. Pat. 5,657,445 ("Pearce 445")	Aug. 12, 1997
U.S. Pat. 4,908,861 ("Brachtl")	Mar. 13, 1990
U.S. Pat. 6,078,909 ("Knutson")	June 20, 2000
U.S. Pat. 5,944,821 ("Angelo 821")	Aug. 31, 1999
U.S. Patent 5,448,045 ("Clark 045")	Sep. 05, 1995

Other Printed Publications

Author	Title	Pub. Date
White et al. ("White")	ABYSS: A trusted Architecture for Software Protection, IEEE Transactions on Software Eng'g, Vol. 16, No. 6, pp. 38-51.	June 1990
White et al. ("White II")	ABYSS: A trusted Architecture for Software Protection, IEEE Transactions on Software Eng'g, Vol. 16, No. 6, pp. 619-629.	1990
Tygar et al. ("Tygar")	Dyad: A System for Using Physically Secure Coprocessors, CMU-CS-94-140R, Carnegie Mellon University.	May 1991
Yee ("Yee 1994")	Using Secure Coprocessors, CMU-CS-94-149, Carnegie Mellon University.	May 1994
Yee ("Yee 1994 II")	Using Secure Coprocessors, CMU-CS-94-149, Carnegie Mellon University.	1994

Author	Title	Pub. Date
Clark ("Clark")	BITS: A Smartcard Protected Operating System, Commc'ns of the ACM, Vol. 37, No. 11, pp. 66-70, 94.	Nov. 1994
Clark ("Clark")	BITS: A Smartcard Protected Operating System, Commc'ns of the ACM, Vol. 37, No. 11, pp. 66-70, 94.	1994
Yee et al. ("Yee 1995")	Secure Coprocessors in Electronic Commerce Applications, Proceedings of the 1st USENIX Workshop on Elec. Commerce, pp. 155-170.	July 1995
Yee et al. ("Yee 1995 II")	Secure Coprocessors in Electronic Commerce Applications, Proceedings of the 1st USENIX Workshop on Elec. Commerce.	1995
Arbaugh et al. ("Arbaugh 1996")	A Secure and Reliable Bootstrap Architecture, Dept. of Comput. & Info. Sci. Tech. Reports, University of Pennsylvania.	1996
Arbaugh et al. ("Arbaugh 1996 II")	A Secure and Reliable Bootstrap Architecture, Dept. of Comput. & Info. Sci. Tech. Reports, University of Pennsylvania.	1996
AMI et al. ("DMI BIOS Specification"/"DMI Specification")	Desktop Management BIOS Specification, Version 2.0.	Mar. 1996
Arbaugh et al. ("Arbaugh 1997")	A Secure and Reliable Bootstrap Architecture, SP '97 Proceedings of the 1997 IEEE Symposium on Security and Privacy, pp. 66-71.	1997
Arbaugh et al. ("Arbaugh 1997 Automated Recovery Tech Report")	Automated Recovery in a Secure Bootstrap Process, Dept. of Comput. & Info. Sci. Tech. Reports, University of Pennsylvania.	1997
Arbaugh et al. ("Arbaugh 1998 Automated Recovery Article")	Automated Recovery in a Secure Bootstrap Process, IEEE NDSS 1998.	1998
Ellison et al. ("Ellison")	Simple Public Key Certificate, IETF.	1997
IBM ("IBM 4758")	IBM 4758 PCI Cryptographic Coprocessor	1997
Intel ("Intel Data Sheet")	Intel Data Sheet, 1-Mbit (128K x 8), Boot Block Flash Memory, 28F001BX-T/28F001BX-B/28F001BN-T/28F001BN-B28F001BX-T.	1995
SUN ("SunOS")	SunOS Reference Manual.	1994
SUN ("SunOS II")	SunOS Reference Manual.	1994
MATLAB ("MATLAB")	MATLAB Installation Guide	1996
Intel ("Intel 486 System Guide")	Intel486^{TM} SL Microprocessor Superset System Design Guide	1992

Author	Title	Pub. Date
Intel ("Intel 430VX PCIset Guide")	INTEL 430VX PCISET82437VX System Controller (TVX) AND82438VX Data Path unit (TDX)	1996

Prior Art Systems & Products

Product / System	Responsible Entity	Date of Sale / Use
Products and systems designed, developed, offered for sale, sold, and in public use related to the AEGIS system (e.g., as described in Arbaugh, Arbaugh 1997, Arbaugh 1997 Automated Recovery Tech Report, Arbaugh 1998 Automated Recovery Article) ("AEGIS System").	Arbaugh et al.	At least by 1997
Products and systems designed, developed, offered for sale, sold, and in public use related to the DMI BIOS system (e.g., as described in DMI BIOS Specification, Labatte 311, Labatte 057, Shipman) ("DMI BIOS System").	AMI	At least by 1996
Products and systems designed, developed, offered for sale, sold, and in public use related to the Microsoft systems (e.g., Windows 95, Windows 98) using Windows Update or similar functionality (e.g., as described in Misra, Slivka, Pearce) ("Windows Update System").	Microsoft Corporation	At least by 1995
Products and systems designed, developed, offered for sale, sold, and in public use related to the ABYSS system (e.g., as described in White) ("ABYSS System").	IBM	At least by 1990
Products and systems designed, developed, offered for sale, sold, and in public use related to the IBM 4758 PCI Cryptographic Coprocessor (e.g., as described in IBM 4758) ("IBM 4758 Coprocessor System").	IBM	At least by 1997
Products and systems designed, developed, offered for sale, sold, and in public use related to the Dyad system (e.g., as described in Tygar, Yee 1994, Yee 1995) ("IBM 4758 System").	Tygar, Yee, et al.	At least by 1991

Product / System	Responsible Entity	Date of Sale / Use
Products and systems designed, developed, offered for sale, sold, and in public use related to the Citadel coprocessor (e.g., as described in Yee 1994, Yee 1995) ("Citadel Coprocessor System").	Yee et al.	At least by 1994
Products and systems designed, developed, offered for sale, sold, and in public use related to the Compaq computer systems (e.g., as described in Angelo 559, Angelo 882, Angelo 821, Olarig, Jablon) ("Compaq System").	Compaq	At least by 1995
Apple Power Book 540c	Apple	At least by May 16, 1994
Compaq Contura Aero 4/25c	Compaq	At least by August 1993
Intel 486 processors ("Intel 486 Processor")	Intel	At least by 1992
The Intel 430VX PCIset, including 82437VX System Controller (TVX), two 82438VX Data Paths (TDX), and the PCI ISA IDE Xcelerator (PIIX3) ("The Intel 430VX PCIset")	Intel	At least by July 1996
Computers running SUN operating systems ("SunOS")	Sun	1994
Computers running MATLAB software ("MATLAB")	MathWorks	1996
Products and systems designed, developed, offered for sale, sold, and in public use related to the VEGAS, VEGAS Pro, and Multitrack Media Editing System ("VEGAS System").	Sonic Foundry	1998-1999

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