

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of: Daniel Poznanovic, et al.
U.S. Patent No.: 7,149,867 Attorney Docket No.: 42653-0026IP1
Issue Date: December 12, 2006
Appl. Serial No.: 10/869,200
Filing Date: June 16, 2004
Title: SYSTEM AND METHOD OF ENHANCING
EFFICIENCY AND UTILIZATION OF MEMORY
BANDWIDTH IN RECONFIGURABLE HARDWARE

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**PETITION FOR *INTER PARTES* REVIEW OF UNITED STATES PATENT
NO. 7,149,867 PURSUANT TO 35 U.S.C. §§ 311–319, 37 C.F.R. § 42**

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Exhibit No.	Description
Exhibit 1001	U.S. Patent No. 7,149,867 to Daniel Poznanovic, <i>et al.</i> , filed June 16, 2004, and issued on December 12, 2006 (the “’867 patent”).
Exhibit 1002	Prosecution history of the ’867 patent.
Exhibit 1003	X. Zhang et al., <i>Architectural Adaptation for Application-Specific Locality Optimizations</i> , IEEE (1997) (“ Zhang ”). ¹
Exhibit 1004	R. Gupta, <i>Architectural Adaptation in AMRM Machines</i> , IEEE (2000) (“ Gupta ”).
Exhibit 1005	A. Chien and R. Gupta, <i>MORPH: A System Architecture for Robust High Performance Using Customization</i> , IEEE (1996) (“ Chien ”).
Exhibit 1006	Declaration of Stanley Shanfield, Ph.D.
Exhibit 1007	RESERVED
Exhibit 1008	RESERVED
Exhibit 1009	RESERVED
Exhibit 1010	Declaration of Rajesh K. Gupta
Exhibit 1011	Chien et al., <i>Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor</i> , IEEE (1999).
Exhibit 1012	Declaration of Jacob Munford
Exhibit 1013	RESERVED
Exhibit 1014	Order Governing Proceedings - Patent Case by Judge Alan D Albright, filed on June 30, 2020 in <i>FG SRC LLC v. Intel</i>

¹ For ease of reference and citation, Petitioner has added line numbers to Exhibits 1003, 1004, 1005 and 1011. For example, the citation “EX1003-15 C2:4-16” refers to Exhibit 1003, Page 15, Column 2, Lines 4-16, and the subsequent citation “id.-12 C1:12-C2:5” refers to Exhibit 1003, Page 12, Column 1, Line 12 through Column 2, Line 5.

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	<i>Corporation</i> , No. 6:20-cv-00315-ADA (W.D. Tex.)
Exhibit 1015	Scheduling Order by Judge Alan D Albright, filed on August 1, 2020 in <i>FG SRC LLC v. Intel Corporation</i> , No. 6:20-cv-00315-ADA (W.D. Tex.)
Exhibit 1016	Plaintiffs SRC Labs, LLC & Saint Regis Mohawk Tribe's Opening Claim Construction Brief, filed on November 5, 2018 in <i>SRC Labs, LLC et al. v. Amazon Web Services, Inc. et al.</i> , No. 2:18-cv-00317-JLP (W.D. Was.)
Exhibit 1017	Provisional Patent Application No. 60/479,339
Exhibit 1018	Plaintiff's Preliminary Infringement Contentions, submitted on July 23, 2020 in <i>FG SRC LLC v. Intel Corporation</i> , No. 6:20-cv-00315-ADA (W.D. Tex.)
Exhibit 1019	Plaintiff's Original Complaint, filed on October 18, 2017 in <i>SRC Labs, LLC et al. v. Amazon Web Services, Inc. et al.</i> , No. 1:17-cv-01227-JD (E.D.V.A.) now No. 2:18-cv-00317-JLP (W.D. Was.)
Exhibit 1020	Plaintiff's Original Complaint, filed on April 30, 2020 in <i>FG SRC LLC v. Xilinx</i> , No. 1:20-cv-00601-UNA (D. Del.)
Exhibit 1021	Order Continuing Stay, filed on May 3, 2019 in <i>SRC Labs, LLC et al. v. Amazon Web Services, Inc. et al.</i> , No. 2:18-cv-00317-JLP (W.D. Was.)
Exhibit 1022	Joint Status Report, filed on January 13, 2021 in <i>SRC Labs, LLC et al. v. Amazon Web Services, Inc. et al.</i> , No. 2:18-cv-00317-JLP (W.D. Was.)
Exhibit 1023	Scheduling Order, filed on February 25, 2021 in <i>FG SRC LLC v. Xilinx</i> , No. 1:20-cv-00601-UNA (D. Del.)
Exhibit 1024	Notice of Subpoena Duces Tecum, dated January 5, 2018, <i>SRC Labs, LLC et al. v. Amazon Web Services, Inc. et al.</i> , No. 1:17-cv-01227-JD (E.D.V.A.) now No. 2:18-cv-00317-JLP (W.D. Was.)

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