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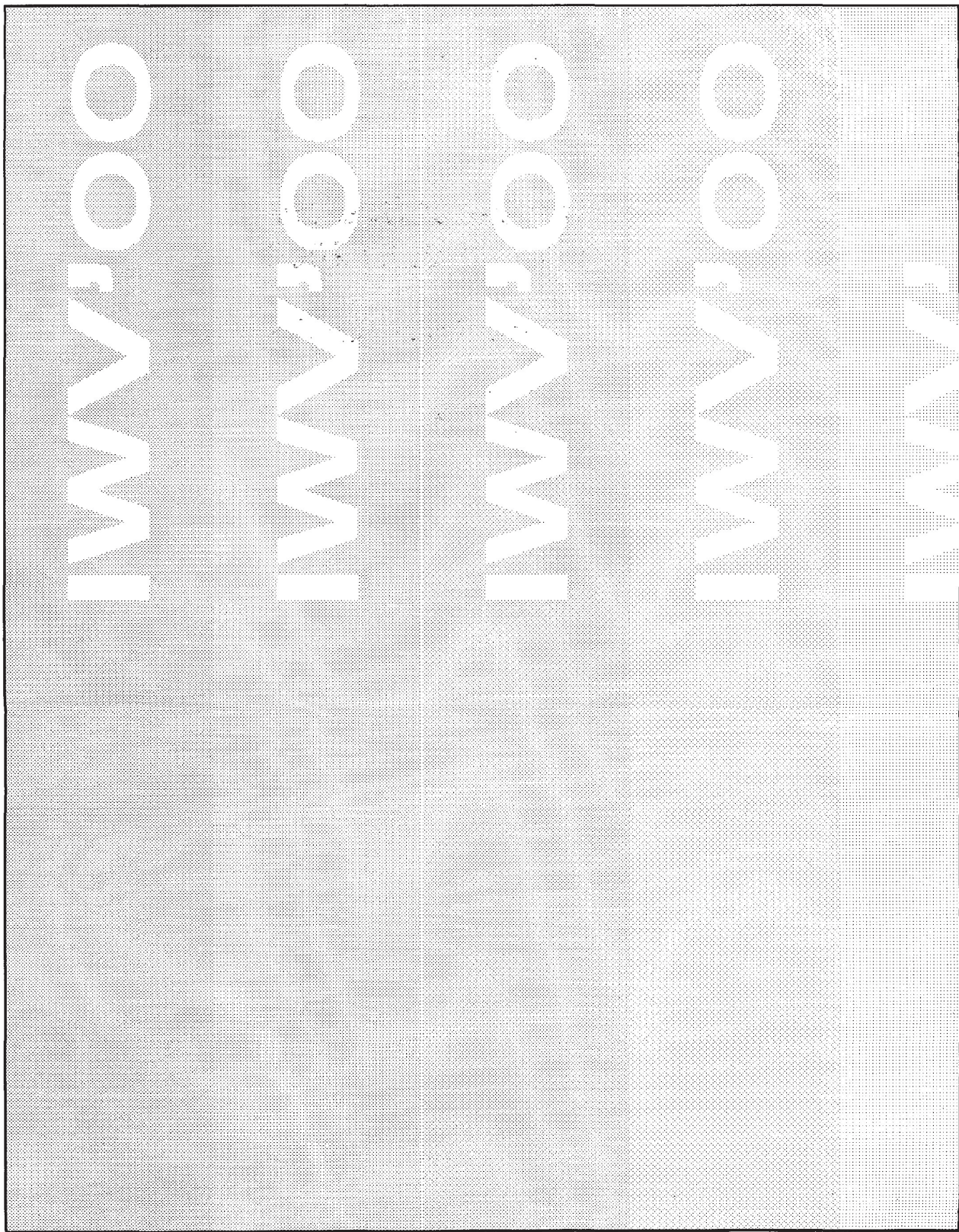
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**IEEE Computer Society
Workshop on VLSI 2000**

System Design for a System-on-Chip Era



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