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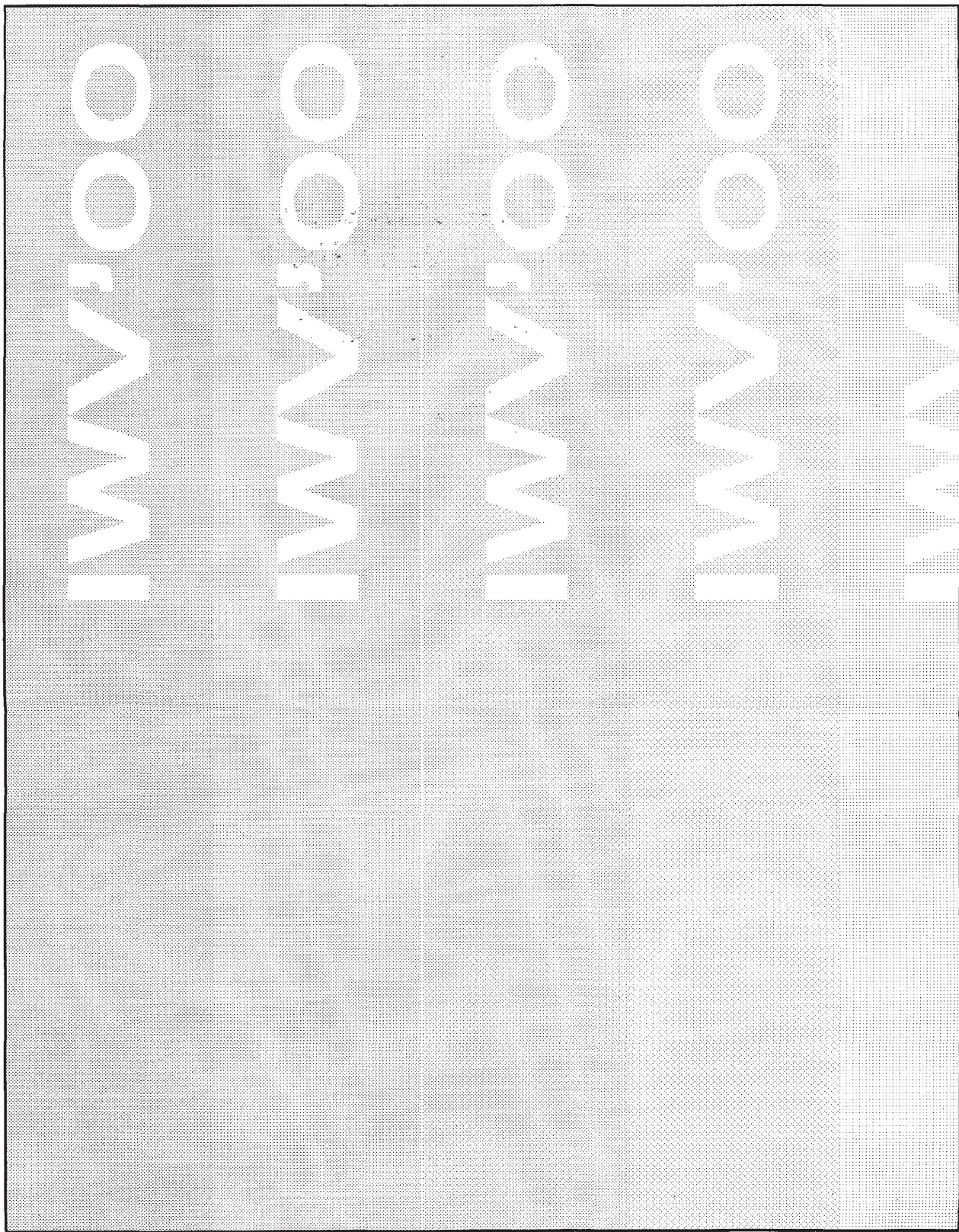
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**IEEE Computer Society
Workshop on VLSI 2000**

System Design for a System-on-Chip Era



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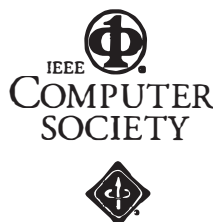
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51
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53
54
55
56

Table of Contents



Message from the General Chairsix

Message from the Technical Program Chairsxi

Workshop Committees..... xiii

Steering Committee xv

System Level Design Methods and Examples I

Mead-Conway VLSI Design Approach and System Design
Challenges Ahead3
Lynn Conway

Alternative Architectures for Video Signal Processing5
W. Wolf

PicoRadio: Ad-hoc Wireless Networking of Ubiquitous Low-energy
Sensor/Monitor Nodes.....9
J. Rabaey, J. Ammer, J. L. da Silva Jr., and D. Patel

System Level Design Methods and Examples II

A System-level Approach to Power/Performance Optimization in Wearable
Computers..... 15
A. Smailagic, D. Reilly, and D. P. Siewiorek

Emerging Trends in VLSI Test and Diagnosis21
Y. Zorian

Multilanguage Design of a Robot Arm Controller: Case Study29
G. Nicolescu, P. Coste, F. Hessel, P. LeMarrec, and A.A. Jerraya

Low Power Design

Instruction Scheduling Based on Energy and Performance Constraints 37
A. Parikh, M. Kandemir, N. Vijaykrishnan, and M.J. Irwin

Dynamic Voltage Scaling Techniques for Distributed Microsensor Networks..... 43
R. Min, T. Furrer, and A. Chandrakasan

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