EXPRESSCARD® STANDARD

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PCMCIA

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		Proposal 010: Release of WAKE# Timing	
		Proposal 012: ESD Figure Correction	
		Proposal 013: ESD Testing	
		Proposal 014: Jitter Spec Updates	
		Proposal 016: Support for PCI Bridges on Modules	
		Proposal 018: BIOS ExpressCard PCle Support in WinXP/2000	
		Proposal 019: Security Notch Dimensioning Alignment	
		Proposal 021: CLKREQ# Dynamic Protocol Enable Default	
		Proposal 022: Enabling Additional Use of SMBus Pins	
		Proposal 023: Finger Grip Dimensional Options	
		Proposal 025: Definition of Terms Rx and Tx	
		Proposal 026: Allow Modules to Stop PLL in L1 When CLKREQ# is Not Honored	
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		Proposal 043: Miscellaneous Editorial Clarifications to the Standard	



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