UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

APPLE INC.,
Petitioner,

v.

KOSS CORPORATION,
Patent Owner.

CASE: IPR2021-00381
U.S. PATENT NO. 10,491,982

DECLARATION OF JOSEPH C. MCALEXANDER III

September 28, 2021

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1. I, Joseph C. McAlexander III, declare as follows:

2. I have been retained by counsel for Koss Corp. ("Koss") as a technical expert in connection with the *inter partes* review ("IPR") proceeding identified above for U.S. Patent 10,491,982 (the "'982 Patent"). I submit this declaration in support of Koss's response to the petition.

I. BACKGROUND AND QUALIFICATIONS

3. I have a Bachelor of Science in Electrical Engineering from North Carolina State University and have studied neural science at the University of Texas Graduate School of Biomedical Science.

4. Upon completion of my electrical engineering degree in 1969, I was commissioned as an officer in the U.S. Army. For 2 years, I managed the air defense operation for the New England area, which included radar and secure communication channels to aircraft, missile batteries, and U.S. Command. I then commanded a signal battalion in South Korea for 1 year, designing and orchestrating at the division level the first of its kind communication power grid mapping study using AM and FM transmission/reception, among others, and utilizing crypto security transmission/reception methods.

5. I am a Registered Professional Engineer in the state of Texas (Reg. No. 79,454) and am a recognized inventor on thirty-one U.S. patents. I have forty-nine years of professional experience, during which I designed and analyzed a

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variety of microcircuits, semiconductors, and control systems, amongst other technologies for Texas Instruments, Inc. and EPI Technologies, Inc. Specifically, I have designed Dynamic Random Access Memories ("DRAMs"), Static Random Access Memories ("SRAMs"), Charged Coupled Devices ("CCDs"), Shift Registers ("SRs"), and a variety of functional circuits, including input/output buffers for addresses and data transmission, decoders, clocks, sense amplifiers, fault tolerant parallel-to-serial data paths for video applications, level shifters, converters, pumps, logic devices, wireless communication systems, and microelectromechanical systems ("MEMs"). I possess significant expertise in operations and manufacturing associated with these technologies, including a sophisticated knowledge of quality control, testing, reliability, and failure analyses.

6. I have conducted high level instruction to design and process engineers and managers at Texas Instruments, among others, in Solid State Device Physics, Semiconductor Processing, Circuit Design Techniques, and Statistical Quality Control Methods. I have also instructed corporate audiences in Effectiveness Training, Japanese Manufacturing Techniques, and problem recognition and solution methods and tools.

7. As part of licensing of my IP circa 2002 – 2004, I negotiated and executed a number of licensing and design programs to provide GPS tracking and transmission of information wirelessly, using paging and CDMA. The

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