

[54] METHOD AND APPARATUS FOR INDEPENDENTLY RESETTING PROCESSORS AND CACHE CONTROLLERS IN MULTIPLE PROCESSOR SYSTEMS

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[73] Assignee: Compaq Computer Corp., Houston, Tex.

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Related U.S. Application Data

[63] Continuation of Ser. No. 15,314, Feb. 9, 1993, which is a continuation of Ser. No. 431,653, Nov. 3, 1989.

[51] Int. Cl.⁶ G06F 9/00

[52] U.S. Cl. 395/700; 364/DIG. 1; 364/280.3; 364/280.2; 364/282

[58] Field of Search 395/700, 375

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[57] ABSTRACT

A method and system for independently resetting primary and secondary processors 20 and 120 respectively under program control in a multiprocessor, cache memory system. Processors 20 and 120 are reset without causing cache memory controllers 24 and 124 to reset.

6 Claims, 7 Drawing Sheets

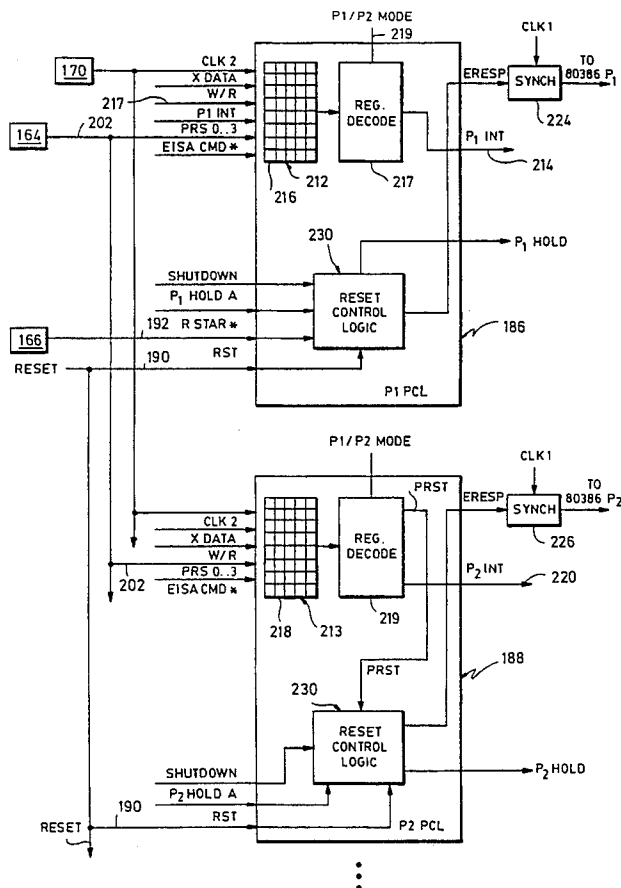
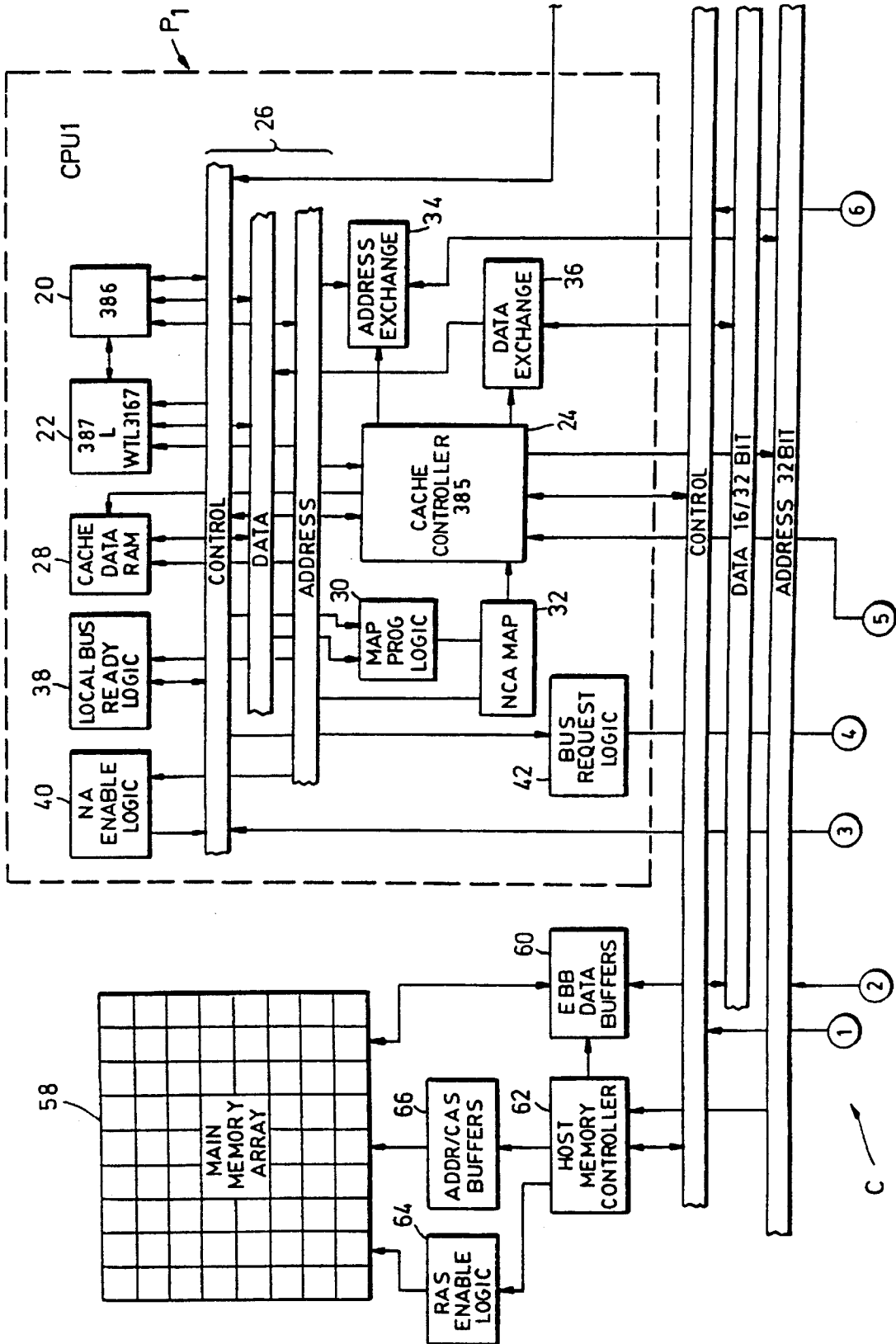


FIG. 1A



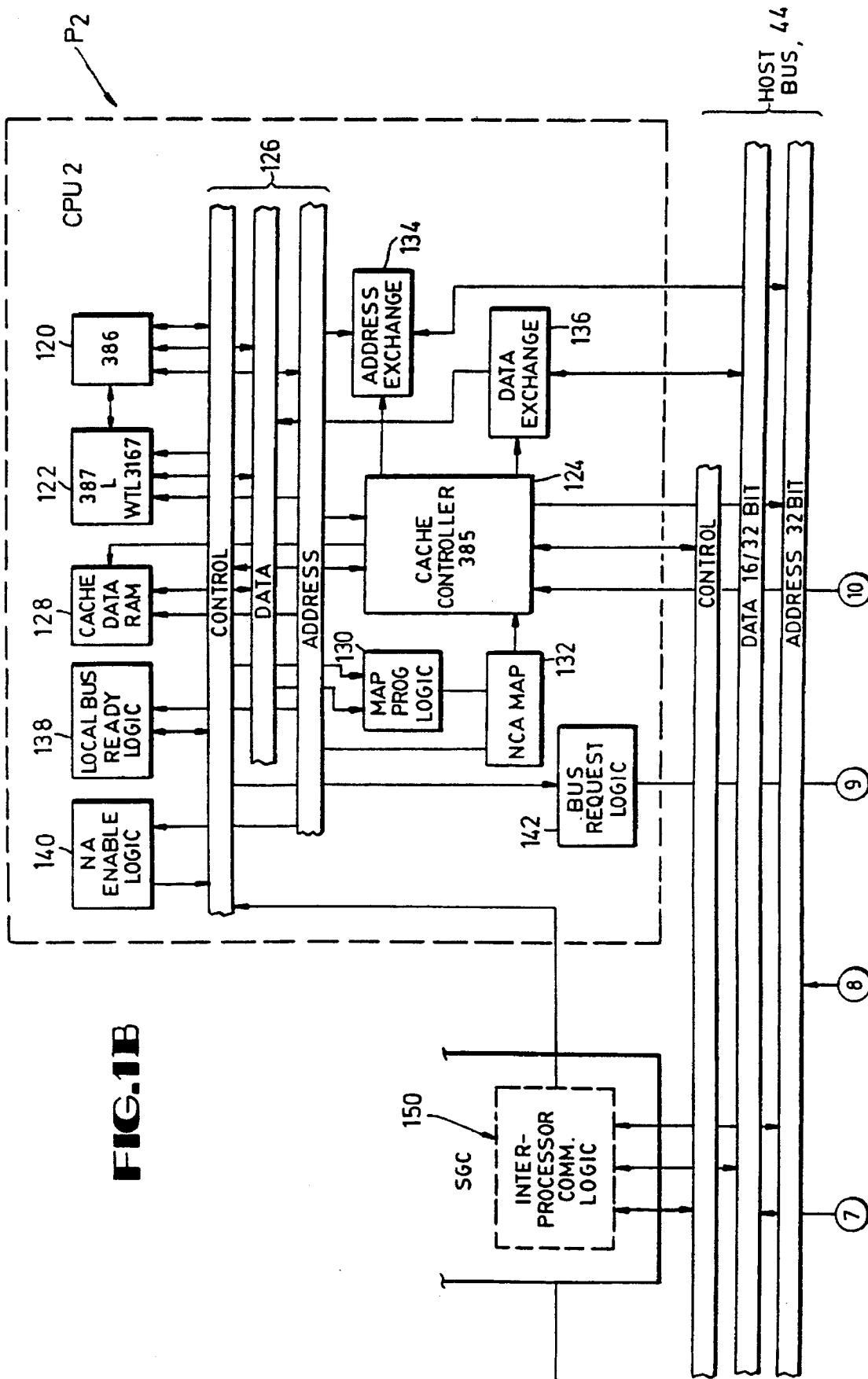


FIG. 1B

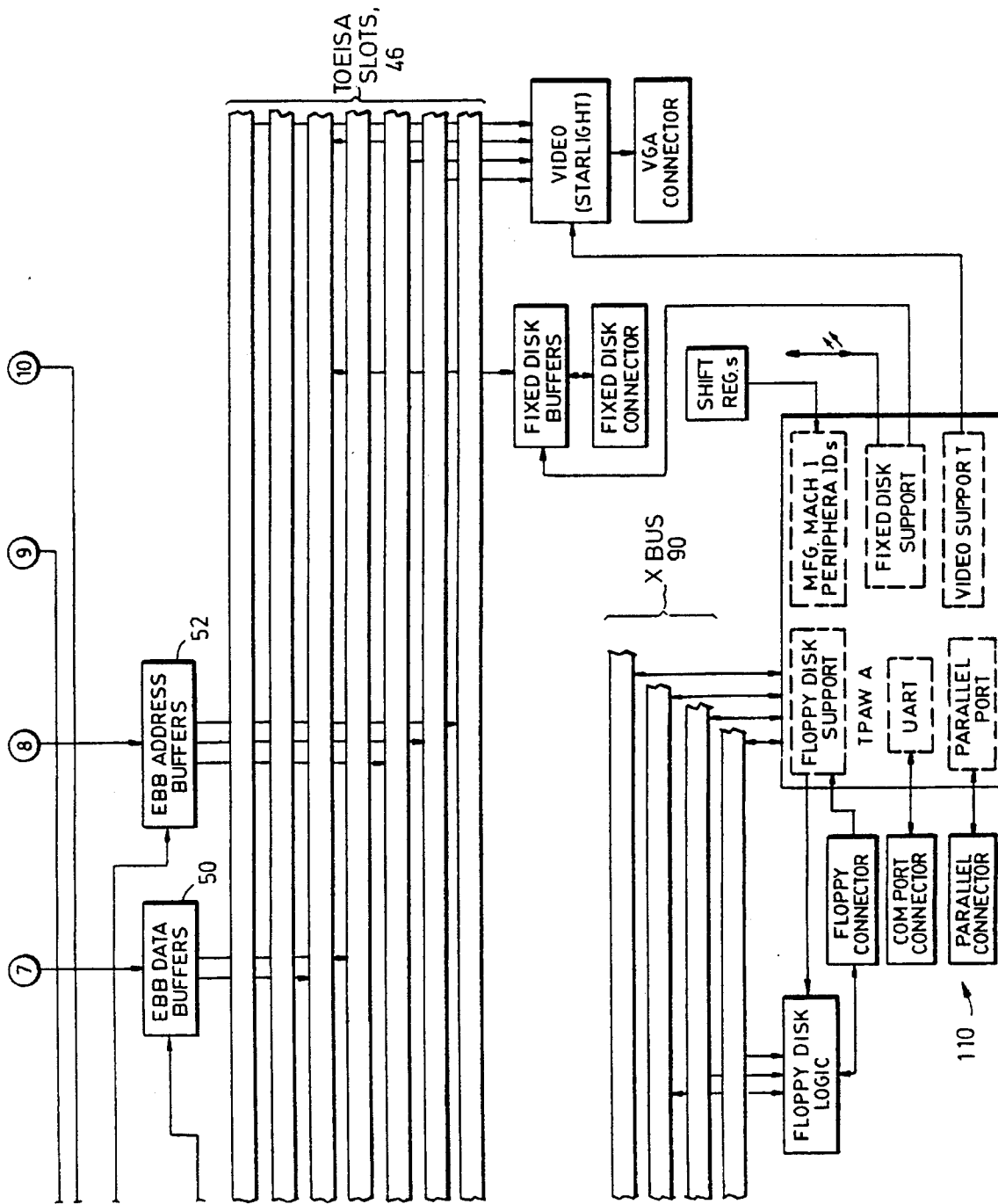


FIG. 2B

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