

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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SAMSUNG ELECTRONICS CO., LTD.,  
Petitioner,

v.

DEMARAY LLC,  
Patent Owner.

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IPR2021-01090  
Patent 7,544,276 B2

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Before CHRISTOPHER L. CRUMBLEY, KRISTINA M. KALAN, and  
KIMBERLY McGRAW, *Administrative Patent Judges*.

KALAN, *Administrative Patent Judge*.

DECISION  
Granting Institution of *Inter Partes* Review  
*35 U.S.C. § 314*

Granting Petitioner's Unopposed Motion for Joinder  
*35 U.S.C. § 315(c); 37 C.F.R. § 42.122*

## I. INTRODUCTION

Samsung Electronics Co., Ltd. (“Petitioner”) filed a Petition (Paper 1, “Pet.”) requesting an *inter partes* review of claims 1–13 of U.S. Patent No. 7,544,276 B2 (Ex. 1001, “the ’276 patent”). Petitioner also filed a Motion for Joinder (Paper 3, “Mot.”), seeking to join as a party to *Applied Materials, Inc. v. Demaray LLC*, IPR2021-00103 (the “Applied IPR”). Demaray LLC (“Patent Owner”) did not file a Preliminary Response to the Petition or an opposition to the Motion for Joinder.

To institute an *inter partes* review, we must determine that the information presented in the Petition shows “there is a reasonable likelihood that the petitioner would prevail with respect to at least 1 of the claims challenged in the petition.” 35 U.S.C. § 314(a). If we determine that a party has filed a petition that warrants institution of an *inter partes* review, we may join that party to another instituted *inter partes* review. 35 U.S.C. § 315(c). For the reasons discussed below, after considering the parties’ submissions and the evidence of record, we determine that Petitioner has demonstrated a reasonable likelihood of prevailing with respect to at least one claim of the ’276 patent. Thus, we institute an *inter partes* review. We also join Petitioner as a party to the Applied IPR.

At this preliminary stage of the proceeding, we have not made a final determination as to the patentability of any challenged claim or any factual or legal issue underlying the patentability inquiry. Any final determination will be based on the record developed during trial.

### A. Related Proceedings

Petitioner identifies IPR2021-00103 as another proceeding involving the ’276 patent. Pet. 2. Petitioner also identifies *Demaray LLC v. Samsung*

*Electronics Co., Ltd.*, No. 6-20-cv-00636 (W.D. Tex.); *Demaray LLC v. Intel Corporation*, No. 6-20-cv-00634 (W.D. Tex.); and *Applied Materials, Inc. v. Demaray LLC*, No. 5-20-cv-09341 (N.D. Cal.) as related matters. *Id.* Each of these proceedings involves the '276 patent. *Id.*

*B. Real Parties-In-Interest*

Petitioner identifies Applied Materials, Inc. (“Applied Materials”), Intel Corporation, Samsung Electronics Co., Ltd., Samsung Electronics America, Inc., Samsung Semiconductor, Inc., and Samsung Austin Semiconductor, LLC, as real parties-in-interest. Pet. 1.

*C. The '276 Patent*

The '276 patent, titled “Biased Pulse DC Reactive Sputtering of Oxide Films,” relates to “deposition of oxide and oxynitride films and, in particular, to deposition of oxide and oxynitride films by pulsed DC reactive sputtering.” Ex. 1001, code (54), 1:12–14. The '276 patent discloses that typically, radio frequency (“RF”) sputtering has been used for deposition of oxide dielectric films, but arcing can occur between sputtering target tiles used to make such films, which causes contamination of the deposited films. *Id.* at 2:25–30. The '276 patent further states that reactors for RF sputtering, particularly their power systems, are complicated. *Id.* at 2:30–38. The '276 patent discloses that reactive DC magnetron sputtering of nonconductive oxides “is done rarely” because insulating surfaces accumulate charge during deposition and result in arcing, which “can damage the power supply, produce particles and degrade the properties of deposited oxide films.” *Id.* at 4:44–52.

Figure 1A of the '276 patent is reproduced below.

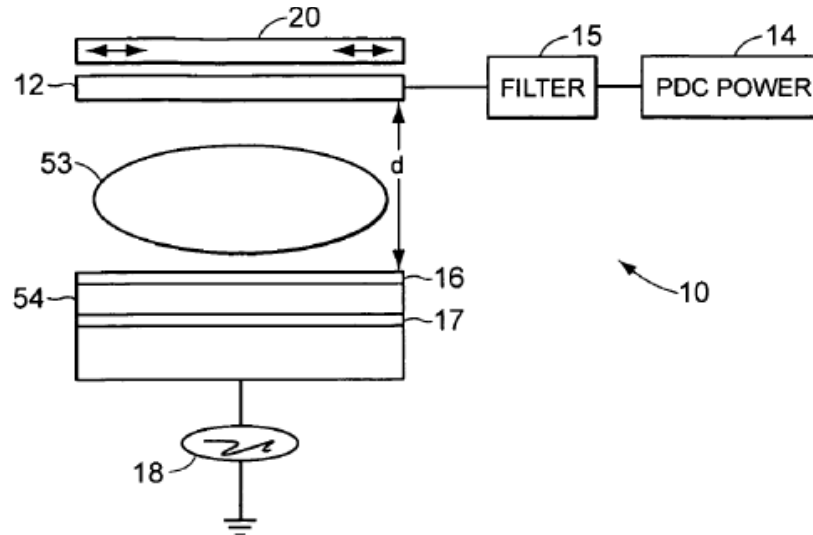


FIG. 1A

Figure 1A depicts a pulsed DC sputtering reactor. *Id.* at 3:26–27. The '276 patent describes reactor apparatus 10 for sputtering of material from target 12. *Id.* at 5:7–9. Magnet 20 is scanned across the top of target 12, which reduces local erosion of target 12 during sputtering. *Id.* at 5:28–29, 8:47–55. Substrate 16 is opposite and parallel to target 12. *Id.* at 5:23–24. Substrate 16 is capacitively coupled to electrode 17 via insulator 54. *Id.* at 5:26–27. Electrode 17 can be coupled to RF power supply 18. *Id.* at 5:27–28. The '276 patent explains that columnar structures in a deposited film can be detrimental for optical wave guide applications, but applying an RF bias on substrate 16 during deposition can substantially eliminate columnar structures. *Id.* at 5:60–67. The '276 patent discloses that target 12 functions as a cathode when power is applied to it, which creates plasma 53. *Id.* at 5:24–26.

Target 12 is electrically coupled through filter 15 to pulsed DC power supply 14. *Id.* at 5:19–20. The '276 patent discloses that the polarity of the power supplied to target 12 by the pulsed DC power supply 14 oscillates

between negative and positive potentials. *Id.* at 5:30–33. According to the '276 patent, the insulating layer on the surface of target 12 discharges during the positive period, which prevents arcing. *Id.* at 5:33–35. The '276 patent discloses that the pulsing frequency must exceed a critical frequency, which depends on a target material, cathode current, and reverse time. *Id.* at 5:35–37.

Reactor apparatus 10 further includes filter 15, which prevents RF power supply 18 from coupling into pulsed DC power supply 14. *Id.* at 5:50–51. According to the '276 patent, filter 15 can be a 2 MHz band rejection filter when a 2 MHz power supply is used for RF power supply 18. *Id.* at 5:51–55. The '276 patent discloses that “the band width of the filter 15 can be approximately 100 kHz.” *Id.* at 5:55–57.

*D. Illustrative Claim*

Claims 1 and 6 of the '276 patent are independent. Claim 1 is representative and is reproduced below:

1. A reactor according to the present invention, comprising:
  - a target area for receiving a target;
  - a substrate area opposite the target area for receiving a substrate;
  - a pulsed DC power supply coupled to the target area, the pulsed DC power supply providing alternating negative and positive voltages to the target;
  - an RF bias power supply coupled to the substrate; and
  - a narrow band-rejection filter that rejects at a frequency of the RE<sup>1</sup> bias power supply coupled between the pulsed DC power supply and the target area.

Ex. 1001, 22:40–50.

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<sup>1</sup> This appears to be a typographical error that should read “RF” instead of “RE.” A district court may “correct an error in a patent by interpretation of the patent where no certificate of correction has been issued . . . only if

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