| UNITED ST | FATES PATEI —— | NT AND T | TRADEMA — | RK OFFICE |
|-----------|-------------------|----------------|--------------|-----------|
| BEFORE | THE PATENT | TRIAL A | AND APPE | AL BOARD |
| | QUALCOMN | M INCOR | PORATED | • |
| | PE' | FITIONE | R | |

 \mathbf{V}_{\bullet}

MONTEREY RESEARCH, LLC, **PATENT OWNER**

INTER PARTES REVIEW NO. IPR2020-01492 **PATENT 6,651,134**

PETITIONER'S DEMONSTRATIVES

Qualcomm

Petitioner's Hearing Demor IPR2020-01492

U.S. Patent No. 6,651,

December 7, 2021

'134 Patent

Purports to have developed "non-interruptible" but

Schaefer

- Schaefer discloses "non-interruptible" burst operations
- A <u>user</u> command is <u>issued</u> at T2 before the precharge is <u>in</u>
- PO's Expert concedes that the only user command is issu
- Petitioner's Expert confirms Schaefer's disclosure
- PO's Expert reliance on "termination" commands is not a
- Schaefer's purported goals do not negate anticipation

4 Patent | Purports to have developed "non-interruptible portion of the port o

| | USBN6/51. | 34181 |
|---|--|---|
| United States Patent | (14) Patent No.: (45) Date of Patent: | US 6,651,134 BI Nov. 18, 2003 |
| | 50005 A = 4:00 G | mer |
| (54) MEMORY DEVICE WITH FIXED LENGTH NON INTERRUPTIBLE BURST | Service A " All the Re- | The back - 2000 is the second |
| (75) Jameses Carbal G. Phelon, Minicha Vire, CA (US) | CONTENTATION | SCATORS |
| (15) Assigner Oppion Studenthiotor Corp., Su. Loc., CA (CS) | Upper State of Michigan Congress State of Congress Congre | hs 50.27% |
| - K-riv | • gited by extendents | ar de |
| CAC TEACH PAGE TOWN | Polester Antoniose - Oceani S Autorios Raunicas - Media I (NI) Antonios Agust. Ar Kr | ć⊶Cle ≽i.γlas I: Maketas. ¢ |
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| see that light is poor | 12-3 | man of the fact that is below. |
| [41] 161, C.L. 711, 162, 163, 36, 23, 36, 36, 36, 36, 36, 36, 36, 36, 36, 3 | de antisconti remigrate la de antisconti remigrate la decisi address regul | al. He with the star he |
| [44] Field of Sounds 7[17]67; 365 233, 238.3; 318.35 | to the same of the same | and that the second second |
| (55) Reference Clini (55) Proper proCOMPNS | SERVICE SERVICE | N Die m |
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| (12) | Unite | d States Patent | (10) |
|-------------|--|---|---------------------|
| | Phelan | | (45) |
| (54) | | Y DEVICE WITH FIXED LENGTH ERRUPTIBLE BURST | 5,9 5,9 |
| (75) | Inventor: | Cathal G. Phelan, Mountain View, CA (US) | 6,0 6,2 |
| (73) | Assignee: | Cypress Semiconductor Corp., San Jose, CA (US) | Underst: Cypress |
| (*) Notice: | Notice: | Subject to any disclaimer, the term of this | * cited |
| | patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days. | Primary | |

Appl. No.: 09/504,344

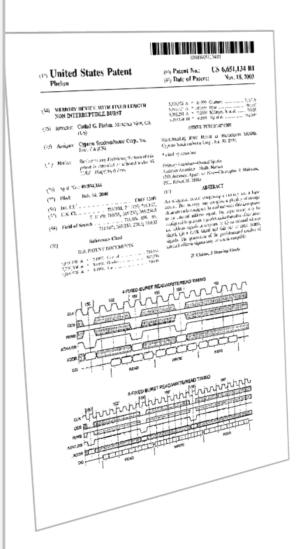
Feb. 14, 2000

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Primary Assistan (74) Att

P.C.; Ro

4 Patent | Purports to have developed "non-interruptib



SUMMARY OF THE I

The present invention concern comprising a memory and a log may comprise a plurality of S configured to read and write data is address Signal. The logic circuit generate a predetermined number Signals in response to (i) an extern clock signal and (iii) one or mo generation of the predetermine address signals may be non-interru

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