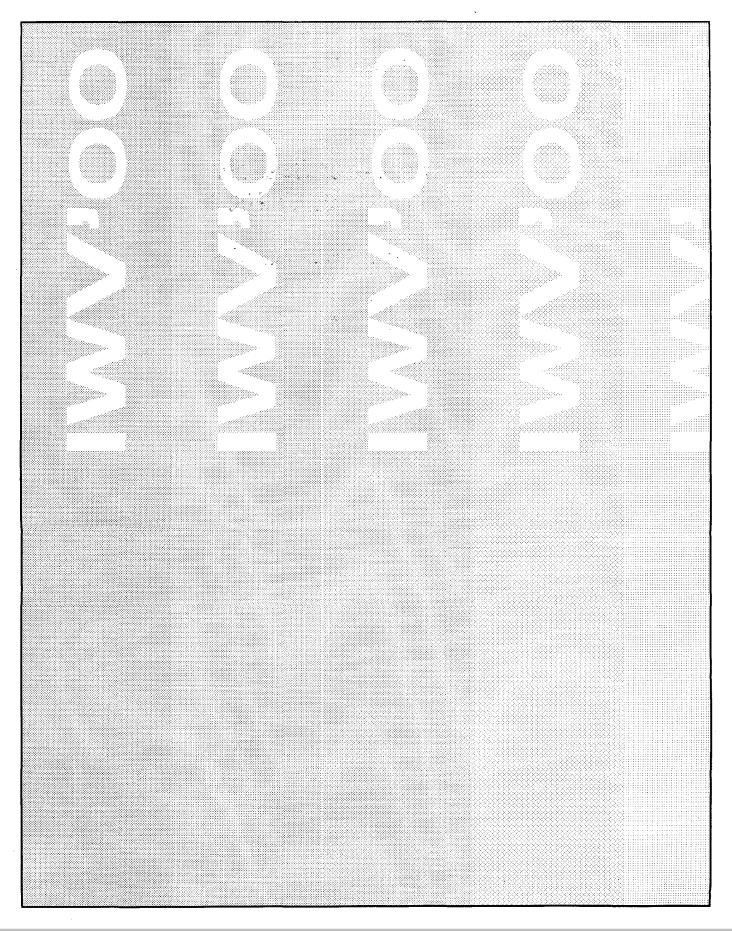
## **Proceedings**

## **IEEE Computer Society Workshop on VLSI 2000**

System Design for a System-on-Chip Era









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### **Table of Contents**



Message from the General Chairs	.ix
Message from the Technical Program Chairs	xi
Workshop Committees	xiii
Steering Committee	.xv
System Level Design Methods and Examples I	
Mead-Conway VLSI Design Approach and System Design Challenges Ahead	3
Alternative Architectures for Video Signal Processing	5
PicoRadio: Ad-hoc Wireless Networking of Ubiquitous Low-energy Sensor/Monitor Nodes	9
System Level Design Methods and Examples II	
A System-level Approach to Power/Performance Optimization in Wearable Computers	15
Emerging Trends in VLSI Test and Diagnosis	21
Multilanguage Design of a Robot Arm Controller: Case Study	29
Low Power Design	
Instruction Scheduling Based on Energy and Performance Constraints	37
Dynamic Voltage Scaling Techniques for Distributed Microsensor Networks	43



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