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# UNITED STATES PATENT AND TRADEMARK OFFICE BEFORE THE PATENT TRIAL AND APPEAL BOARD INTEL CORPORATION, Petitioner v. FG SRC LLC, Patent Owner CASE NO.: IPR2020-01449 PATENT NO. 7,149,867

# PETITIONER'S REPLY TO PRELIMINARY RESPONSE

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### TABLE OF EXHIBITS

Exhibit No.	Description
Exhibit 1001	U.S. Patent No. 7,149,867 to Daniel Poznanovic, <i>et al.</i> , filed June 16, 2004, and issued on December 12, 2006 (the "'867 patent").
Exhibit 1002	Prosecution history of the '867 patent.
Exhibit 1003	X. Zhang et al., Architectural Adaptation of Application-Specific Locality Optimizations, IEEE (1997) ("Zhang"). <sup>1</sup>
Exhibit 1004	R. Gupta, Architectural Adaptation in AMRM Machines, IEEE (2000) ("Gupta").
Exhibit 1005	A. Chien and R. Gupta, MORPH: A System Architecture for Robust Higher Performance Using Customization," IEEE (1996) ("Chien").
Exhibit 1006	Declaration of Stanley Shanfield, Ph.D.
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Exhibit 1010	Declaration of Rajesh K. Gupta
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Exhibit 1012	Declaration of Jacob Munford
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<sup>&</sup>lt;sup>1</sup> For ease of reference and citation, Petitioner has added line numbers to Exhibits 1003, 1004, 1005 and 1011. For example, the citation "EX1003-15 C2:4-16" refers to Exhibit 1003, Page 15, Column 2, Lines 4-16, and the subsequent citation "*id.*-12 C1:12-C2:5" refers to Exhibit 1003, Page 12, Column 1, Line 12 through Column 2, Line 5.



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