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(54) **INTERFACE FOR INTEGRATING RECONFIGURABLE PROCESSORS INTO A GENERAL PURPOSE COMPUTING SYSTEM**

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(57) **ABSTRACT**

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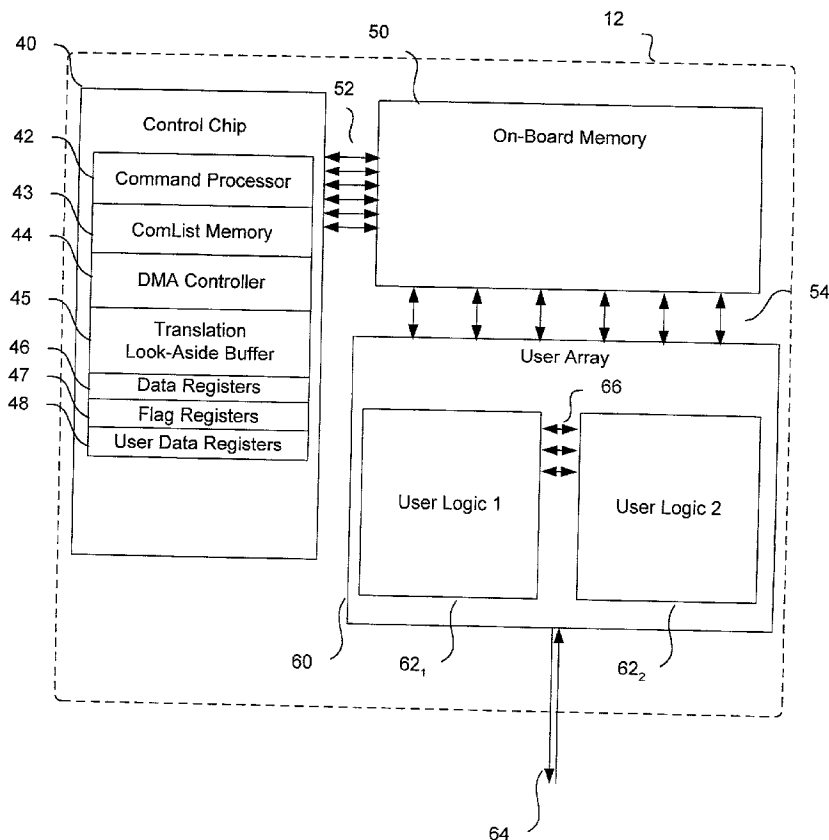
The present invention describes a method and system for an interface for integrating reconfigurable processors into a general purpose computing system. In particular, the system resides in a computer system containing standard instruction processors, as well as reconfigurable processors. The interface includes a command processor, a command list memory, various registers, a direct memory access engine, a translation look-aside buffer, a dedicated section of common memory, and a dedicated memory. The interface is controlled during compilation of a user application, or various direct commands.

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(60) Provisional application No. 60/286,979, filed on Apr. 30, 2001.



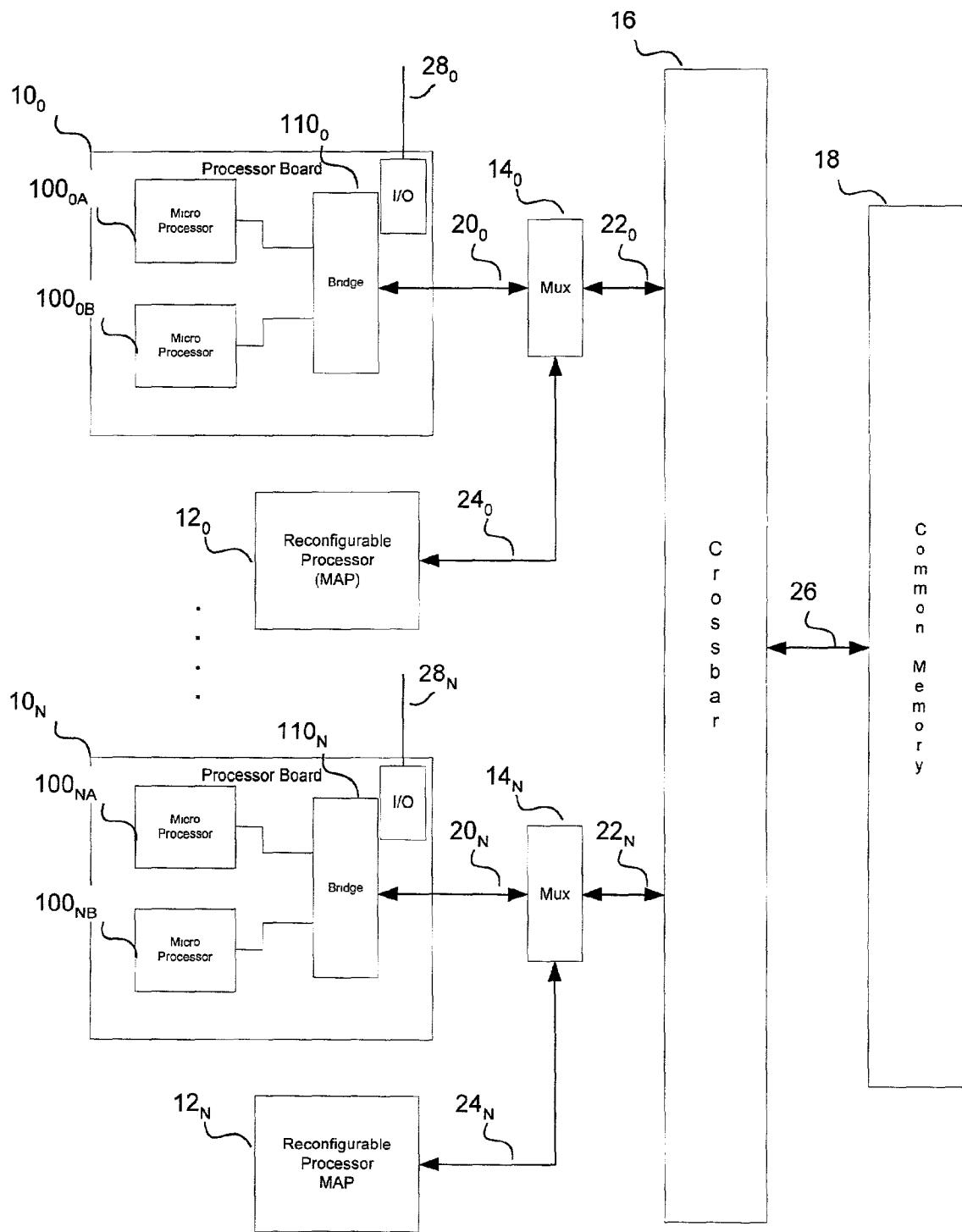


Figure 1

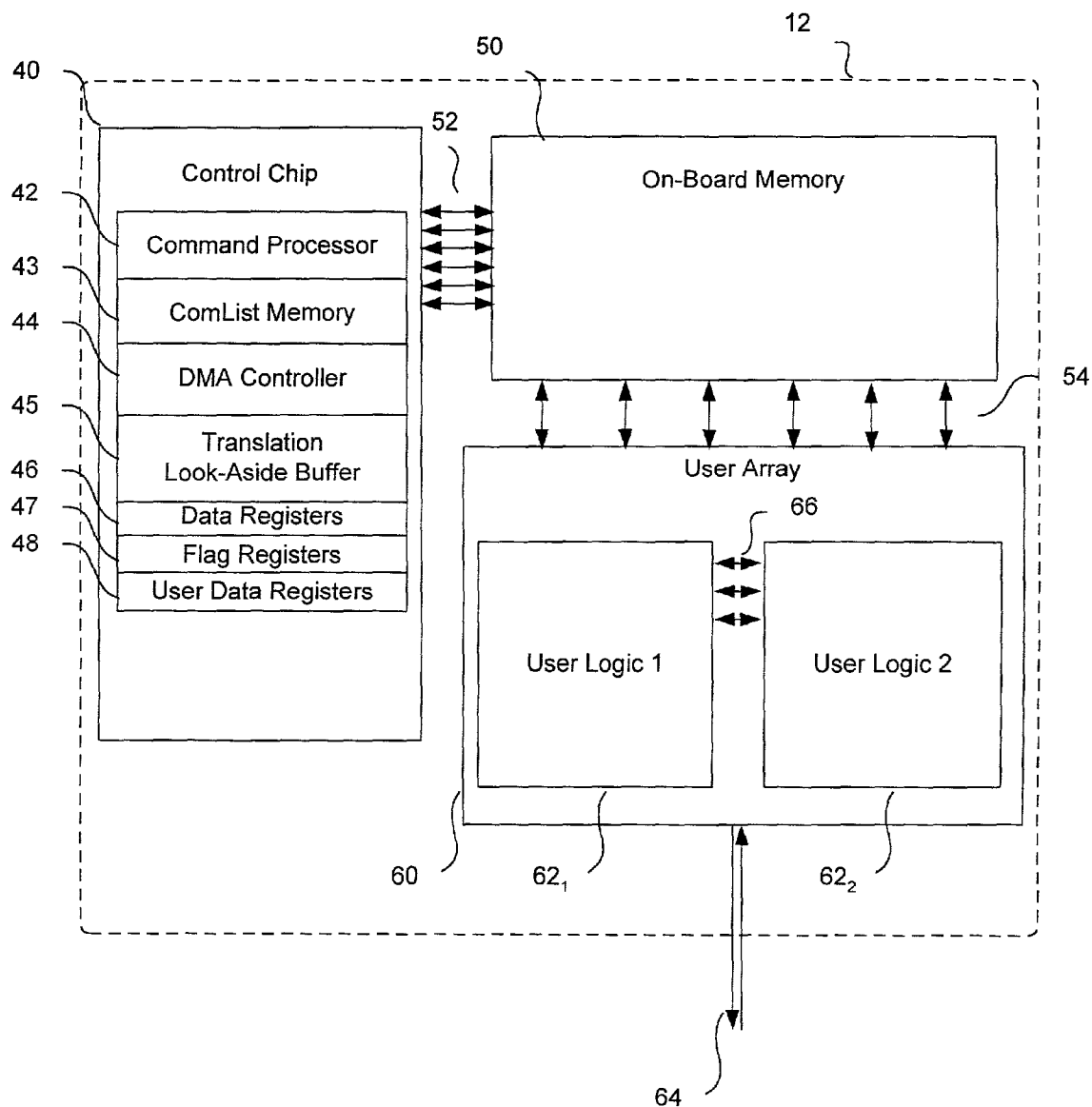


Figure 2

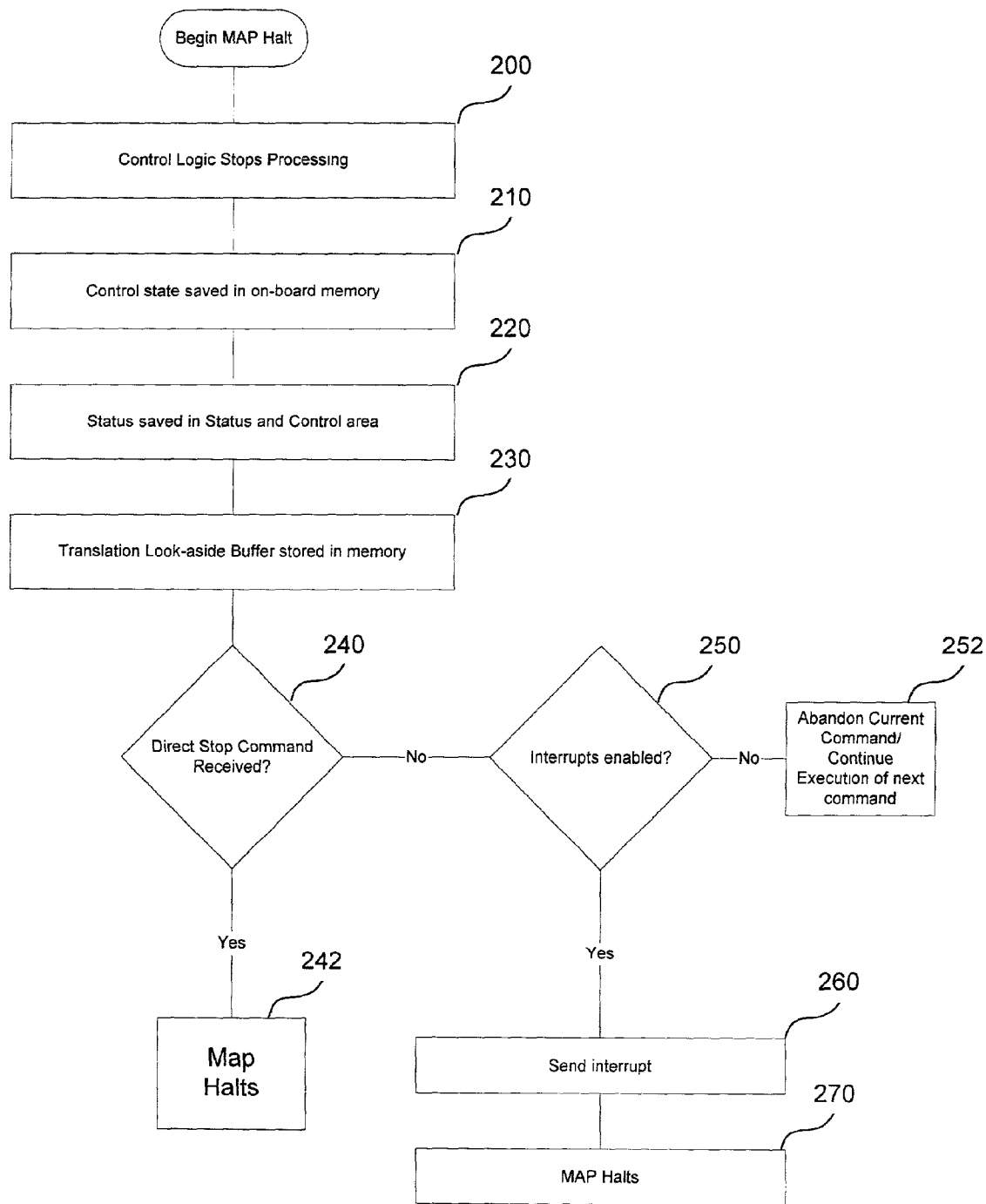


Figure 3

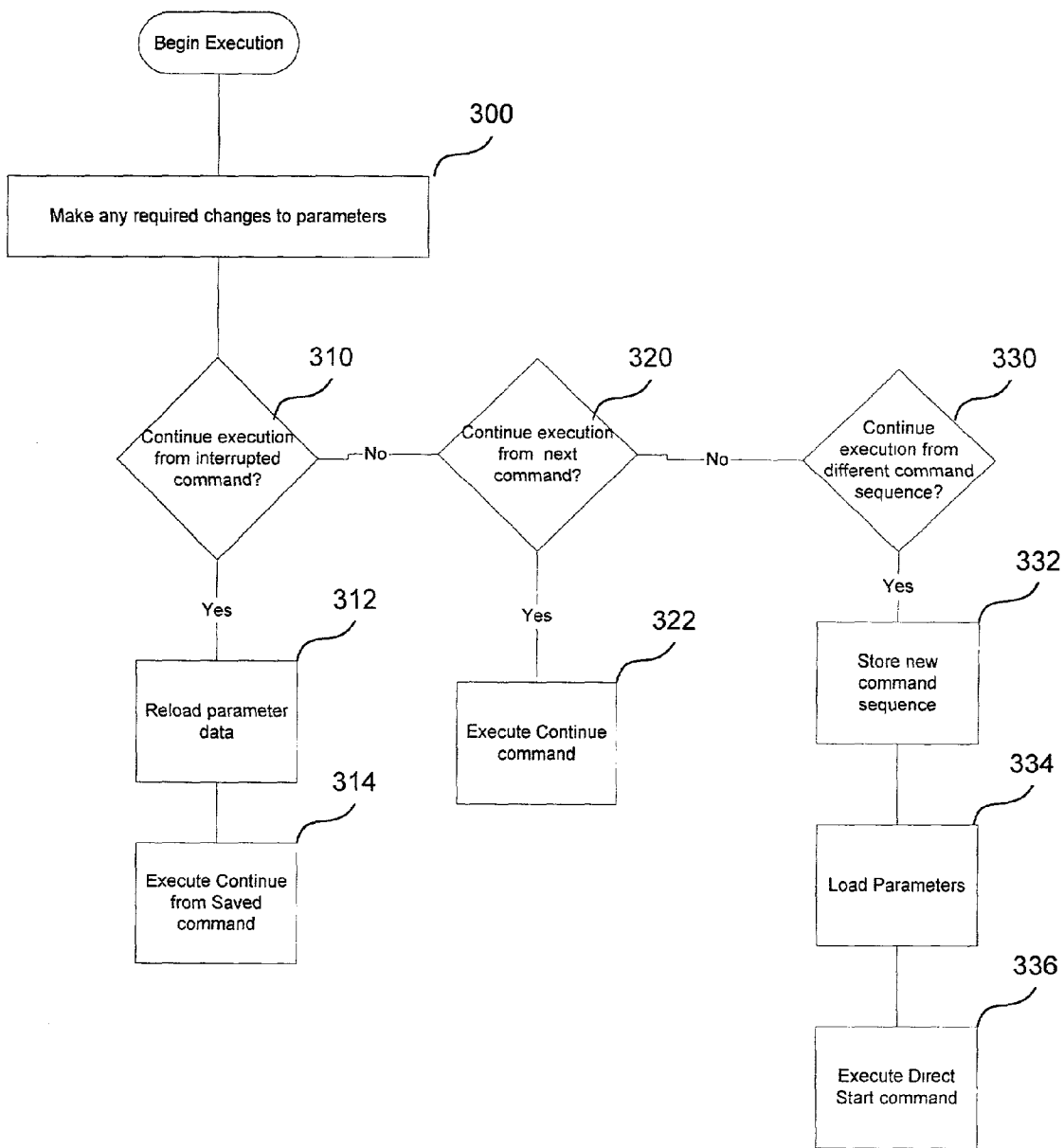


Figure 4

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