**IN THE UNITED STATES DISTRICT COURT
FOR THE WESTERN DISTRICT OF TEXAS
AUSTIN DIVISION**

FG SRC, LLC,	§	
Plaintiff	§	
	§	**1-20-CV-00834-ADA**
-v-	§	
	§	
INTEL CORPORATION,	§	
Defendant	§	

AMENDED CLAIM CONSTRUCTION ORDER

The Court held a *Markman* hearing on March 5, 2021. The parties elected to argue the first

two terms only. The Court took the matter under advisement. After reviewing the parties' briefs,

the patent specification, the prosecution history, and the parties' arguments, the Court entered a

claim construction order on March 11, 2021 (ECF No. 67). The Court enters this Order to amend

its March 11, 2021 claim construction order.

SIGNED this 5th day of April, 2021.

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ALAN D ALBRIGHT
UNITED STATES DISTRICT JUDGE

Term	Plaintiff's Proposed Construction	Defendants' Proposed Construction	Court's
"[the data prefetch unit] retrieves only computational data required by the algorithm from a second memory… and places the retrieved computational data in the first memory" Claim 1	"[the data prefetch unit] retrieves from a second memory that computational data which is required by the algorithm and no other computational data … and places the retrieved computational data in the first memory."	"[the data prefetch unit] retrieves the data input to the algorithm implemented in computational logic and no other data or instruction from a second memory … and places the retrieved data in the first memory."	"[the data retrieves c required b no other d from a sec places the first memo
"read and write only data required for computations by the algorithm between the data prefetch unit and the common memory" Claim 9	"read, using the data prefetch unit, only data required for computations by the algorithm from common memory and write, using the data prefetch unit, only data required for computations by the algorithm."	"read and write the data input to the algorithm implemented in computational logic and no other data or instruction between the data prefetch unit and the common memory."	"read and for compu algorithm, instruction prefetch u memory"

Term	Plaintiff's Proposed Construction	Defendants' Proposed Construction	Court's
"operates independent of and in parallel with logic blocks using the [computational data / compuional [sic] data]" Claims 1, 9	The term "compuional" in Claim 1 should be construed as "computational." Otherwise, this term has its plain and ordinary meaning and need not be construed. In the alternative only, this term may be construed to have its plain and ordinary meaning, which is "executes a preconfigured operation without intervention of and in parallel with logic blocks using the computational data."	"can initiate and carry out its operations each of prior to, in parallel with, or after the requirement for the data input to the computational logic."	The term "Claim 1 sh "computat Plain-and-