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Shimoda et al.

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(54) DISPLAY PANEI	(54)	DISPL	AY PA	ANEL
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(51) **Int. Cl.**

H01J 1/62 (2006.01)

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Related U.S. Appl. No. 11/235,579, filed: Sep. 26, 2005; Inventors: Tomoyuki Shirasaki et al; Title: Display Panel.

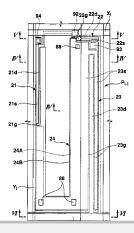
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(57) ABSTRACT

A display panel includes a transistor array substrate which has a plurality of transistors including at least a driving transistor, and a plurality of pixel electrodes electrically connected to the driving transistor of the plurality of transistors. A plurality of light-emitting layers are provided on the pixel electrodes. A counter electrode is provided on the light-emitting layers. Each of a plurality of interconnections is arranged between the pixel electrodes adjacent to each other and electrically connected to the counter electrode.

21 Claims, 21 Drawing Sheets





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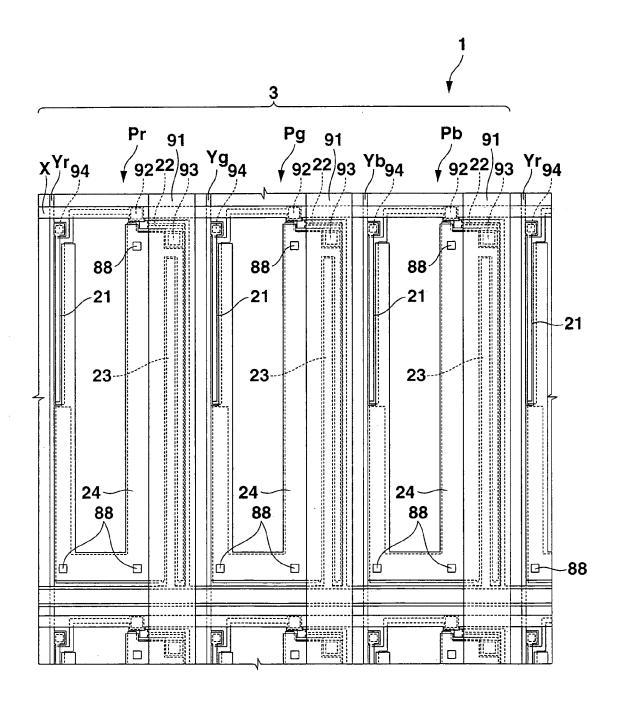


FIG.1



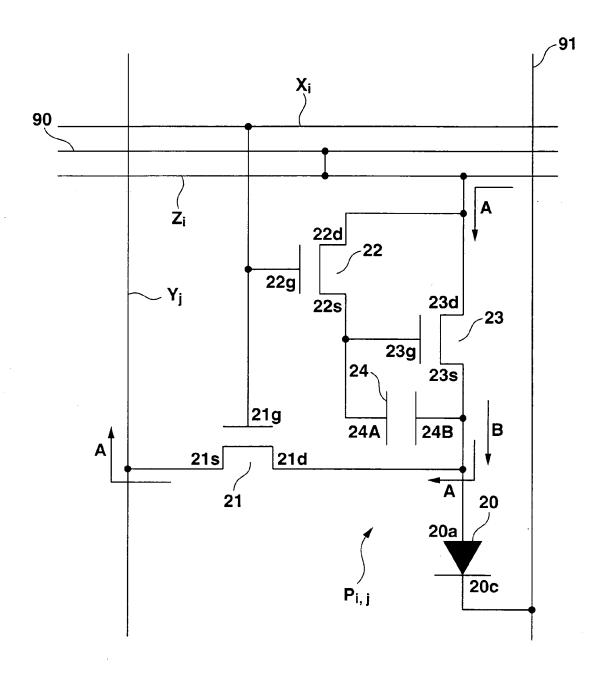
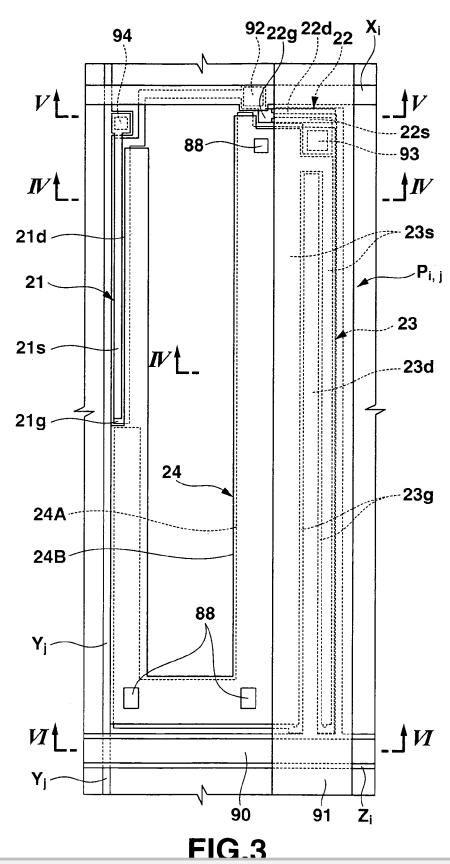


FIG.2







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