IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re <i>Inter Partes</i> Review of:)
U.S. Patent No. 7,573,068)
Issued: August 11, 2009)
Application No.: 11/232,368)
)

For: TRANSISTOR ARRAY SUBSTRATE AND DISPLAY PANEL

FILED VIA E2E

PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 7,573,068



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	3.	[1b]—"a plurality of driving transistors which are arrayed in a matrix on the substrate, each of the driving transistors having a gate, a source, and a drain, and a gate insulating film inserted between the gate, and the source and drain;"	29
	4.	[1c]—"a plurality of signal lines which are patterned together with the gates of said plurality of driving transistors and arrayed to run in a predetermined direction on the substrate"	35
	5.	[1d]—"a plurality of supply lines which are patterned together with the sources and drains of said plurality of driving transistors and arrayed to cross said plurality of signal lines via the gate insulating film, one of the source and the drain of each of the driving transistors being electrically connected to the supply lines"	39
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			different from a material film serving as a prospective pixel electrode and a material film serving as a prospective counter electrode and which is thicker than the gates of the driving transistors and the sources and drains of the driving transistors"	80



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