



US005360992A

United States Patent [19][11] **Patent Number:** **5,360,992****Lowrey et al.**[45] **Date of Patent:** **Nov. 1, 1994**[54] **TWO PIECE ASSEMBLY FOR THE SELECTION OF PINOUTS AND BOND OPTIONS ON A SEMICONDUCTOR DEVICE**[75] **Inventors:** **Tyler A. Lowrey**, Boise, Id.; **Alan R. Reinberg**, Westport, Conn.; **Kevin D. Martin**, Boise, Id.[73] **Assignee:** **Micron Technology, Inc.**, Boise, Id.[21] **Appl. No.:** **221,974**[22] **Filed:** **Apr. 1, 1994****Related U.S. Application Data**

[63] Continuation of Ser. No. 811,768, Dec. 20, 1991, abandoned.

[51] **Int. Cl.⁵** **H01L 23/48**; H01L 29/44;
H01L 29/52; H01L 29/60[52] **U.S. Cl.** **257/666**; 257/698;
257/692[58] **Field of Search** 257/691, 698, 692, 693,
257/690, 666[56] **References Cited****U.S. PATENT DOCUMENTS**

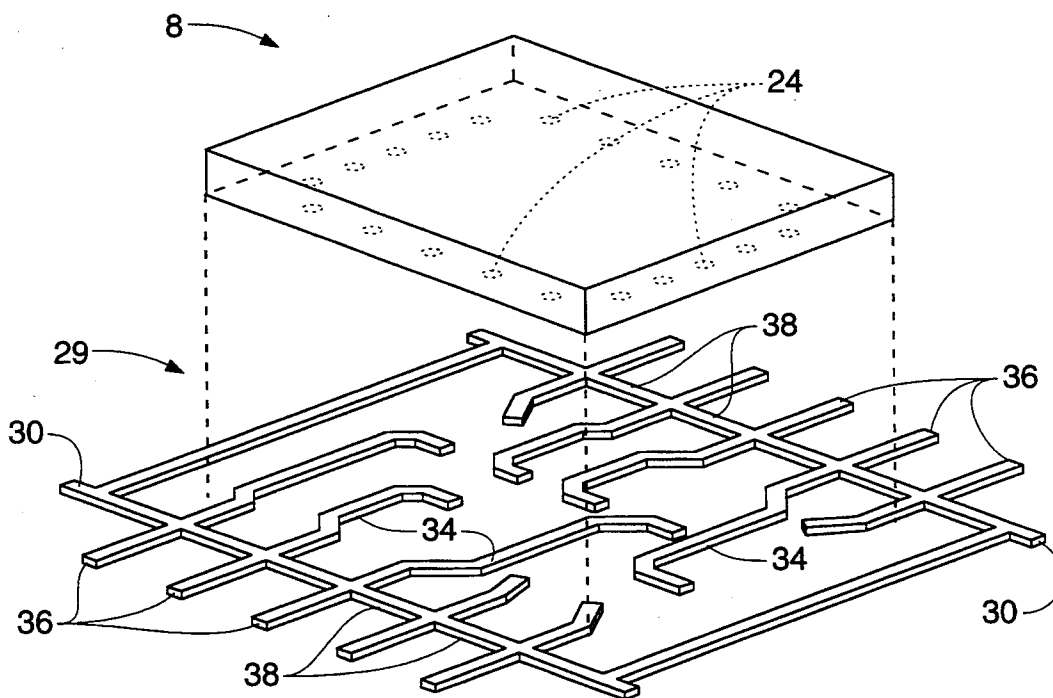
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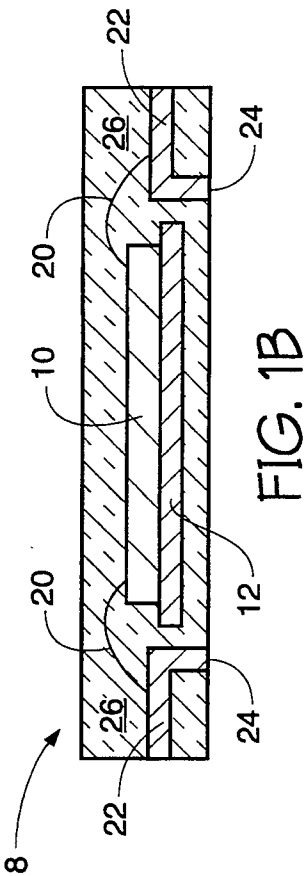
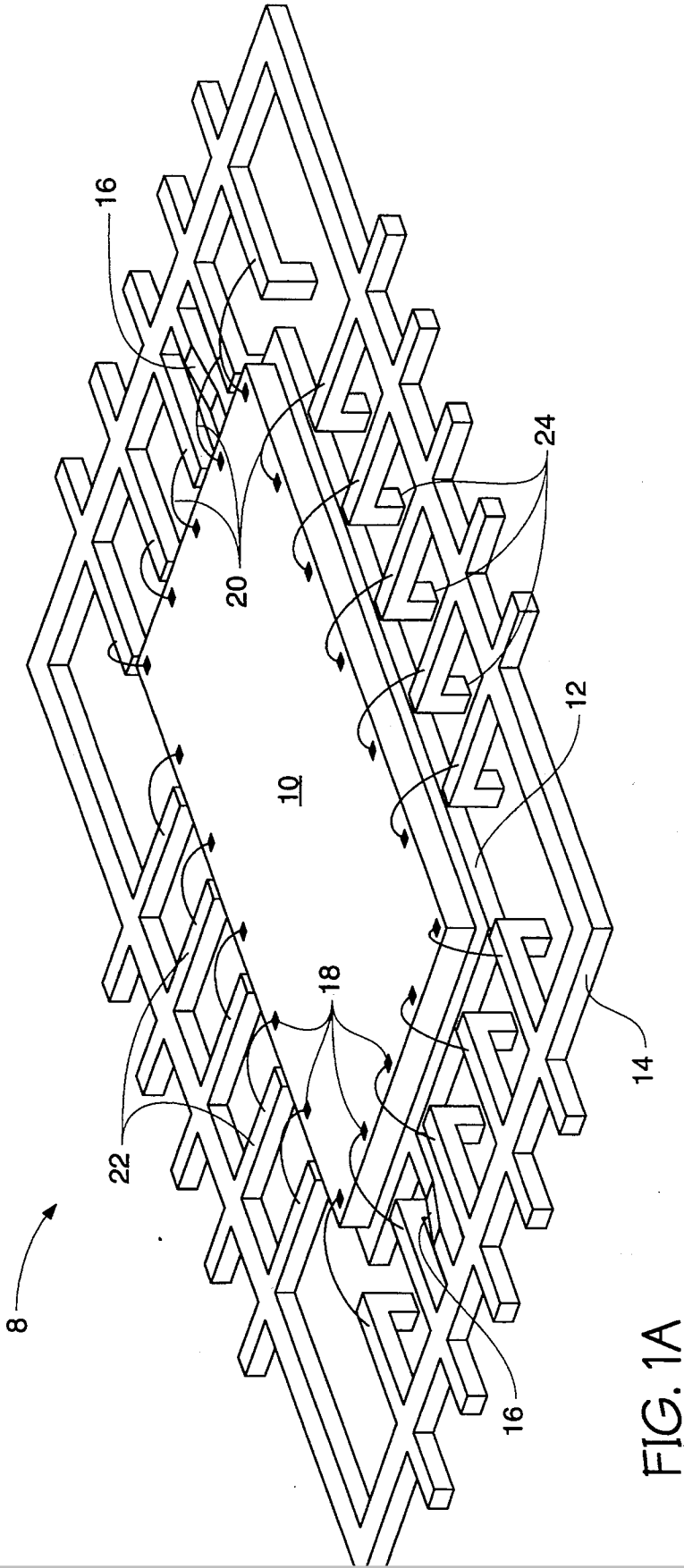
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Primary Examiner—Andrew J. James*Assistant Examiner*—S. V. Clark*Attorney, Agent, or Firm*—Kevin D. Martin[57] **ABSTRACT**

The invention comprises a semiconductor package which allows pinouts and bond options to be customized after the encasement of a die in plastic, ceramic, or other suitable materials. A first embodiment of the invention has a first assembly comprising an encapsulated die having bond pads connected to bond wires which terminate in exterior pad portions on the exterior of the encapsulant. Conductive paths which are part of a second assembly electrically connect with the exterior pad portions of the first assembly and pass signals to device pinouts, which can be leads or other connecting means, to an electronic device into which the module is installed. By selectively connecting the exterior pad portions of the first assembly to the connection points of the conductive paths of the second assembly, the device pinouts and bond options can be selected. To manufacture a device having different pinouts or bond options, a bottom section having a different design is used.

20 Claims, 7 Drawing Sheets



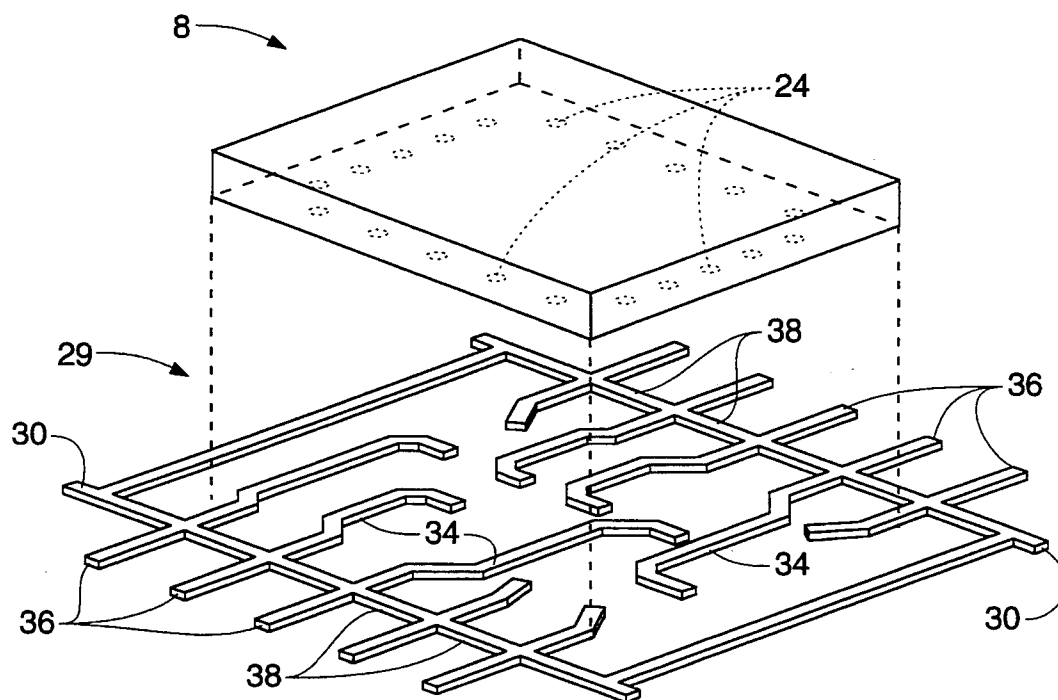


FIG. 2A

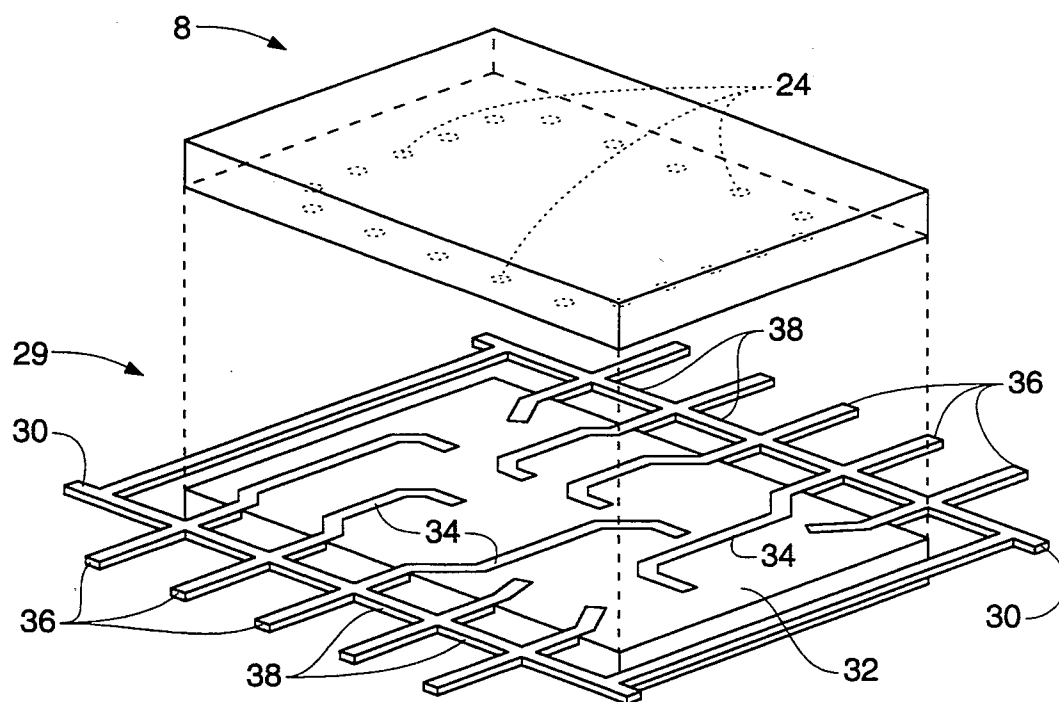


FIG. 2B

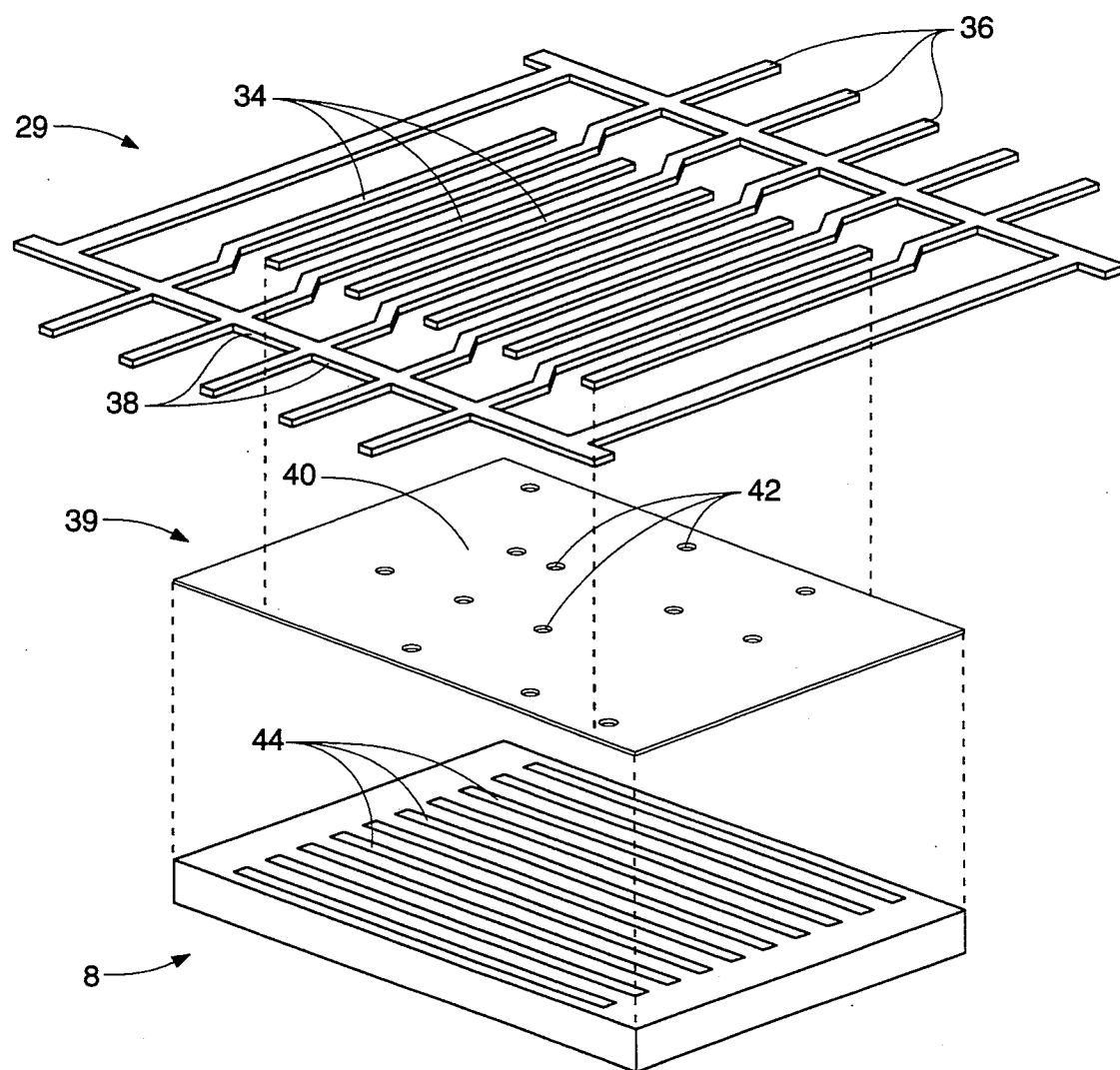


FIG. 3

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