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United States Patent [19]

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[11]

[54] PAD INPUT SELECT CIRCUIT FOR USE WITH BOND OPTIONS

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[51] **Int. Cl.**⁶ **G06F 12/00**; G11C 7/00

U.S. Cl. 711/170; 365/189.02; 365/230.02 [52]

[58] 365/230.02; 395/653

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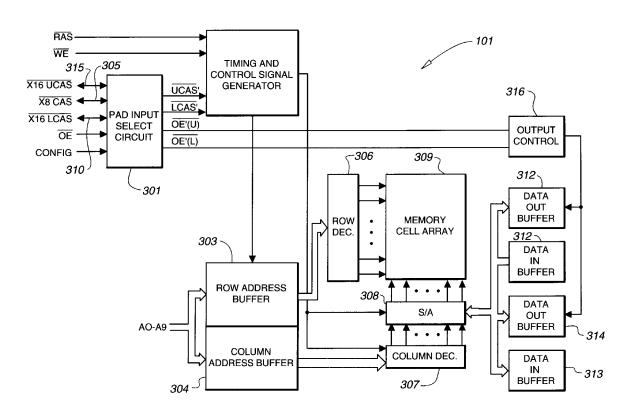
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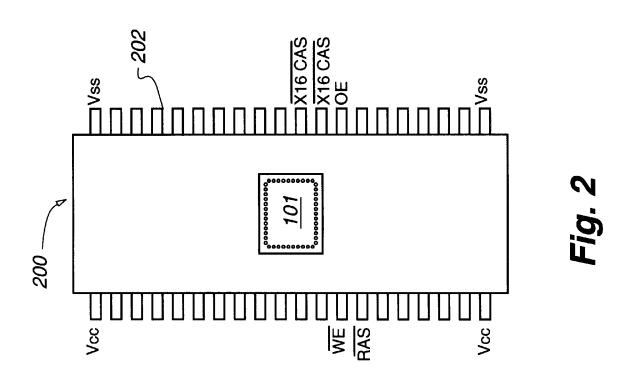
[57] **ABSTRACT**

A configurable input device for an integrated circuit having a plurality of input pads, the input device including a plurality of buffers, where each buffer is associated with one of the input pads. Each buffer receives a mode select signal and the buffer is responsive to the mode select signal to place the buffer in an enabled mode or a disabled mode. A receiver portion within each buffer is coupled to the associated input pad. The receiver portion pulls the associated input pad to a preselected logic state while the buffer is in the disabled mode. An output driver within each buffer generates an output signal responsive to a signal on the associated input pad while the buffer is in the enable mode and provides a high impedance while the buffer is in the disabled mode. An output node is coupled to the output drivers of the plurality of buffers.

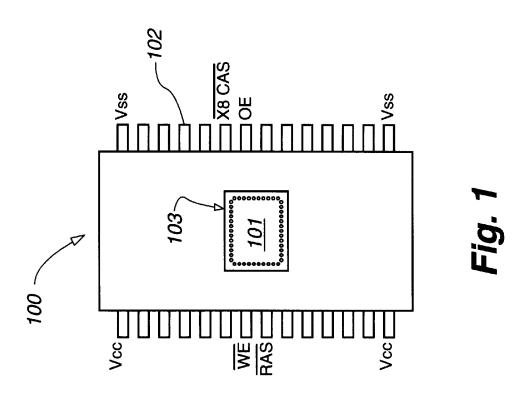
18 Claims, 5 Drawing Sheets



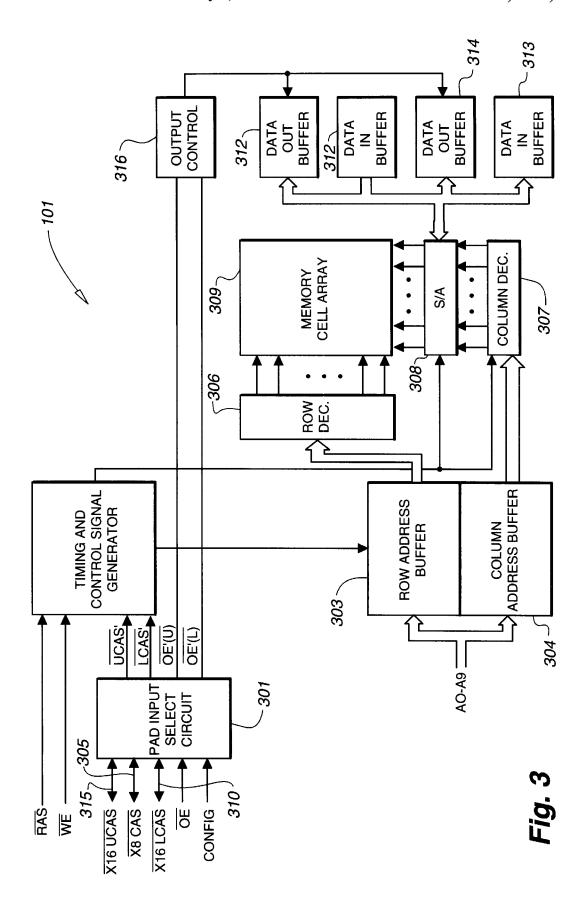


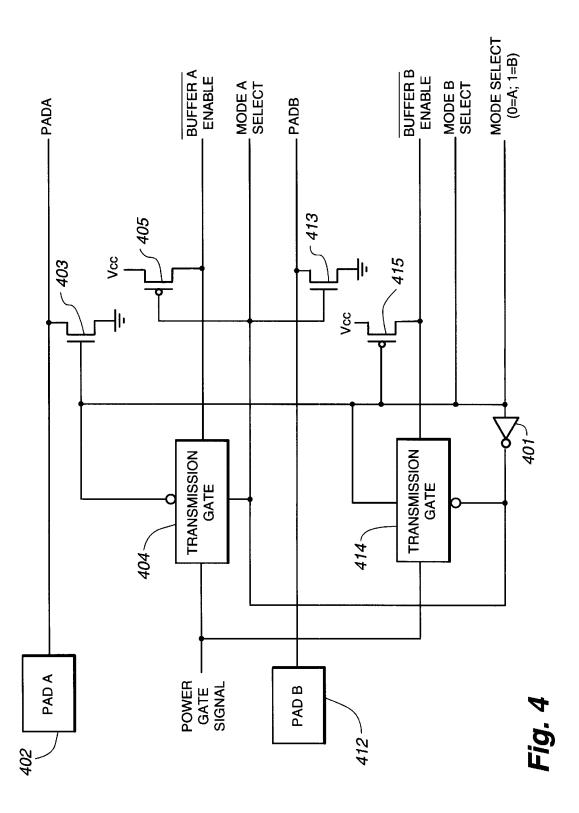


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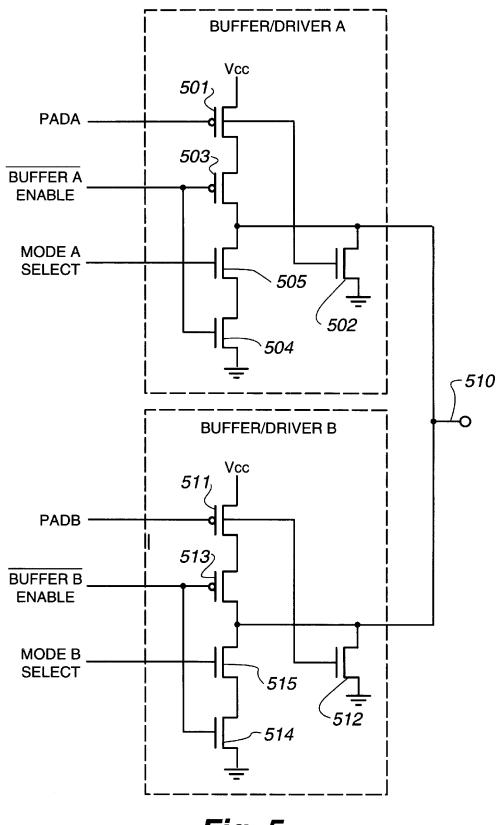


Fig. 5

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