UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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ADVANCED MICRO DEVICES, INC.,
Petitioner

v.

MONTEREY RESEARCH, LLC,
Patent Owner

——————————————

Case IPR2020-00985

U.S. Patent No. 6,651,134

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PATENT OWNER SUR-REPLY

Mail Stop Patent Board
Patent Trial and Appeal Board
U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

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PATENT OWNER'S EXHIBIT LIST

Exhibit No.	DESCRIPTION
2001	U.S. Patent No. 5,729,504 to Cowles ("Cowles")
2002	U.S. Patent No. 6,289,138 to Yip ("Yip")
2003	Declaration In Support Of Patent Owner Monterey Research, LLC's Unopposed Motion For Admission *Pro Hac Vice* Of Michael A. Wueste.
2004	Declaration Of Michael C. Brogioli, Ph.D.
2005	Curriculum Vitae Of Michael C. Brogioli, Ph.D.
2006	2021-02-12 Deposition Transcript of Dr. R. Jacob Baker.
2007	Excerpts Of R. JACOB BAKER ET AL., DRAM CIRCUIT DESIGN FUNDAMENTAL AND HIGH-SPEED TOPICS, IEEE Press (2008)
2008	https://www.transcend-info.com/Support/FAQ-296
2009	PCI Local Bus Specification, Production Version Revision 2.1 (June 1, 1995)
2010	JEDEC Standard – Double Data Rate (DDR) SDRAM, JESD79F, JEDEC Solid State Technology Association.
2011	JEDEC Standard – DDR2 SDRAM Specification, JESD79-2B, JEDEC Solid State Technology Association.
2012	JEDEC Standard – Low Power Double Data Rate 3 (LPDDR3), JESD209-3, JEDEC Solid State Technology Association.
2013	JEDEC Standard – Low Power Double Data Rate 4 (LPDDR4), JESD209-4, JEDEC Solid State Technology Association.
2014	JEDEC Standard – DDR3 SDRAM Standard, JESD79-3F, JEDEC Solid State Technology Association.
2015	JEDEC Standard – DDR4 SDRAM, JESD79-4B, JEDEC Solid State Technology Association.
2016	JEDEC Standard – Graphics Double Data Rate (GDDR5) SGRAM Standard, JESD212C, JEDEC Solid State Technology Association.
2017	JEDEC Standard – Graphics Double Data Rate (GDDR5X) SGRAM Standard, JESD232A, JEDEC Solid State Technology Association.
2018	JEDEC Standard – Graphics Double Data Rate (GDDR6) SGRAM Standard, JESD250B, JEDEC Solid State Technology Association.
2019	https://www.intel.com/pressroom/kits/quickrefyr.htm
2020	https://web.archive.org/web/20000818114318/http://www.cypress.com/press/releases/990726.html

All citations to specific pages of exhibits follow the pagination added to those exhibits per 37 C.F.R. § 42.63(d)(2)(i).

All emphasis added unless specified otherwise.

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