JEDEC STANDARD

Low Power Double Data Rate 3 (LPDDR3)

JESD209-3

MAY 2012

JEDEC SOLID STATE TECHNOLOGY ASSOCIATION

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Published by

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LOW POWER DOUBLE DATA RATE 3 SDRAM (LPDDR3)

(From JEDEC Board ballot JCB-12-22, formulated under the cognizance of the JC-42.6 Subcommittee on Low Power Memory.)

1 Scope

This document defines the LPDDR3 specification, including features, functionalities, ACand DC characteristics, packages, and ball/signal assignments. The purpose of this specification is to define the minimum set of requirements for JEDEC compliant 4 Gb through 32 Gb for x16 and x32 SDRAM devices. This specification was created using aspects of the following specifications: DDR2 (JESD79-2), DDR3 (JESD79-3), LPDDR (JESD209), and LPDDR2 (JESD209-2). Each aspect of the specification was considered and approved by committee ballot(s). The accumulation of these ballots was then incorporated to prepare the LPDDR3 specification.

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