

Martin G. Walker, PhD

Consultant Curriculum Vitae

Expertise

IC Design

- EDA Software
- Digital circuit design and simulation
- Analog IC design and analysis
- Analog circuit design
- RF and microwave circuit design

Source Code

- Identification of copied source code
 - Analysis of copied source code
 - Analysis of alleged trade secret misappropriation
 - Patent infringement of source code
 - Copyright infringement of source code, including database schemas
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Litigation Experience

Dr. Walker has testified in jury trials and arbitration involving software technology issues, including testifying in federal court and the ITC on matters relating to infringement and validity of various patents as well as misappropriation of trade secrets. He has been engaged as a “technology neutral” to assist in the resolution and adjudication of electronic discovery issues. Dr. Walker is particularly adept at explaining complex technology issues to lay audiences and, as such, has often been asked to present Markman tutorials and prepare demonstratives for arguments as well as his testimony. He regularly works with attorneys to help articulate and manage the many technology issues that arise in complex litigation.

Industry Experience

Dr. Walker is a recognized expert with over 40-years of experience in design of integrated circuits, including analog, digital and RF/microwave circuits. His analog IC circuit design experience includes analog components such as PLLs, voltage regulators/controllers and linear systems. In the digital IC design Dr. Walker has experience at the circuit level, including such structures as ESD components, level shifters, sense amps, SDRAM circuits such as DDR sequencing circuits and On-Die Termination (ODT). His RF/Microwave design experience includes microwave components (such as amplifiers, oscillators, mixers and switches) as well as sub-systems (tuners, low-noise down converters, etc).

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Dr. Walker has over 25 years experience in electronic design automation (EDA) software systems, simulation and circuit design and modeling. He is a recognized expert in the field of EDA systems, simulation, and modeling. He also has experience with the design of digital and analog IC, with verilog and VHDL analysis, synthesis of digital ICs such as flash memory controllers, analyses and verification of metal interconnects and related process technology.

During his consulting career, Dr. Walker has gained experience with smart-phone applications and system-level programming and integration with underlying hardware. For example, he has analyzed operation of Android and iOS systems to determine their functionality and analyze whether the functionality meets the claim limitations of cellular technology patents. For example, he has analyzed WiFi and cellular data communications, including the interfaces between the handset operating system and the WiFi and cellular chip sets.

Additionally, Dr. Walker has analyzed the operation of cellular modems, including hardware, firmware, and software to determine the operation of the modems as they related to various 3GPP, CDMA2000, and LTE cellular communication standards.

During the course of his practice, Dr. Walker has analyzed such diverse code as EDA software, VoIP software, database schema, mail-list processing software, speech-recognition systems and stock-brokerage software. He is familiar with a wide variety of source-code repositories including CVS, RCS, Subversion, Perforce, and Rational ClearCase.

Dr. Walker has also performed numerous analyses of systems-on-a-chip. The operation of such ICs is often defined by a combination of hardware and software. The hardware is usually specified at the RTL level in Verilog or VHDL. This hardware is often controlled by software running on an embedded processor, such as an ARM. He has been asked to understand, analyze, and document the operation of the system in the context of patent infringement or (in the case of potential prior art) invalidity. For example, he has investigated implementation of signal processing algorithms that implement 3GPP cellular telephone standards on baseband modem ICs.

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Litigation Projects

Matter	Sequence Design vs various defendants
Description	Patent infringement – pre-litigation investigation (EDA Software)
Law Firm	Thelen Reid
Client	Sequence Design
Status	Settled
Dates	2000-02
Matter	Silvaco v CSI
Description	Misappropriation of Trade Secrets; source code analysis (EDA Software)
Law Firm	Dechert, LLP
Client	Silvaco
Status	Settled
Dates	2001-03
Testimony	Multiple depositions; Testified at arbitration; Testified at trial
Matter	IRS v Taxpayer
Description	Analysis for tax purposes of EDA software valuation and associated royalty streams. Provided expert report.
Law Firm	FTI Teklicon (for IRS)
Client	IRS
Status	Completed
Dates	2001-02
Matter	Synopsys vs Nassda
Description	Patent infringement; misappropriation of trade secrets (EDA Software). Provided expert report and declarations. Analyzed evidence of electronic document spoliation.
Law Firm	Dechert LLP
Client	Synopsys
Status	Settled
Dates	2001-04
Testimony	Deposition
Matter	Après v Ho
Description	Misappropriation of trade secrets related to EDA software
Law Firm	Law offices of Al Reynaldo (for cross-defendant Après)
Client	Après
Status	Settled
Dates	2003
Testimony	Trial
Matter	HCL vs eKomas

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Description Analysis and identification of directly copied source code written in a web-host scripting language. The product at issue is a web-based application for loan management.

Law Firm Dechert
Client HCL
Status Settled
Dates 2003-04

Matter Tera Systems vs InTime Software
Description Patent infringement related to EDA software. Researched invalidity and supported claim construction.

Law Firm McDermott
Client InTime Software
Status Settled
Dates 2004

Matter Siliconix vs AATI
Description Patent infringement regarding method for manufacturing a semiconductor device. Support for claim construction and invalidity analysis. Researched prior art.

Law Firm Dechert LLP
Client AATI
Status Settled
Dates 2004-05

Matter Berry vs Fleming, et al.
Description Source code copyright and misappropriation of trade secrets of an SQL database schema as well as associated source code. Reviewed source code at issue. Provided expert report relating to software development, non-infringement of copyright, and analysis of forensic data to refute claims of electronic evidence spoliation.

Law Firm Kirkland
Client Flemming and other Defendants
Status Completed - Summary judgment of non-infringement of copyright for more than 90% of potential damages. Jury verdict awarding minimal damages for balance of claims.

Dates 2005

Matter Synopsys vs Magma
Description Theft of IP relating to EDA software as well as patent infringement in two venues. Analyzed Magma software written mostly in C++ for infringement identified historical software supporting invalidity of Magma patents. Provided support for claim construction and invalidity analysis.

Law Firm Dechert/Wilson Sonsini
Client Synopsys

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Status	Settled.
Dates	2005
Matter	Keywords vs ISE
Description	Investigated allegations of source code copying and copyright infringement of source code written in an interpreted language similar to perl. Declaration ISO of opposition to preliminary injunction.
Client	ISE
Status	Settled.
Dates	2005
Matter	Experian v. I-Centrix
Description	Investigating allegations of source code copying and misappropriation of trade secrets. The code was originally written in Fortran, but then translated into the C programming language. Performed analysis of electronic data that suggested evidence tampering on the part of defendants. Expert Report documenting identification of literal copying of source code as well as copied algorithms. Also identified methods the defendants used in an attempt to disguise the misappropriated code and algorithms.
Law Firm	Jones Day
Client	Experian
Status	Settled.
Dates	2005-06
Testimony	Deposition
Matter	FIS v. CalAmp
Description	Investigation of alleged misappropriation of trade secrets. Performed investigation and created expert report regarding the alleged misappropriation.
Law Firm	Gordon Rees
Client	CalAmp
Status	Settled.
Dates	2006
Matter	LiveOps v. Teleo
Description	Misappropriation of trade secrets, copyright infringement of VoIP software systems. Investigation to detect software copying.
Law Firm	Gordon Rees
Client	LiveOps
Status	Settled.
Dates	2006

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