

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SAMSUNG ELECTRONICS CO., LTD.
Petitioner

v.

BELL NORTHERN RESEARCH, LLC
Patent Owner

Patent No. 8,416,862

**PETITION FOR *INTER PARTES* REVIEW
OF U.S. PATENT NO. 8,416,862**

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Ex. 1008	Roh <i>et al.</i> , “An Efficient Feedback Method for MIMO Systems with Slowly Time-Varying Channels,” volume 2 of <i>Proceedings of 2004 IEEE Wireless Communications and Networking Conference</i> , March 21-25, 2004, Atlanta, GA (“Roh”)
Ex. 1009	U.S. Patent No. 7,570,696 to Maltsev <i>et al.</i> (“Maltsev”)
Ex. 1010	Haykin <i>et al.</i> , <u>Modern Wireless Communications</u> (“Haykin”)
Ex. 1011	Yang <i>et al.</i> , “Reducing the Computations of the SVD Array Given by Brent and Luk,” <i>Proceedings of SPIE</i> , vol. 1152, <i>Advanced Algorithms and Architectures for Signal Processing IV</i> , November 14, 1989 (“Yang”)
Ex. 1012	U.S. Patent No. 7,492,829 to Lin <i>et al.</i> (“Lin”)
Ex. 1013	Sadrabadi <i>et al.</i> , “A New Method of Channel Feedback Quantization for High Data Rate MIMO Systems,” volume 1 of <i>GLOBECOM '04 IEEE Global Telecommunications Conference</i> , November 29 – December 3, 2004, Dallas, Texas (“Sadrabadi”)
Ex. 1014	[RESERVED]
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Ex. 1017	U.S. Patent No. 7,742,546 to Ketchum <i>et al.</i> (“ <i>Ketchum-546</i> ”)
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Ex. 1021	Excerpt of <u>The Authoritative Dictionary of IEEE Standard Terms</u> (7 th ed., IEEE Press 2000)
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Ex. 1029	U.S. Patent No. 5,986,973 to Jericevic <i>et al.</i> (“ <i>Jericevic</i> ”)
Ex. 1030	Yang <i>et al.</i> , “Reducing the Computations of the Singular Value Decomposition Array Given by Brent and Luk,” <i>J. Matrix Anal. Appl.</i> , Vol. 12, No. 4, pp. 713-725, Oct. 1991 (“ <i>Yang II</i> ”)
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Ex. 1035	U.S. Patent Publication No. 2002/0081978 (“ <i>Hou</i> ”)
Ex. 1036	Steyskal, H., “Digital Beamforming Basics,” <i>Journal of Electronic Defense</i> (July 1996)
Ex. 1037	[RESERVED]
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Ex. 1040	Li, Q., & Lin, X. E., “Compact feedback for MIMO-OFDM systems over frequency selective channels,” in <i>2005 IEEE 61st Vehicular Technology Conference</i> , Vol. 1, pp. 187-191 (IEEE May 2005).
Ex. 1041	Delosme, J. M., “Bit-level systolic algorithm for the symmetric eigenvalue problem,” in <i>[1990] Proceedings of the International Conference on Application Specific Array Processors</i> , pp. 770-781 (IEEE September 1990).
Ex. 1042	Kota, K., <i>Architectural, numerical and implementation issues in the vlsi design of an integrated cordic-svd processor</i> (Doctoral dissertation, Rice University, 1991).
Ex. 1043	Kota, K., & Cavallaro, J. R., “A Normalization Scheme to Reduce Numerical Errors in Inverse Tangent computations on a Fixed-point CORDIC Processor,” in <i>IEEE International Symposium on Circuits and Systems (ISCAS)</i> , pp. 244-247 (May 1992).
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