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## About

Experienced systems engineer/technologist, with ~20 years of wireless and wired experience.

## Experience



### Sr. Systems Engineer

Intel Corporation

Jun 2012 – Present · 7 yrs 9 mos

santa clara, CA

Worked on wireless mmwave prototyping for backhaul use cases which is now being commercialized by Facebook, LED communication prototyping, and am now working on 3GPP 5G NR-U along with positioning technology. Worked on Matlab system level simulations that showed NR-U coexists with Wi-Fi. Submitted over 50 patents.



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synchronization measurements using Fine Timing Measurement (FTM) frames. Changed OFDM timing algorithm in all Wi-Fi chipsets to have optimal symbol timing. Worked with FPGA ...[see more](#)

### Chief Technology Officer

Upheels  
Oct 2011 – Apr 2012 · 7 mos  
San Francisco Bay Area

Lead a team of more than 10 engineers and built an e-commerce social network focused on high-end women's fashion.



### Principal Scientist

Broadcom Corporation  
Jan 2004 – Sep 2011 · 7 yrs 9 mos  
Sunnyvale, CA

Key system model contributor for 4321/4322/43236/5357/6362 chipsets.  
Aspects worked on: Channel modeling, interleaver design, MIMO channel estimation / tracking, optimal FFT window placement, beamforming matrix compression/decompression design, antenna selection and beamforming compression contributions to standard, radar detection design leading to first MIMO AP chipset to be DFS compliant, nonlinear amplifier modeling and nonlinear compensation for internal power amplifier design.

- Contributing member to EWC spec (from which current IEEE 802.11n standard is based)
- Generated various test vectors as well as RTL matching.
- Actively participated in the bringup efforts for various chipsets : 5357/43236/6362.

[see less](#)



### Systems Engineer

Solarflare Communications  
2002 – 2004 · 2 yrs  
Irvine, CA

- Worked on fixed point simulations and various signal processing algorithms including self-echo/next and alien next reduction. Actively contributed to 10GBase-T study group in IEEE.



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### Stanford University

Master's Degree, Electrical Engineering  
1996 – 1998



### California Institute of Technology

BS, Electrical Engineering  
1992 – 1996

## Volunteer Experience

### Volunteer interviewer

Incentive Awards Program  
Feb 2014 • 1 mo  
Education

Read and interview high school students applications towards IAP program. IAP provides up to \$32,000 in scholarship funds over eight (8) semesters, and student support services to low-income, first-generation college students who have demonstrated leadership potential, high academic achievement, and a commitment to serve others.

### Member Of The Supervisory Board

Intel latino network  
Oct 2016 – Present • 3 yrs 5 mos

## Skills & Endorsements

Digital Signal Processors · 37

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ASIC · 18

Stéphane Beaugregard and 17 connections have given endorsements for this skill

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## Recommendations

[Received \(1\)](#)

[Given \(2\)](#)



**Marc Bertola**

Director of Software  
Engineering

February 12, 2015, Carlos was senior to Marc but didn't manage directly

Carlos is the cornerstone of the indoor Wi-Fi positioning program at Qualcomm Atheros. Apart from his obvious role as an important author of the Fine Timing Measurement protocol in the 802.11mc draft, Carlos has provided all teams involved in its implementation with an important amount of technical information and... [See more](#)

## Accomplishments

**2** Languages ▾

English · Spanish

**2** Patents ▲

SYSTEMS AND METHODS OF OFFLOADED POSITIONING FOR DETERMINING LOCATION OF WLAN NODES

Filed May 8, 2014 · us 20140335891A1

Other inventors

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Other inventors

1

**Organization**  
IEEE TGaz



### Interests



**Wireless Communications & Mobil...**  
19,922 members



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2,429 members



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2,346 followers



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**802.11 Wireless professionals**  
12,610 members

**5G Radio Access Technology (RAT) ...**  
2,632 members

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