



(12) **United States Patent**  
**Gilbertson**

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(54) **SYSTEM AND METHOD FOR PROGRAMMABLY CONTROLLING DATA TRANSFER REQUEST RATES BETWEEN DATA SOURCES AND DESTINATIONS IN A DATA PROCESSING SYSTEM**

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\* cited by examiner

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(\*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: **09/218,211**

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(51) **Int. Cl.**<sup>7</sup> ..... **G06F 15/16**

(52) **U.S. Cl.** ..... **709/232; 709/233; 709/234; 709/235; 710/39; 710/60**

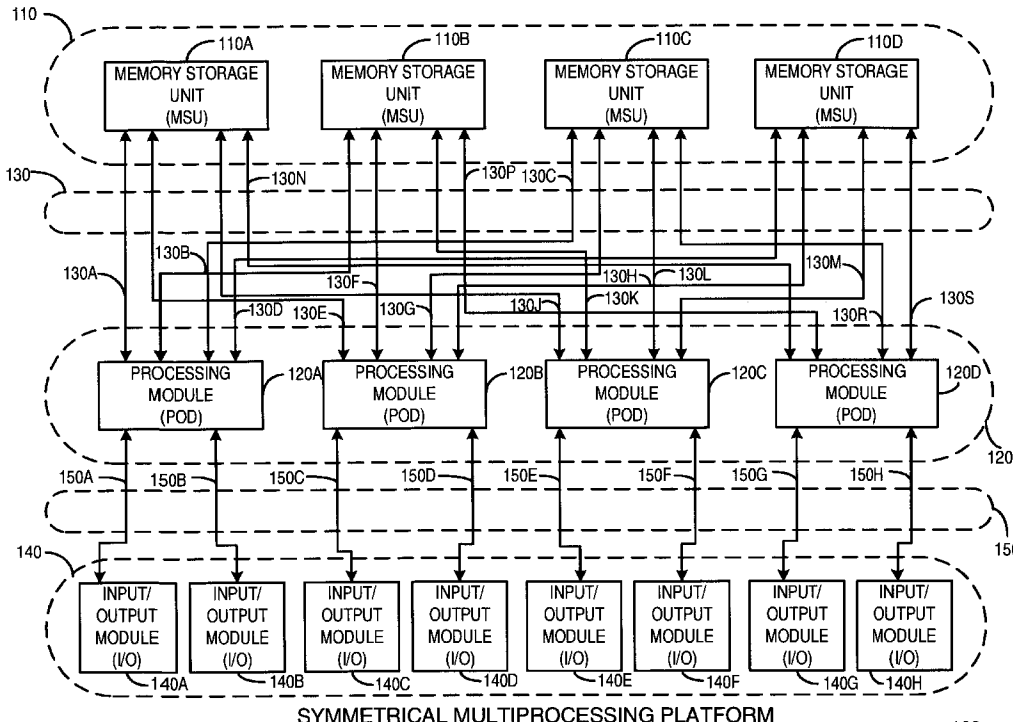
(58) **Field of Search** ..... 709/234, 200, 709/222, 252, 243, 207, 238, 235, 232, 233, 240; 710/263, 264, 244, 112, 116, 39, 40, 52, 58-60; 711/150, 151; 370/395, 414

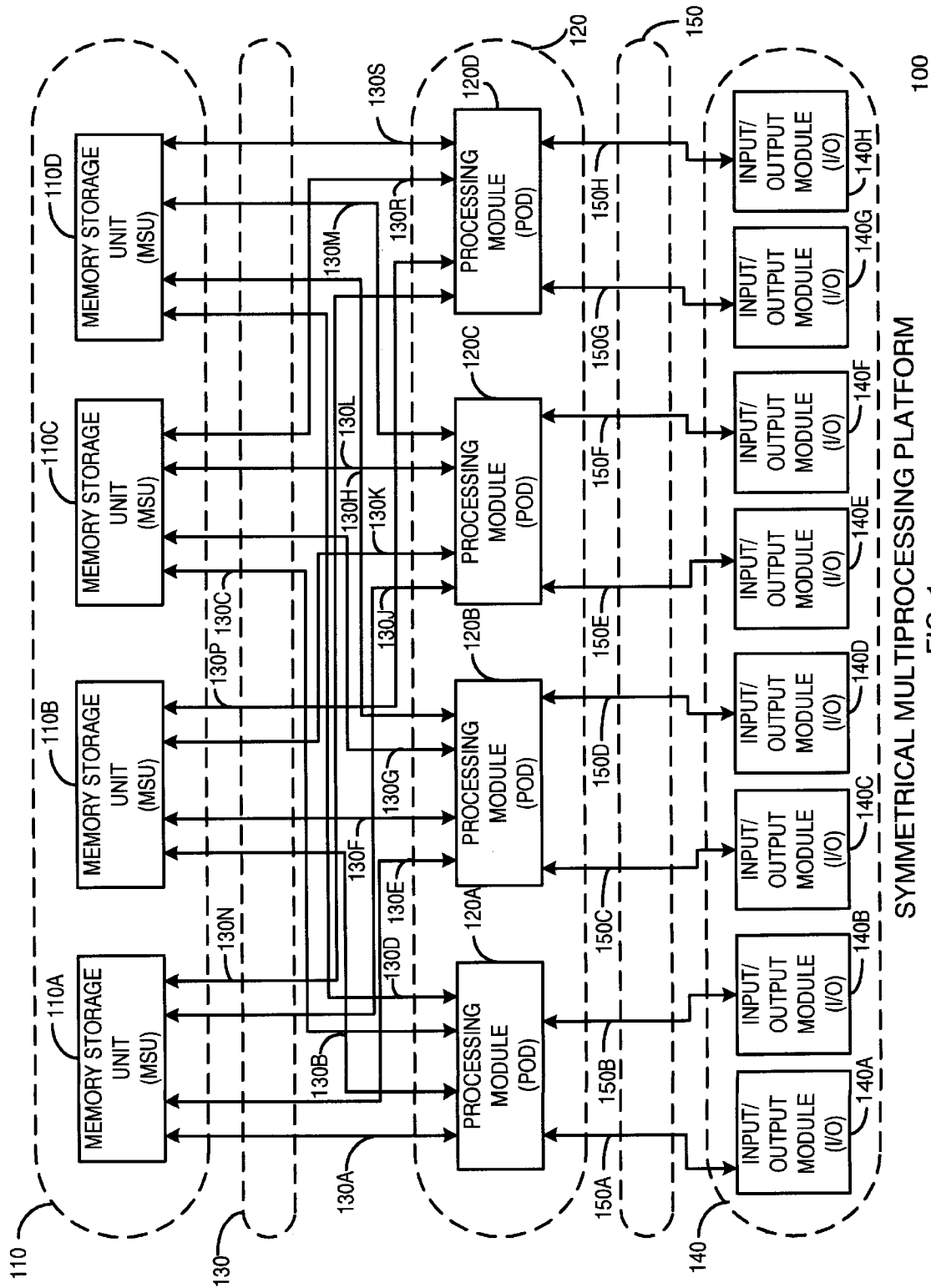
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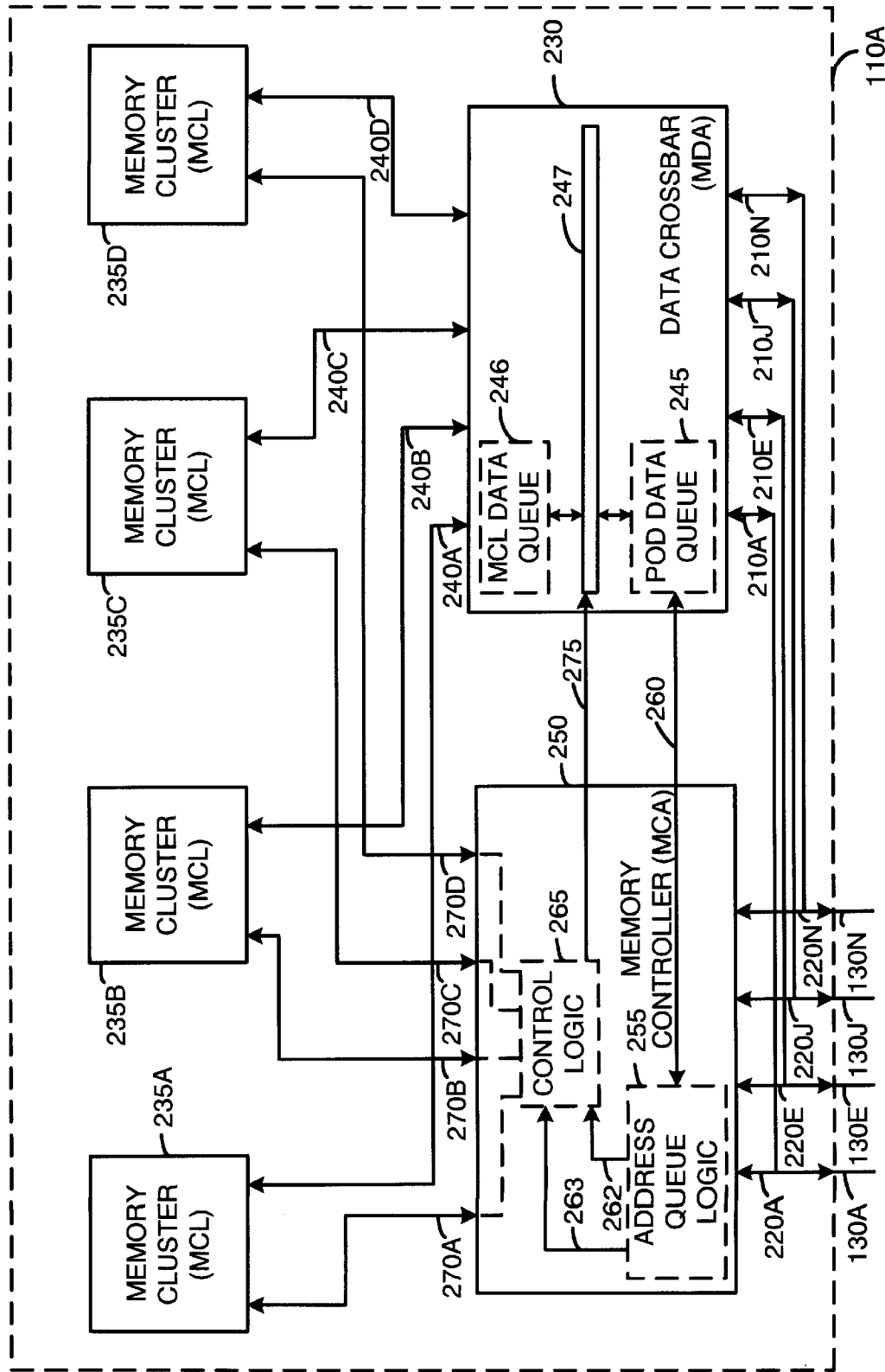
**33 Claims, 14 Drawing Sheets**



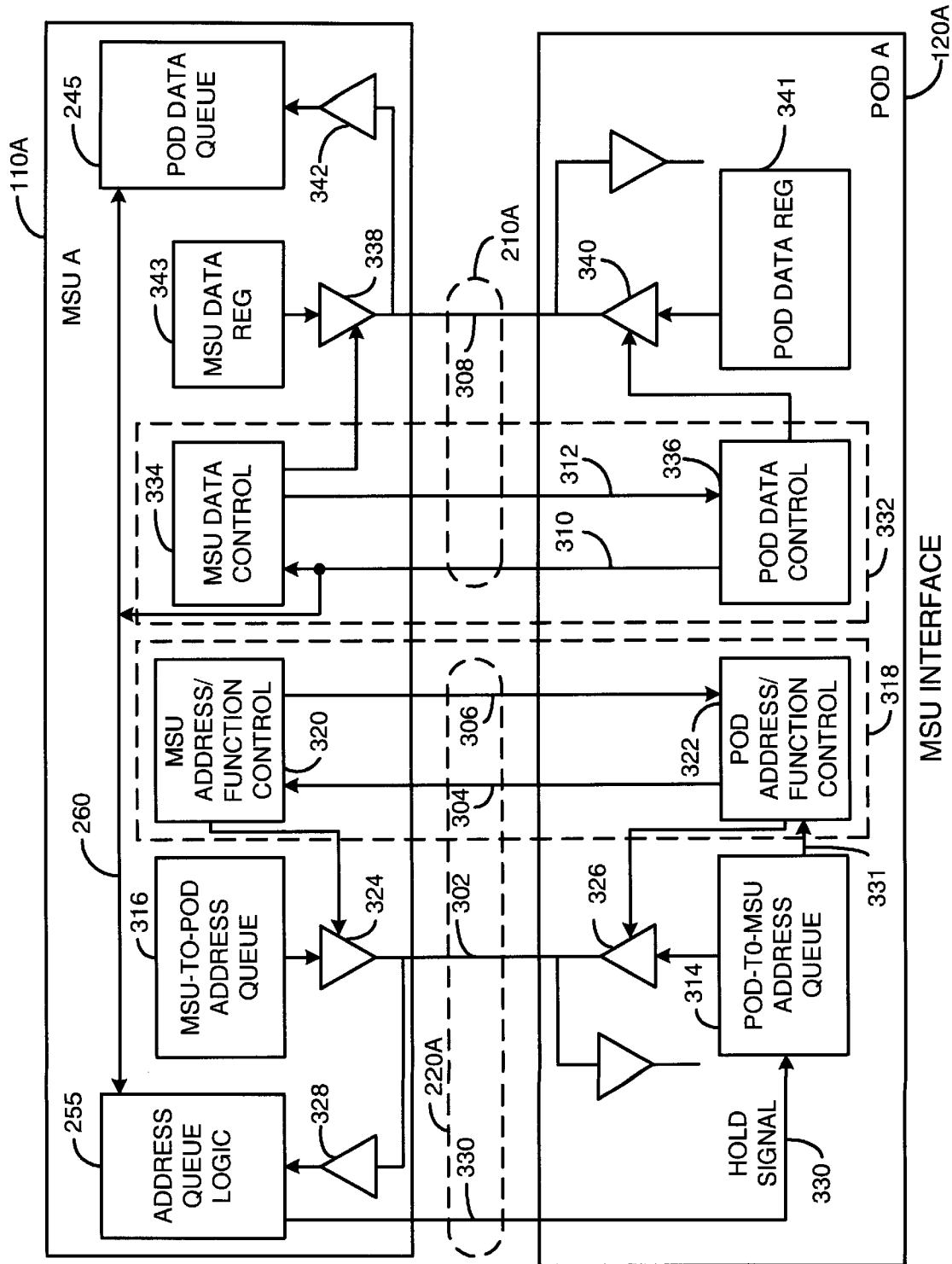


SYMMETRICAL MULTIPROCESSING PLATFORM

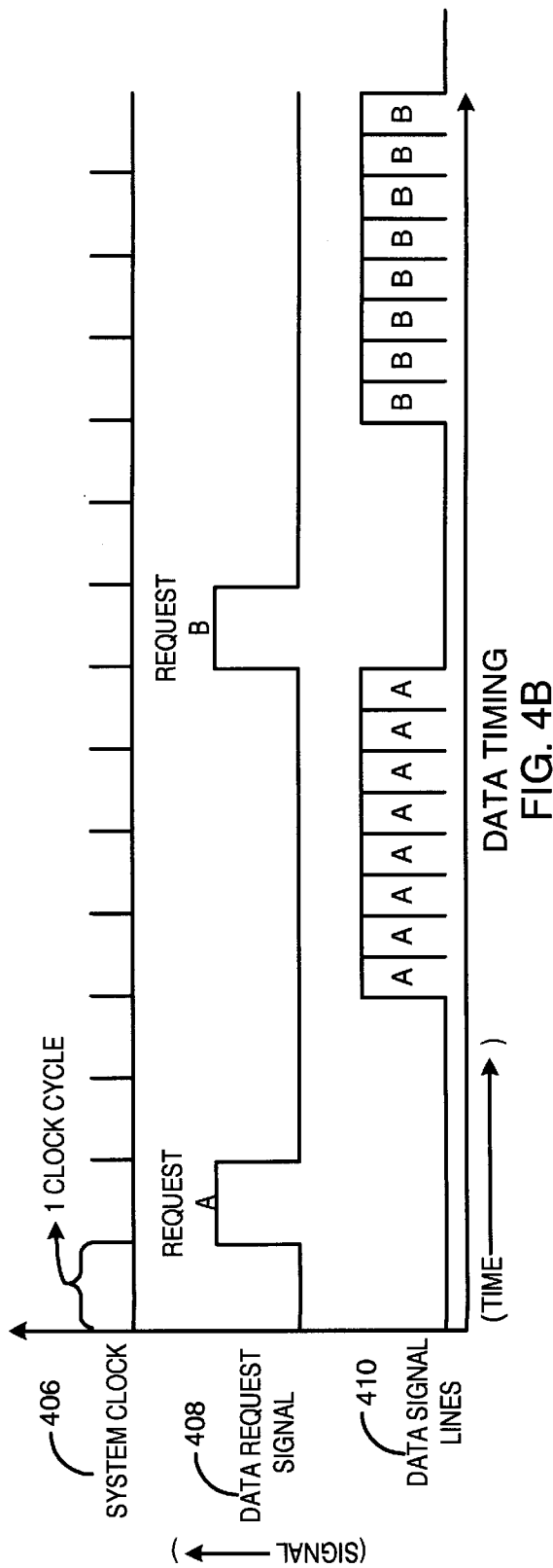
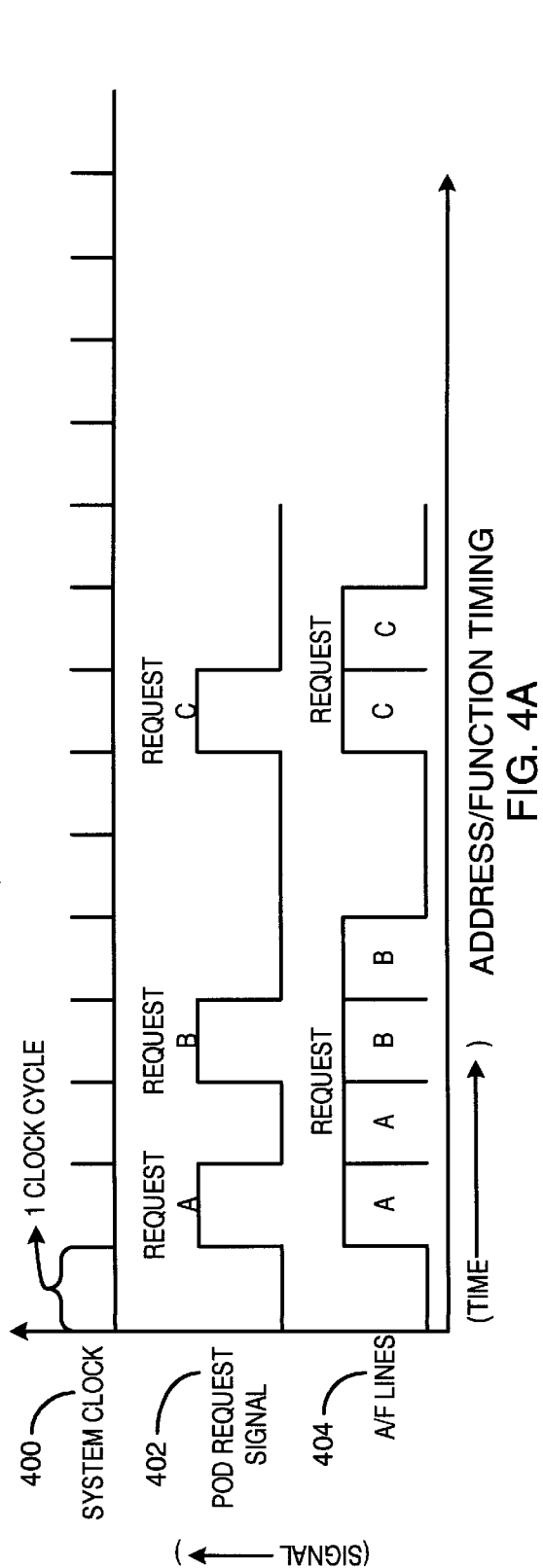
FIG. 1



MEMORY STORAGE UNIT (MSU)  
FIG. 2



MSU INTERFACE  
FIG. 3



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