

DECLARATION OF MIKE STRAWN

1. My name is Mike Strawn. I am over the age of twenty-one years, of sound mind, and capable of making the statements set forth in this Declaration. I am competent to testify about the matters set forth herein. All the facts and statements contained herein are within my personal knowledge.

2. I visited the University of Houston's M.D. Anderson Library ("Anderson Library") located at 4333 University Drive, Houston, Texas 77204 on January 22, 2020 and scanned certain pages from Proceedings 1997 IEEE Multi-Chip Module Conference ("IMCMC").

3. Anderson Library's call number for IMCMC is TK7874 .I3238 1997. Anderson Library had one copy of IMCMC, which was indexed and shelved as indicated by Anderson Library's online catalog, a true and correct copy of which is attached as Appendix A. In the copy of IMCMC, I scanned the cover, the table of contents, the "Date Due" slip, and the article titled "*Delay Models for MCM Interconnects When Response is Non-Monotone*" by Andrew B. Kahng, Kei, Masuko, and Sudhakar Muddu. A true and correct copy of these pages from IMCMC is attached as Appendix B.

4. I declare under penalty of perjury that the foregoing is true and correct.

Executed on January 22, 2020 in Houston, Texas, U.S.A.

By: Mike Strawn
Mike Strawn

Qualcomm Incorporated

APPENDIX A

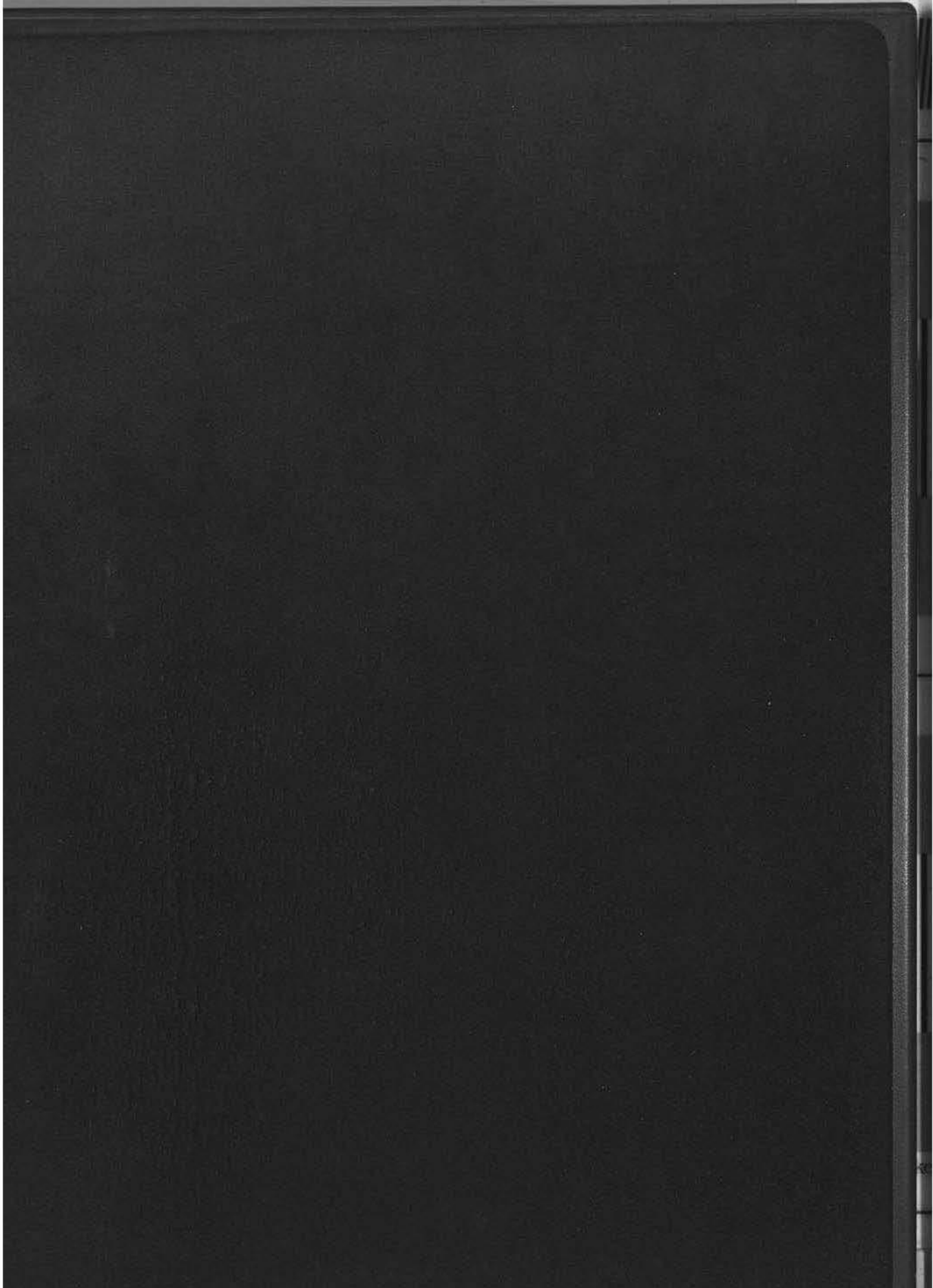
- **Creator:** IEEE Multi-Chip Module Conference (1997 : Santa Cruz, Calif.)
- **Contributor:** IEEE Computer Society.
- **Subject:** Multichip modules (Microelectronics) -- Congresses; Thin films; Neural networks (Computer science); CAD/CAM systems; Optoelectronic devices
- **Genre:** Congresses.
- **Contents:** Session 1. Flip-chip I -- Session II. Mixed signal MCMs -- Session III. MCM design and CAD -- Session IV: Panel: The best road to integration? Single chip or multi-chip? -- Session V: Interconnect analysis and simulation -- Session VI. Flip-chip II -- Session VII. Test, technology and infrastructure -- Session VIII: Optical MCMs -- Session IX: Wrap up panel -- Author index.
- **Other title:** 1997 IEEE Multi-Chip Module Conference.; IEEE Multi-Chip Module Conference.
- **Publisher:** Los Alamitos, Calif. : IEEE Computer Society Press
- **Creation Date:** ©1997
- **Format:** x, 171 pages : illustrations ; 29 cm.
- **Source:** Alma
- **Identifier:** LC: 96079698; ISBN: 0818677899 (softbound); ISBN: 0780339037 (casebound); OCLC: (OCoLC)36524697

Availability and location:

University of Houston:

- Available:
 - UH/MD Anderson Library Anderson General Collection TK7874 .I3238 1997

APPENDIX B



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