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**Please find below and/or attached an Office communication concerning this application or proceeding.**

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## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 37-40, 42-43 are rejected under 35 U.S.C. 102(e) as being anticipated by Yu (US Patent # 6, 625,150).

Regarding claim 37, Yu teaches a method, (Fig. 4) comprising: receiving data associated with a packet (column 6, lines 10-11); identifying one of a plurality of packet processors to receive the packet after the packet is entirely received (column 5, lines 3-7); temporarily buffering (410) the data until the packet is entirely received (column 6, line 50); transmitting the packet to the identified packet processor (column 6, lines 13-15).

Regarding claim 38, Yu teaches providing a plurality of queues (410) corresponding to the plurality of packet processors (412 of Fig. 4); and assigning the packet to one of the queues corresponding to the identified packet processor (column 6, lines 45-52).

Art Unit: 2416

Regarding claim 39, Yu teaches receiving a plurality of packets associated with a plurality of packet streams (column 6, lines 10-11); and providing one of the queues for combination of one of the packet processors and one of the packet streams (column 6, line 50).

Regarding claim 40, Yu teaches storing packets for the queues as a linked lists of data in a common memory (column 6, line 50).

Regarding claim 42, Bragg teaches maintaining values that reflect a flow of packets to each of the packet processors (column 6, lines 45-52).

Regarding claim 43, Yu teaches that identifying one of the plurality of packet processors (412) includes identifying one of the packet processors to receive the packet based on the maintained values (column 5, lines 3-7).

### ***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 25, 30, 31, 49 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bragg (US Patent # 6,587,469) in view of Yu (US Patent 6, 625,150).

Regarding claims 25, Bragg teaches a system (Fig. 2) comprising: a plurality of packet processors (25) to process packet (column 3, lines 33-38) bandwidth divider (24 of Fig. 2) to: receive data associated with a packet, buffer (36) the data until the packet

Art Unit: 2416

is entirely received (column 3, lines 44-45), identify (31) one of the packet processors to receive the packet based on a length of the packet after the packet is entirely received (column 3, lines 39-44). Bragg differs from the claimed invention in that Bragg fails to disclose transmit the packet to the identified packet processor. However, the Yu's reference figure 4 discloses transmit the packet to the identified packet processor (column 6, lines 13-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the teachings of Yu into the system of Bragg with motivation being that it provides capability for the system to enhance performance.

Regarding claim 30, Bragg teaches that the bandwidth divider is further configured to: maintain values that reflect a flow of packets to each of the packet processors (column 3, lines 66-67).

Regarding claim 31, Bragg teaches that the bandwidth divider is configured to identify one of the packet processors to receive the packet based on a length of the packet and the maintained values (column 3, lines 39-44).

Regarding claim 49, Bragg teaches a bandwidth divider (24 of Fig. 2) connected to a plurality of processors (25 corresponds to the claimed processors) comprising: means for monitoring flows of packets provided to the processors (column 1, line 63-column 2, lines 1-6); means for receiving data associated with a packet (column 3, lines 39-40); means for buffering the data until the packet is entirely received (column 3, lines 44-45). Bragg differs from the claimed invention in that Bragg fails to disclose transmit the packet to the identified packet processor. However, the Yu's reference figure 4 discloses transmit the packet to the identified packet processor (column 6, lines 13-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the teachings of Yu into the system of Bragg with motivation being that it provides capability for the system to enhance performance.

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