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(54) **DISPLAY PANEL**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 441 days.

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(Continued)

(51) **Int. Cl.**

H01J 1/62 (2006.01)

Primary Examiner—Aaron Williams

(52) **U.S. Cl.** **313/500**; 345/44; 345/45; 313/505; 315/169.3

(74) *Attorney, Agent, or Firm*—Frishauf, Holtz, Goodman & Chick, P.C.

(58) **Field of Classification Search** None
See application file for complete search history.

(57)

ABSTRACT

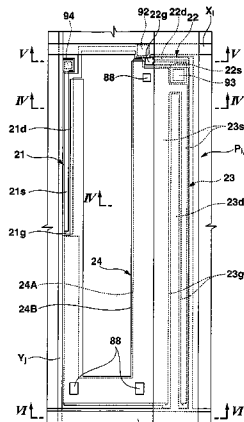
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A display panel includes a transistor array substrate which has a plurality of transistors including at least a driving transistor, and a plurality of pixel electrodes electrically connected to the driving transistor of the plurality of transistors. A plurality of light-emitting layers are provided on the pixel electrodes. A counter electrode is provided on the light-emitting layers. Each of a plurality of interconnections is arranged between the pixel electrodes adjacent to each other and electrically connected to the counter electrode.

21 Claims, 21 Drawing Sheets



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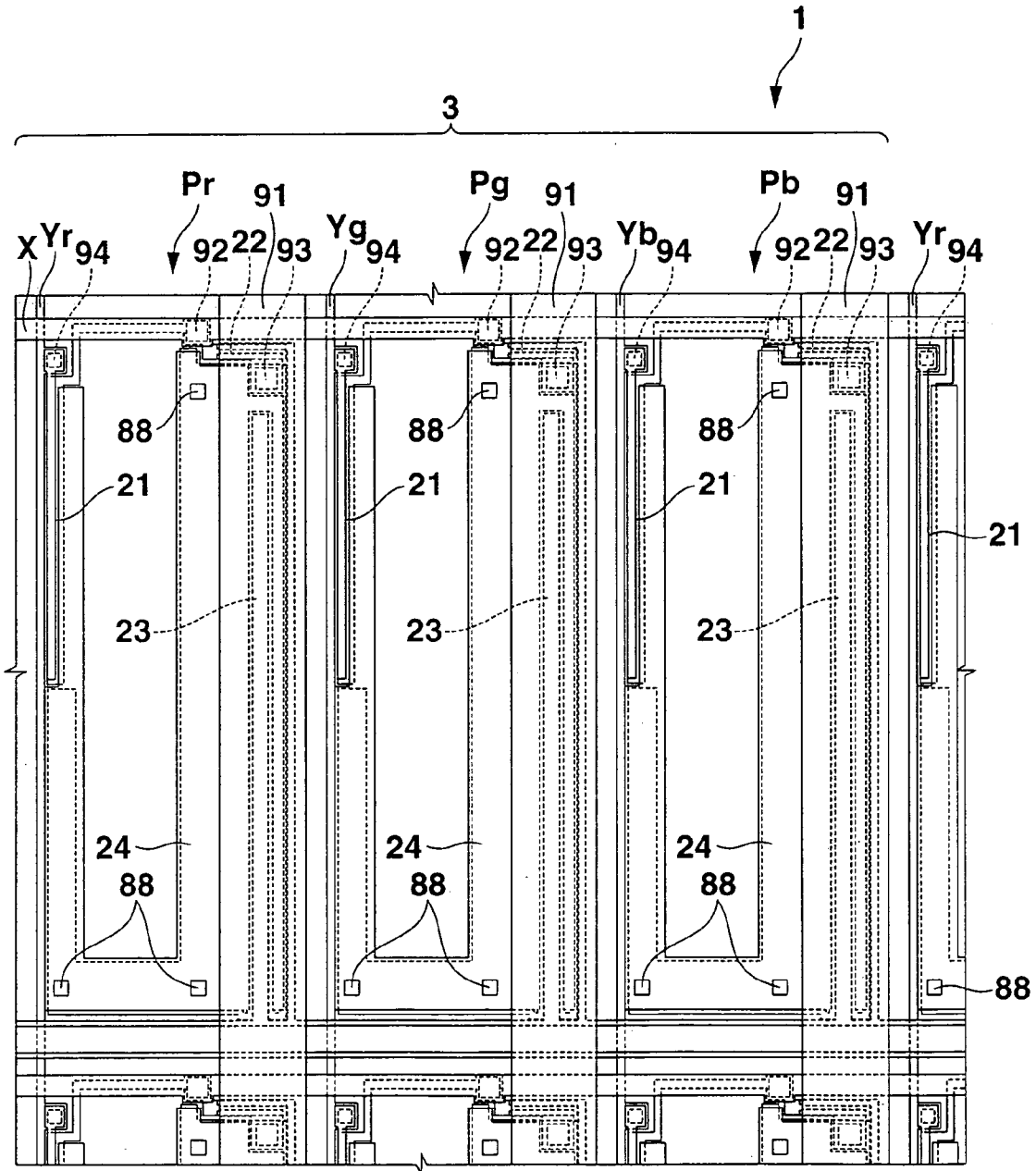


FIG. 1

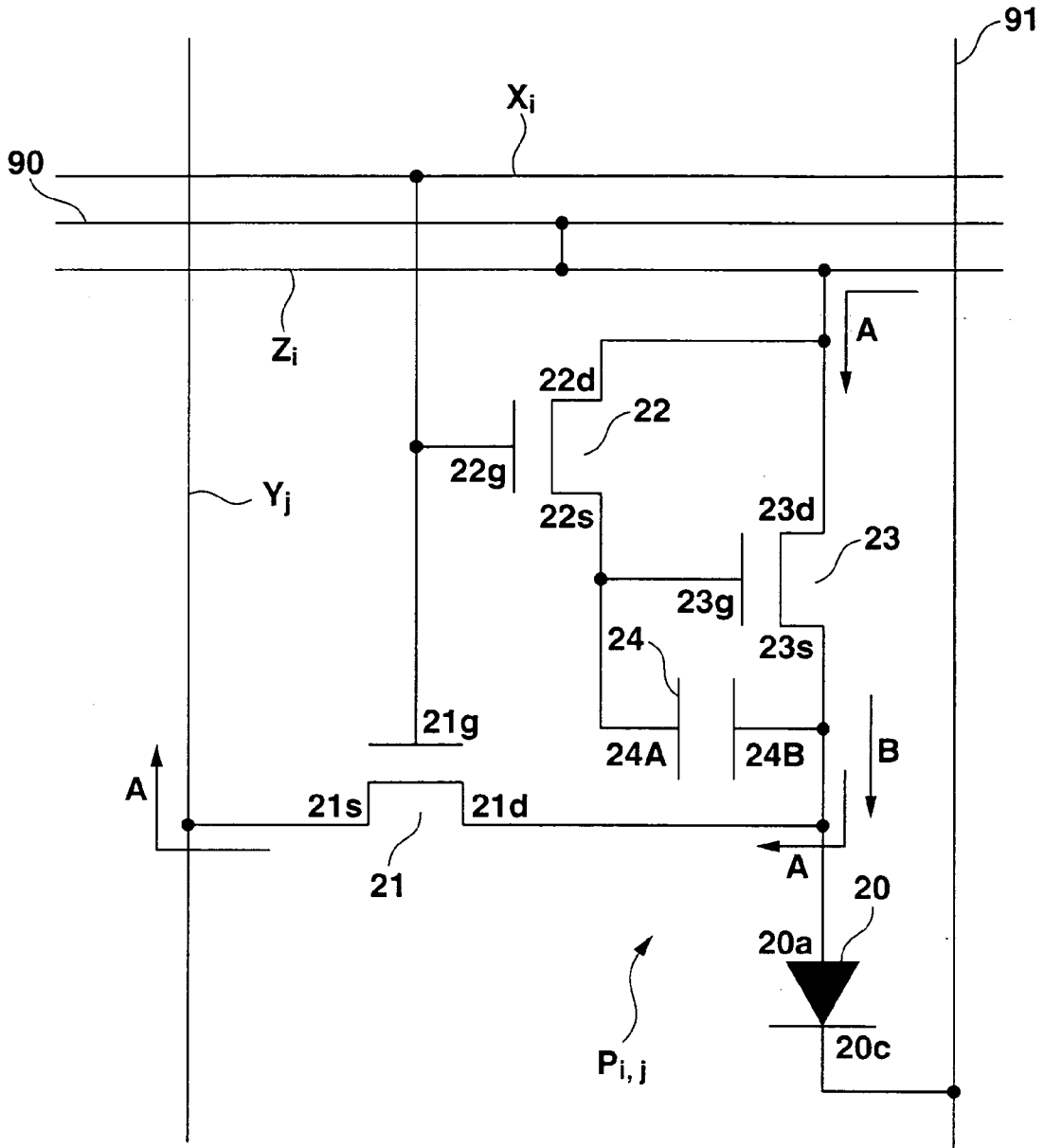


FIG.2

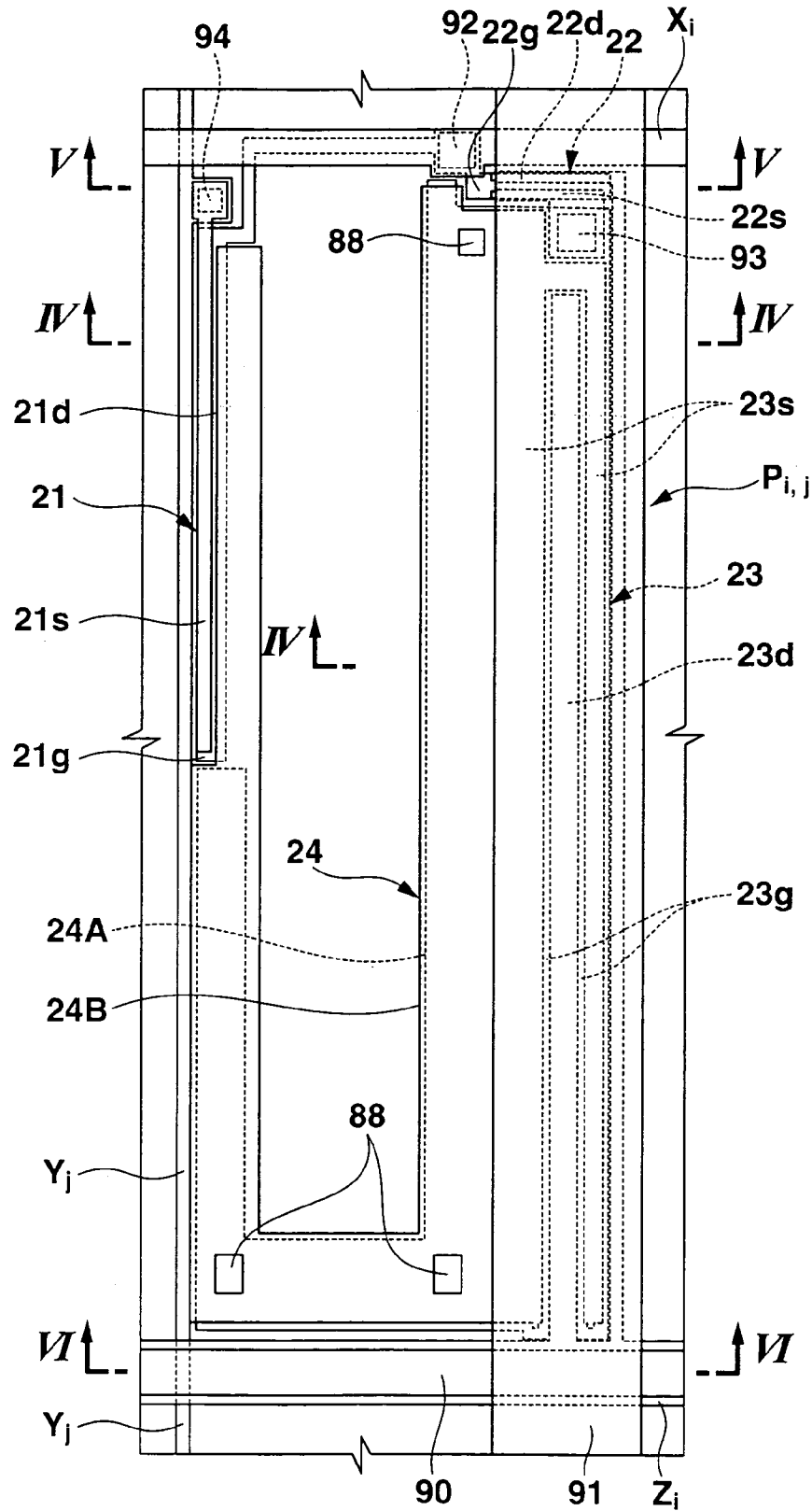


FIG. 3

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