

*Curriculum Vitae***VIPIN CHAUDHARY**

Department of Computer and Data Sciences  
Case Western Reserve University  
Cleveland, Ohio 44106

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**EDUCATION**

- 1992 Ph.D., Electrical and Computer Engineering, The University of Texas at Austin, Austin, TX  
Dissertation: On Mapping Parallel Algorithms in a Distributed Computing Environment  
Advisor: Dr. J. K. Aggarwal
- 1989 M.S., Computer Science, The University of Texas at Austin, Austin, TX. No Thesis.
- 1986 B.Tech (Honors), Computer Science and Engineering, Indian Institute of Technology,  
Kharagpur, India  
Thesis: Prolog Compiler
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**SUMMARY**

A veteran of High Performance Computing (HPC), Dr. Chaudhary has been actively participating in the science, business, government, and technology innovation frontiers of HPC for almost three decades. His contributions range from heading research laboratories and holding executive management positions, to starting new technology ventures. He is currently the Kranzusch Professor and inaugural Chair of Department of Computer and Data Sciences at Case Western Reserve University.

Previously he was a Program Director in the Office of Advance Cyberinfrastructure at National Science Foundation where he co-led the National Strategic Computing Initiative from NSF for the United States and was in the working group of the Quantum Leap Initiative, National Quantum Initiative, National Artificial Intelligence Research Institutes, Cyber, and the I-Corps Program (where he was also a Program Director). I-Corps program is now part of "The American Innovation and Competitiveness Act" that enables commercialization of research and venture startups. He co-chaired the Networking and Information Technology Research and Technology Program's Middleware and Grid Interagency Coordination (MAGIC) Team for United States. He was also in the working group of the US Interagency Modeling and Analysis Group and a member of the Advanced Computing Roundtable of the Council on Competitiveness.

He was the Empire Innovation Professor of Computer Science and Engineering, the Director of the university's Data Intensive Computing Initiative and the co-founder of the Center for Computational and Data-Enabled Science and Engineering at University at Buffalo, State University of New York.

He cofounded Scalable Informatics, a leading provider of pragmatic, high performance software-defined storage and compute solutions to a wide range of markets, from financial and scientific computing to research and big data analytics. From 2010 to 2013, Dr. Chaudhary was the Chief

Executive Officer of Computational Research Laboratories (CRL) where he grew the company globally to be an HPC cloud and solutions leader before selling it to Tata Consulting Services. Prior to this, as Senior Director of Advanced Development at Cradle Technologies, Inc., he was responsible for advanced programming tools for multi-processor chips. He was also the Chief Architect at Corio Inc., which had a successful IPO in July, 2000.

Dr. Chaudhary was awarded the prestigious President of India Gold Medal in 1986 for securing the first rank amongst graduating students at the Indian Institute of Technology (IIT). He received the B.Tech. (Hons.) degree in Computer Science and Engineering from the Indian Institute of Technology, Kharagpur, in 1986 and a Ph.D. degree from The University of Texas at Austin in 1992.

### **RESEARCH INTERESTS**

High Performance Computing and Applications to Science, Engineering, Biology, and Medicine; Big Data; Computer Assisted Diagnosis and Interventions; Medical Image Processing; Computer Architecture and Embedded Systems; Spectrum Management; Quantum Computing.

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### **EMPLOYMENT HISTORY**

08/2020 – Endowed Kranzusch Professor and Inaugural Chair, Department of Computer and Data Sciences, Case School of Engineering, Case Western Reserve University, Cleveland, Ohio.

06/2016 – 06/2020 Program Director, National Science Foundation, Directorate for Computer and Information Science and Engineering, Office of Advanced CyberInfrastructure

- Co-leading the National Strategic Computing Initiative from NSF for the United States
- Working group of the Quantum Leap Initiative and the National Quantum Initiative
- Working group of the National Artificial Intelligence Research Institutes Working Group
- Working group of the I-Corps Program (where he was also a Program Director). I-Corps program is now part of “The American Innovation and Competitiveness Act” that enables commercialization of research and venture startups.
- Co-chairing the Networking and Information Technology Research and Technology Program's Middleware and Grid Interagency Coordination (MAGIC) Team for United States.
- Working group of the US Interagency Modeling and Analysis Group

- Member of the Advanced Computing Roundtable of the Council on Competitiveness.
  - Started program with NASA - SWQU (Next Generation Software for Data Driven Models of Space Weather with Quantified Uncertainties)
  - Started multiple new programs (PPoSS, SWQU, CSSI, OAC Core, QCIS-FF, QII-TAQS, QLCI, National AI Research Institutes, Quantum Algorithm Challenge, Enabling Quantum Computing Platform Access) and managed many others (CDS&E, CDS&E-MSS, SPX, I-Corps, FMITF)
- 08/2011 – 08/2020 SUNY Empire Innovation Professor, Computer Science and Engineering, University at Buffalo, The State University of New York
- 05/2015 -- Founder, SpectrumFi, Inc.
- Licensing “Methods and systems for spectrum management” technology developed at UB.
- 02/2010 – 01/2013 CEO, Computational Research Labs (Tata CRL)
- Built the 4<sup>th</sup> largest supercomputer and the largest private supercomputer in the world
  - Provided complete High Performance Computing cloud and solutions
  - Grew the company worldwide
  - Sold to Tata Consulting Services
- 04/2009 – 05/2016 Founder, Diagnaid, Inc.
- Developed computer aided diagnosis for spine; raised small capital
- 09/2006 – 07/2011 SUNY Empire Innovation Associate Professor, Computer Science and Engineering, University at Buffalo, The State University of New York
- 09/2006 – 08/2011 Associate Professor (Adjunct), Department of Neurological Surgery, Wayne State University
- 09/2006 – 08/2007 Associate Professor (Adjunct), Department of Computer Science, Wayne State University
- 08/2003 – 03/2018 Co-Founder, Scalable Informatics, Inc
- Built the fastest storage and analytics computer systems targeting finance, pharmaceuticals, media, and scientific markets
  - Sold IP to private company
- 10/2005 – 05/2012 Founder and President, Micass L.L.C.
- Developed neurosurgery system; raised small capital

- 08/2005 – 07/2006 Associate Professor (Full Time Associate), Department of Biomedical Engineering, Wayne State University
- 09/2003 – 08/2006 Director, Institute for Scientific Computing, Wayne State University
- 09/2002 – 08/2006 Associate Professor, Institute for Manufacturing Research, Wayne State University
- 02/2001 – 01/2002 Senior Director, Cradle Technologies, Inc.
- Responsible for architecture and programmability of the largest heterogeneous many-core media processor
  - Helped raise substantial funding
- 09/2000 – 08/2003 Founder and Associate Director, Institute for Scientific Computing, Wayne State University
- 01/2000 – 01/2001 Chief Architect, Corio, Inc.
- Responsible for the data center architecture and delivery solution of the earliest cloud service
  - Successful IPO, July 2000 and later bought by IBM as their “On Demand” services.
- 09/1998 – 08/2006 Associate Professor (Full Time Associate before Nov 2002), Department of Computer Science, Wayne State University
- 08/1998 – 07/2007 Associate Professor (Adjunct since Nov 2002), Department of Electrical and Computer Engineering, Wayne State University
- 06/1992 – 07/1998 Assistant Professor, Department of Electrical and Computer Engineering, Wayne State University
- 01/1992 – 05/1992 Post-Doctoral Fellow (Jan-May), Computer and Vision Research Center, The University of Texas at Austin, Austin, TX
- 03/1987 – 12/1991 Graduate Research Assistant, Computer and Vision Research Center, The University of Texas at Austin, Austin, TX
- 08/1986 – 07/1988 Microelectronics and Computer Development (MCD) Fellow, Department of Computer Science, The University of Texas at Austin, Austin, TX

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## AWARDS

- 2019 National Science Foundation Director’s Superior Accomplishment Award
- 2018- Honorary Professor, Amity University, Noida, India
- 2008 2007 Visionary Innovator, STOR, University at Buffalo, SUNY
- 1997 Excellence in Teaching Award, College of Engineering, Wayne State University

1996	Teaching Innovation Award, College of Engineering, Wayne State University
1993	National Science Foundation Research Initiation Award
1993	Wayne State University Faculty Research Award
1986 – 1988	Microelectronics and Computer Development (MCD) Fellow in Computer Sciences, The University of Texas at Austin
1986	<i>President of India Gold Medal</i> for first rank among all graduating students
1986	Institute Silver Medal for first rank among graduating Computer Science and Engineering students
1982 – 1986	General Proficiency Scholarship for academic accomplishments
1982	National Talent Search Scholarship, NCERT, India
1981	Certificate of Merit in the All India Annual Mathematics Talent Competition,
1980	National Scholarship and Certificate of Merit for outstanding performance in the All India Secondary School Examination, CBSE, India

### TEAM AWARDS

2017	S. Liu, F. Shen, V. Chaudhary, and H. Liu, “ <i>MayoNLP at SemEval 2017 Task 10: Word Embedding Distance Pattern for Keyphrase Classification in Scientific Publications</i> ”, International Workshop on Semantic Evaluation (SemEval-2017), held with Annual Meeting of the Association for Computational Linguistics (ACL), August, 2017, Vancouver, Canada. ( <b>Top system in the challenge</b> , <a href="#">tweet</a> )
2013	“ <i>Scalable Informatics siFlash and Jackrabbit</i> ”, STAC-M3 Financial Industry Benchmarks (Best performance in 10 of 17 benchmarks. Continued to own these records for two years, only to be broken by Scalable Informatics hardware by customer.)
2005	“ <i>Cradle CT3600 MDSP</i> ”, EDN Innovation Award (Chip of the Year).
2003	“ <i>Cradle ECE3400/MPE3400</i> ”, Top 5 Microprocessor Report (MPR) Analysts’ Choice Award as Best Extreme Processor.

### OTHER HONORS

2005	IEEE Computer Society, Certificate of Appreciation for Outstanding Service
1988	Who's Who among International students in American Universities.

### PROFESSIONAL MEMBERSHIPS AND ACTIVITIES

#### *Professional Memberships*

- Senior Member, Institute of Electrical and Electronics Engineers (IEEE), IEEE Computer Society, IEEE Engineering in Medicine and Biology
- Member, American Association for the Advancement of Science (AAAS)
- Member, SPIE
- Member, American Medical Informatics Association (AMIA)
- Member, Association for Computing Machinery (ACM) – SIGPLAN, SIGARCH

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