Search

Carlos Aldana
Intel

Message More...

Carlos Aldana · 3rd

Intel

San Francisco, California · 500+ connections · Contact info

Intel Corporation

Stanford University

About

Experienced systems engineer/technologist, with ~20 years of wireless and wired experience.

Experience

Sr. Systems Engineer
Intel Corporation

Jun 2012 – Present · 7 yrs 9 mos
santa clara, CA
Worked on wireless mmwave prototyping for backhaul use cases which is now being commercialized by Facebook, LED communication prototyping, and am now working on 3GPP 5G NR-U along with positioning technology. Worked on Matlab system level simulations that showed NR-U coexists with Wi-Fi. Submitted over 50 patents.

Promoted

Are
App
busi
cust

LL.M
Texa
Com
Netv

Inco
Are
Life

People Also V

Irene
Senio
Desig

Byro
Lead

Joon
Head

Dale
Hardw
Analy

Jung
Head
focus
San J

Carlos Aldana
Intel

synchronization measurements using Fine Timing Measurement (FTM) frames. Changed OFDM timing algorithm in all Wi-Fi chipsets to have optimal symbol timing. Worked with FPGA ...see more

Chief Technology Officer
Upheels
Oct 2011 – Apr 2012 · 7 mos
San Francisco Bay Area

Lead a team of more than 10 engineers and built an e-commerce social network focused on high-end women's fashion.

Principal Scientist
Broadcom Corporation
Jan 2004 – Sep 2011 · 7 yrs 9 mos
Sunnyvale, CA

Key system model contributor for 4321/4322/43236/5357/6362 chipsets.
Aspects worked on: Channel modeling, interleaver design, MIMO channel estimation / tracking, optimal FFT window placement, beamforming matrix compression/decompression design, antenna selection and beamforming compression contributions to standard, radar detection design leading to first MIMO AP chipset to be DFS compliant, nonlinear amplifier modeling and nonlinear compensation for internal power amplifier design.

• Contributing member to EWC spec (from which current IEEE 802.11n standard is based)
• Generated various test vectors as well as RTL matching.
• Actively participated in the bringup efforts for various chipsets : 5357/43236/6362.

see less

Systems Engineer
Solarflare Communications
2002 – 2004 · 2 yrs
Irvine, CA

• Worked on fixed point simulations and various signal processing algorithms including self-echo/next and alien next reduction. Actively contributed to 10GBase-T study group in IEEE.

Add new

See more cours

Promoted

Stanford University
Master's Degree, Electrical Engineering
1996 – 1998

California Institute of Technology
BS, Electrical Engineering
1992 – 1996

Volunteer Experience

Volunteeer interviewer
Incentive Awards Program
Feb 2014 • 1 mo
Education

Read and interview high school students applications towards IAP program. IAP provides up to $32,000 in scholarship funds over eight (8) semesters, and student support services to low-income, first-generation college students who have demonstrated leadership potential, high academic achievement, and a commitment to serve others.

Member Of The Supervisory Board
Intel latino network
Oct 2016 – Present • 3 yrs 5 mos

Skills & Endorsements

Digital Signal Processors · 37

Carlos Aldana
Intel

ASIC · 18

Stéphane Beauregard and 17 connections have given endorsements for this skill

Show more

Recommendations

Received (1) Given (2)

Marc Bertola
Director of Software Engineering
February 12, 2015, Carlos was senior to Marc but didn't manage directly

Carlos is the cornerstone of the indoor Wi-Fi positioning program at Qualcomm Atheros. Apart from his obvious role as an important author of the Fine Timing Measurement protocol in the 802.11mc draft, Carlos has provided all teams involved in its implementation with an important amount of technical information and... See more

Accomplishments

2 Languages
English • Spanish

2 Patents

SYSTEMS AND METHODS OF OFFLOADED POSITIONING FOR DETERMINING LOCATION OF WLAN NODES
Filed May 8, 2014 • us 20140335891A1

Other inventors

Carlos Aldana
Intel

Other inventors

1 Organization
IEEE TGaz

Interests

Wireless Communications & Mobil...
19,922 members

IEEE SA Official IEEE Standards Ass...
2,429 members

Energous Corporation
2,346 followers

A STARTUP SPECIALISTS GROUP O...
298,868 members

802.11 Wireless professionals
12,610 members

5G Radio Access Technology (RAT) ...
2,632 members

See all

DOCKET ALARM

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts

Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research

With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips

Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS
Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS
Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS
Sync your system to PACER to automate legal marketing.