

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ADVANCED MICRO DEVICES, INC.
Petitioner

v.

AQUILA INNOVATIONS, INC.
Patent Owner

Case IPR2019-01526
U.S Patent No. 6,895,519 B2

DECLARATION OF DAVID H. ALBONESI

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4.	“a clock generation circuit that receives a plurality of standard clocks, wherein said clock generation circuit generates a clock supplied to said central processing unit according to control by said system control circuit; and” [1.3]	60
5.	“a second memory that stores an application program, wherein calling of said clock control library and changing of said register value are programmably controlled by said application program to enable user selectable clock frequency transitions,” [1.4]	62
6.	“wherein said special modes comprise a first special mode in which clock supply to principal constituents of said central processing unit is halted, a second special mode in which clock supply to an entirety of said central processing unit is halted, and a third special mode in which supply of power to the entirety of said central processing unit is halted.” [1.5]	66
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