

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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ADVANCED MICRO DEVICES, INC.,  
Petitioner

v.

AQUILA INNOVATIONS INC.,  
Patent Owner

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Case IPR2019-01526  
Patent 6,895,519

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**PETITIONER ADVANCED MICRO DEVICES, INC.'S REPLY TO  
PATENT OWNER'S RESPONSE**

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U.S. Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**PETITIONER’S UPDATED EXHIBIT LIST**

<b>Exhibit No.</b>	<b>Description</b>
<b>1001</b>	U.S. Patent No. 6,895,519 B2 to Endo (“the ’519 patent”)
<b>1002</b>	File history of U.S. Patent No. 6,895,519 B2 to Endo (“’519 file history”)
<b>1003</b>	Declaration of Dr. David Albonesi
<b>1004</b>	U.S. Patent No. 6,665,802 to Ober (“Ober”)
<b>1005</b>	“Draft ACPI Driver Interface Design Notes and Reference,” Microsoft Hardware White Paper, Microsoft Corporation, 1998 (“Windows APCI”)
<b>1006</b>	U.S. Patent No. 6,516,422 to Doblar <i>et al.</i> (“Doblar”)
<b>1007</b>	U.S. Patent No. 6,823,516 to Cooper (“Cooper”)
<b>1008</b>	U.S. Patent No. 6,681,336 to Nakazato <i>et al.</i> (“Nakazato”)
<b>1009</b>	<i>Curriculum vitae</i> of Dr. David Albonesi
<b>1010</b>	McDaniel, G., <i>IBM Dictionary of Computing</i> , McGraw-Hill, 10th ed., (1993) (“IBM Dictionary”)
<b>1011</b>	“Aquila Innovations, Inc.’s Claim Construction Brief,” Aquila Innovations, Inc. v. Advanced Micro Devices, Inc., Case No. 1:18-cv-00554-LY (W.D. Tex.) (filed July 2, 2019). (“Claim Construction Brief”)
<b>1012</b>	“Aquila Innovations, Inc.’s Preliminary Infringement Contentions,” <i>Aquila Innovations, Inc. v. Advanced Micro Devices, Inc.</i> , Case No. 1:18-cv-00554-LY (W.D. Tex.) (filed Feb. 3, 2019). (“Preliminary Infringement Contentions”)
<b>1013</b>	Compaq Computer Corporation <i>et al.</i> , “Advanced Configuration and Power Interface Specification, Revision 2.0” (July 27,2000) (“ACPIspec”)
<b>1014</b>	U.S. Patent No. 5,952,890 to Fallisgaard <i>et al.</i> (Fallisgaard)
<b>1015</b>	Intel Pentium III Processor/840 Developer Kit Manual (April 2001) (“Intel Pentium III Manual”)
<b>1016</b>	TMS320C55x DSP Functional Overview (June 2000) (“TMS Overview”)
<b>1017</b>	ST7 8-Bit MCU Family User Guide (July 2002)(“ST7 User Guide)

<b>Exhibit No.</b>	<b>Description</b>
<b>1018</b>	U.S. Patent No. 7,155,617 B2 to Gary <i>et al.</i> (“the ’617 patent”)
<b>1019</b>	Microsoft Computer Dictionary, Microsoft Press, 5th ed., (2002)(“Microsoft Dictionary”)
<b>1020</b>	MICROSOFT.COM, OnNow and Power Management (“OnNow”)
<b>1021</b>	Affidavit of Christopher Butler
<b>1022</b>	Olukotun et al., <i>The Case for a Single-Chip Multiprocessor</i> (1996)
<b>1023</b>	Albonesi et al., <i>Tradeoffs in the Design of Single Chip Multiprocessors</i> (1994)
<b>1024</b>	Bossen et al, <i>Power4 Systems: Design for Reliability</i> (2001)
<b>1025</b>	U.S. Patent No. 5,260,979 to Parker et al. (“the ’979 patent”)
<b>1026</b>	U.S. Patent No. 5,530,726 to Toshiaki Ohno (“the ’726 patent”)
<b>1027</b>	Trevor Mudge, “ <i>Power: A First-Class Architectural Design Constraint</i> ,” IEEE Computer, April 2001
<b>1028</b>	Declaration of Dr. David Albonesi in Support of Petitioner’s Reply to Patent Owner’s Response
<b>1029</b>	Deposition Transcript of Dr. Steven A. Przybylski, August 14, 2020.
<b>1030</b>	Internet Archive capture of <a href="http://www.microsoft.com">http://www.microsoft.com</a> , June 10, 2001 (accessed August 27, 2020).
<b>1031</b>	Internet Archive capture of <a href="http://www.microsoft.com/windows/default.asp">http://www.microsoft.com/windows/default.asp</a> , June 9, 2001 (accessed August 27, 2020).
<b>1032</b>	Internet Archive capture of <a href="http://www.microsoft.com/HWDev/">http://www.microsoft.com/HWDev/</a> , June 11, 2001 (accessed August 27, 2020).

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